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SPECIFICATION

CUSTOMER　:

MODULE NO.:　　H12864A-WMI-VN

APPROVED BY: (FOR CUSTOMER USE ONLY)	
	PCB VERSION:　　　　DATA:

SALES BY	APPROVED BY	CHECKED BY	PREPARED BY

VERSION	DATE	REVISED PAGE NO.	SUMMARY

Contents

1.Module Classification Information

H   12864   A – W   MI   – V   N   **

Salesman apply changing Code:	

Controller IC model

V:Negative Voltage
T:TemperatureCompensation
N:Without Negative Voltage
JP,JT:English/Japanese
EP,ET,EC,ES:English/Europe

YH:STN Positive Y-G Transflective 6H
MI: STN Negative Blue Transmissive 6H
FH:FSTN Positive Transflective 6H
TI:FSTN Negative Black Transmissive

Backlight:
Y:LED Yellow Green
W:LED White
B:LED Blue
R:LED Red

Serials number:001～999

Model Dots OR Inch
0802：8 Characters By 2 Lines
12864:128*64 Dots
057001: 5.7 Inch for First modle

HZY Dispaly

2.Precautions in Use of LCM

(1)Avoid applying excessive shocks to the module or making any alterations or modifications to it.

(2)Don't make extra holes on the printed circuit board, modify its shape or change the components of LCD Module.

(3)Don't disassemble the LCM.

(4)Don't operate it above the absolute maximum rating.

(5)Don't drop, bend or twist LCM.

(6)Soldering: only to the I/O terminals.

(7)Storage: please storage in anti-static electricity container and clean environment.

(8)HZY have the right to change the passive components

(9)HZY have the right to change the PCB Rev.

3.General Specification

Item	Dimension	Unit
Number of Characters	128 x 64 dots	—
Module dimension	93 x 70.0 x 13.6(MAX)	mm
View area	72.0 x 40.0	mm
Active area	66.52 x 33.24	mm
Dot size	0.48 x 0.48	mm
Dot pitch	0.52 x 0.52	mm
LCD type	STN, Negative, Transmissive ,Blue (In LCD production, It will occur slightly color difference. We can only guarantee the same color in the same batch.)	
Duty	1/64	
View direction	6 o'clock	
Backlight Type	LED White	
Controller	NT7107 & NT7108	

4.Absolute Maximum Ratings

Item	Symbol	Min	Typ	Max	Unit
Operating Temperature	T_{OP}	-20	—	+70	℃
Storage Temperature	T_{ST}	-30	—	+80	℃
Input Voltage	V_I	0	—	V_{DD}	V
Supply Voltage For Logic	V_{DD}	0	—	6.7	V
Supply Voltage For LCD	$V_{DD}-V_{SS}$	0	—	16.7	V
Supply Voltage For LCD	$V_{DD}-V_{OUT}$	—	—	-10	V

5.Electrical Characteristics

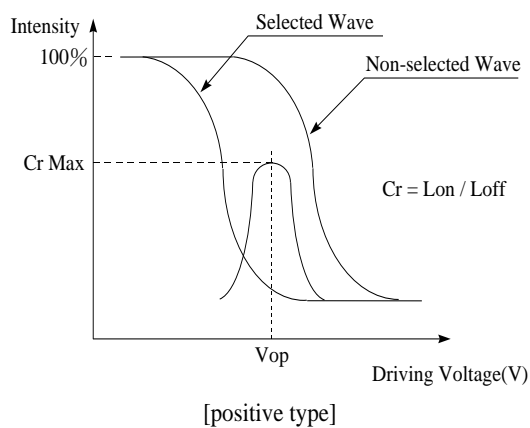| Item	Symbol	Condition	Min	Typ	Max	Unit
Supply Voltage For Logic	$V_{DD}-V_{SS}$	—	4.5	5.0	5.5	V
Supply Voltage For LCD *Note	$V_{DD}-V_0$	Ta=-20℃	—	—	---	V
		Ta=25℃	--	9.0	--	V
		Ta=+70℃	--	—	—	V
Input High Volt.	V_{IH}	—	2.0	—	V_{DD}	V
Input Low Volt.	V_{IL}	—	0	—	0.8	V
Output High Volt.	V_{OH}	—	2.4	—	V_{DD}	V
Output Low Volt.	V_{OL}	—	—	—	0.4	V
Supply Current (Not included BL)	I_{DD}	—	—	18	—	mA

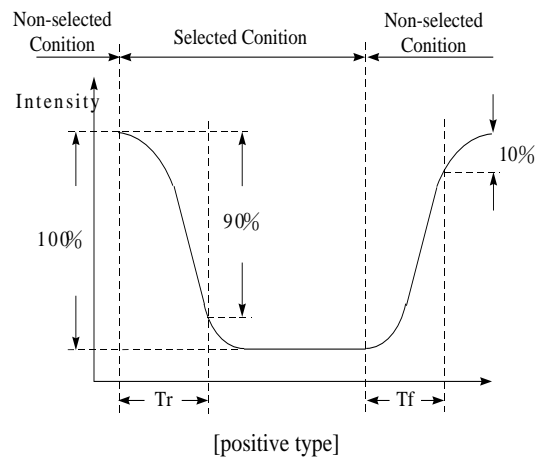* Note: Please design the VOP adjustment circuit on customer's main board
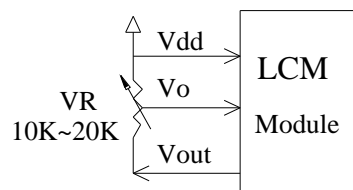
6.Optical Characteristics

Item	Symbol	Condition	Min	Typ	Max	Unit
View Angle	(V) θ	CR$\geqq$2	20	—	40	deg
	(H) φ	CR$\geqq$2	-30	—	30	deg
Contrast Ratio	CR	—	—	3	—	—
Response Time	T rise	—	—	200	300	ms
	T fall	—	—	200	300	ms

Definition of Operation Voltage (Vop)   **Definition of Response Time (Tr , Tf)**

[positive type]   [positive type]

Conditions :

Operating Voltage : Vop   Viewing Angle(θ ， φ) : 0°， 0°

Frame Frequency : 64 HZ   Driving Waveform : 1/N duty , 1/a bias

## Definition of viewing angle(CR$\geqq$2)
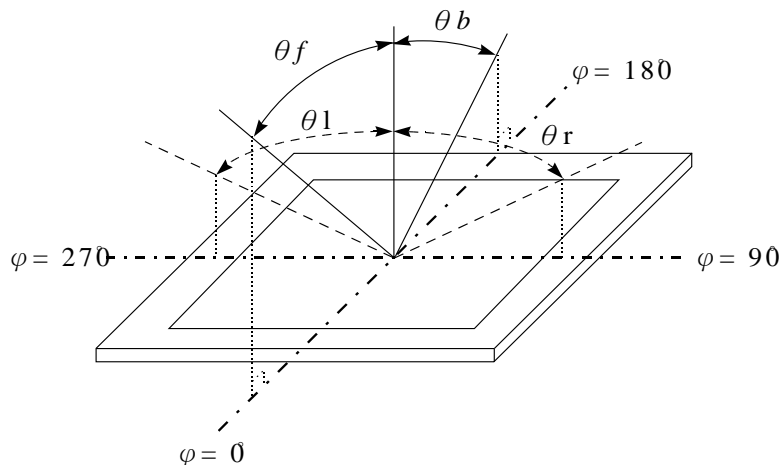
7.Backlight Information

Specification

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT	TEST CONDITION
Supply Current	ILED	57.6	64	100	mA	V=5.0V
Supply Voltage	V	--	5.0	--	V	－
Reverse Voltage	VR	－	－	8	V	－
Luminous Intensity	IV	320	---	－	cd/m^2	ILED=64mA
LED Life Time (For Reference only)	－	－	50K	－	Hr.	ILED≦64mA 25℃,50-60%RH, (Note 1)
Color	White					

Note: The LED of B/L is drive by current only, drive voltage is for reference only.

       drive voltage can make driving current under safety area (current between

       minimum and maximum).

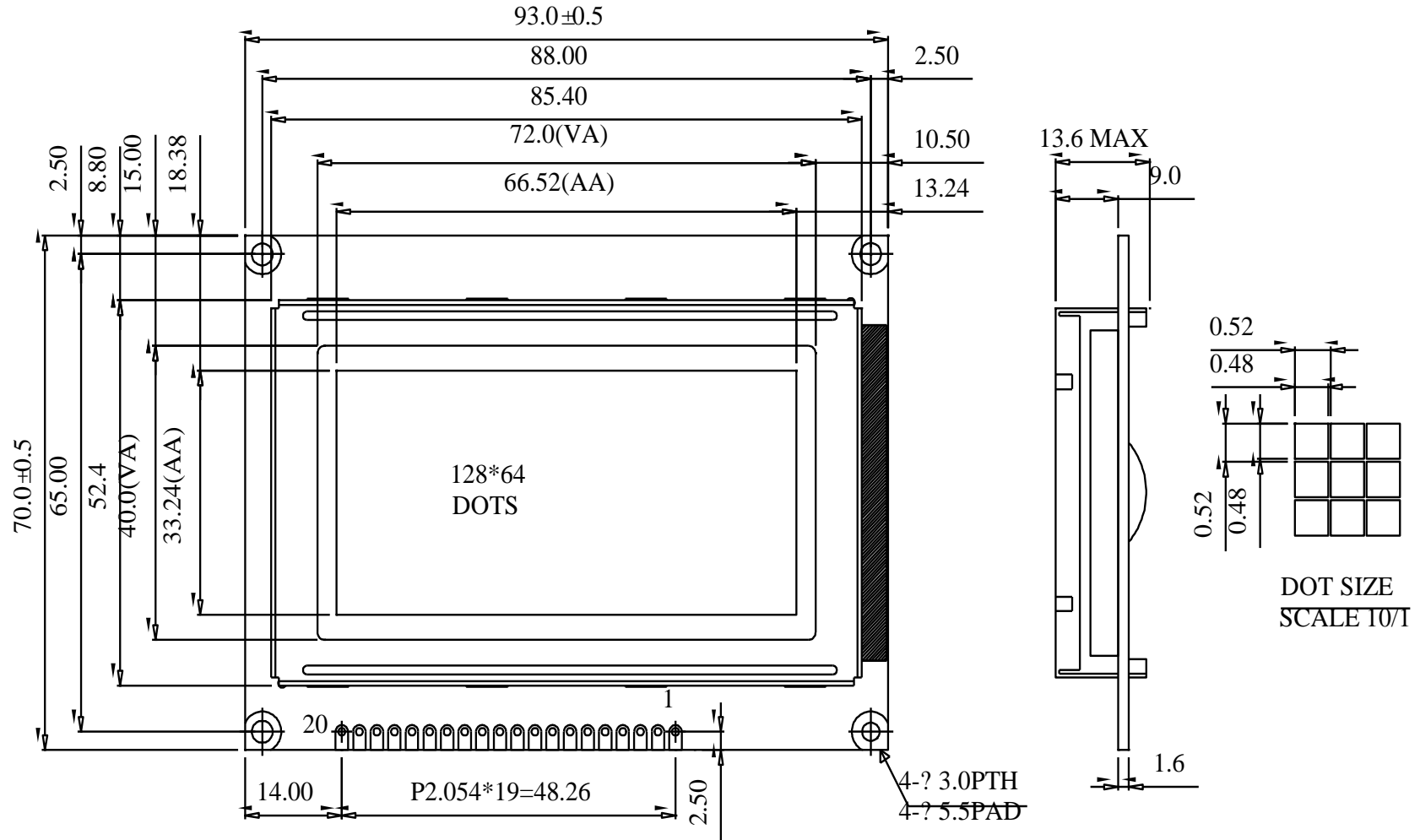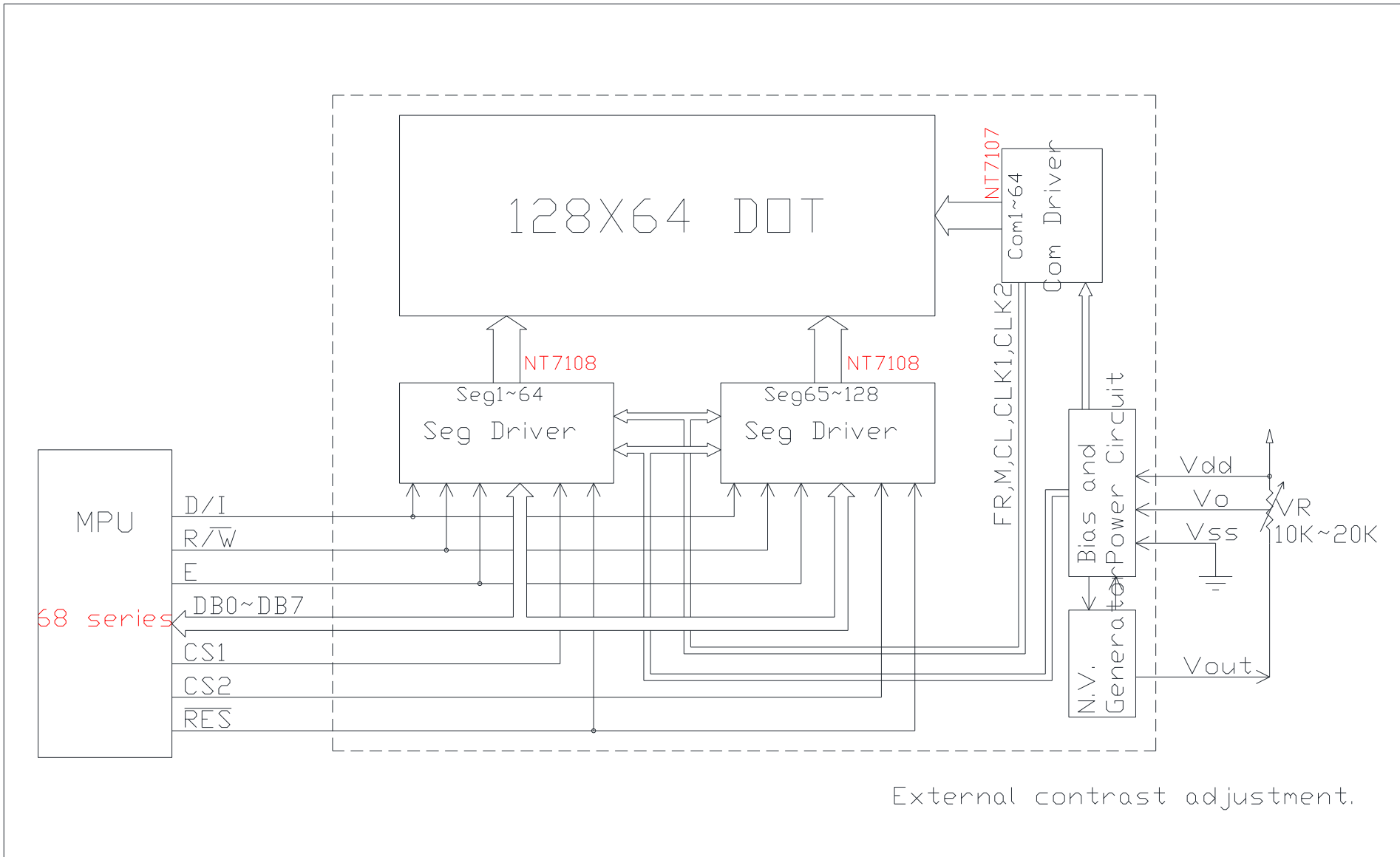**Note1 :50K hours is only a estimate for reference.**

.Drive from pin19,pin20
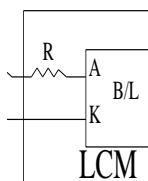
8.Interface Description

Pin No.	Symbol	Level	Description
1	V_{SS}	0V	Ground
2	V_{DD}	+5V	Supply voltage for logic
3	V_O	(Variable)	Operating voltage for LCD
4	D/I	H/L	H: Data , L : Instruction
5	R/W	H/L	H: Read (MPU←Module) , L: Write (MPU→Module)
6	E	H	Enable signal
7	DB0	H/L	Data bus line
8	DB1	H/L	Data bus line
9	DB2	H/L	Data bus line
10	DB3	H/L	Data bus line
11	DB4	H/L	Data bus line
12	DB5	H/L	Data bus line
13	DB6	H/L	Data bus line
14	DB7	H/L	Data bus line
15	CS1	H	Select Column 1~ Column 64
16	CS2	H	Select Column 65~ Column 128
17	RES	L	Reset signal
18	Vout	—	Negative voltage output
19	A	+5V	Power Supply for LED backlight (+)
20	K	—	Power Supply for LED backlight (-)

9.Contour Drawing &Block Diagram

128*64
DOTS

93.0±0.5
88.00
85.40
72.0(VA)
66.52(AA)
2.50
10.50
13.24
13.6 MAX
9.0
2.50
8.80
15.00
18.38
70.0±0.5
65.00
52.4
40.0(VA)
33.24(AA)
20
1
14.00
P2.054*19=48.26
2.50
4-? 3.0PTH
4-? 5.5PAD
1.6
0.52
0.48
0.52
0.48
DOT SIZE
SCALE 10/1

128X64 DOT

NT7107

Com1~64
Com Driver

NT7108

Seg1~64
Seg Driver

Seg65~128
Seg Driver

FR,M,CL,CLK1,CLK2

Bias and
Power Circuit

Vdd

Vo

Vss

VR
10K~20K

N.V.
Generator

Vout

MPU

D/I

R/W

E

DB0~DB7

68 series

CS1

CS2

RES

External contrast adjustment.

10.Timing Characteristics

MPU Interface   (T=25℃, VDD=+5.0V ±0.5)

Characteristic	Symbol	Min	Typ	Max	Unit
E cycle	tcyc	1000	—	—	ns
E high level width	twhE	450	—	—	ns
E low level width	twlE	450	—	—	ns
E rise time	tr	—	—	25	ns
E tall time	tf	—	—	25	ns
Address set-up time	tas	140	—	—	ns
Address hold time	tah	10	—	—	ns
Data set-up time	tdsw	140	—	—	ns
Data delay time	tddr	—	—	320	ns
Data hold time (write)	tdhw	10	—	—	ns
Data hold time (read)	tdhr	20	—	—	ns
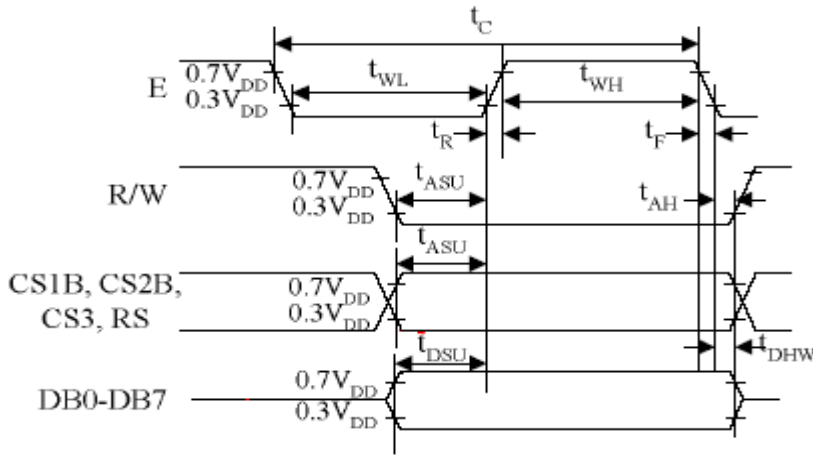
MPU Write Timing
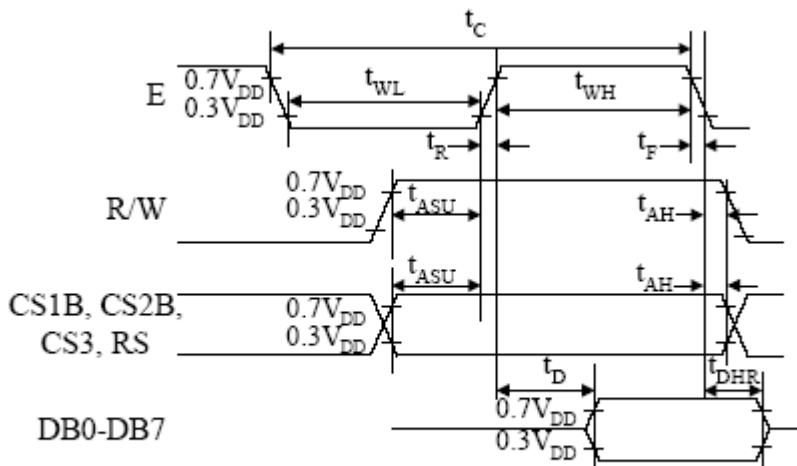
MPU Read Timing

11.Display Control Instruction

The display control instructions control the internal state of the AIP31107. Instruction is received from MPU to AIP31108 for the display control. The following table shows various instructions.

Instruction	RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	Function
Display on/off	L	L	L	L	H	H	H	H	H	L/H	Controls the display on or off. Internal status and display RAM data is not affected. L:OFF, H:ON
Set address (Y address)	L	L	L	H	Y address (0-63)						Sets the Y address in the Y address counter.
Set page (X address)	L	L	H	L	H	H	H	Page (0-7)			Sets the X address at the X address register.
Display Start line (Z address)	L	L	H	H	Display start line (0-63)						Indicates the display data RAM displayed at the top of the screen.
Status read	L	H	Busy	L	On/ Off	Reset	L	L	L	L	Read status. BUSY L: Ready H: In operation ON/OFF L: Display ON H: Display OFF RESET L: Normal H: Reset
Write display data	H	L	Write data								Writes data (DB0: 7) into display data RAM. After writing instruction, Y address is increased by 1 automatically.
Read display data	H	H	Read data								Reads data (DB0: 7) from display data RAM to the data bus.

12.Detailed Explanation

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
0	0	0	0	1	1	1	1	1	D

The display data appears when D is 1 and disappears when D is 0. Though the data is not on the

screen with D=0, it remains in the display data RAM. Therefore, you can make it appear by changing D=0 into D=1.

SET ADDRESS (Y ADDRESS)

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
0	0	0	1	AC5	AC4	AC3	AC2	AC1	AC0

Y address (AC0-AC5) of the display data RAM is set in the Y address counter. An address is set by instruction and increased by 1 automatically by read or write operations of display data.

SET PAGE (X ADDRESS)

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
0	0	1	0	1	1	1	AC2	AC1	AC0

X address (AC0-AC2) of the display data RAM is set in the X address register. Writing or reading to or from MPU is executed in this specified page until the next page is set.

DISPLAY START LINE (Z ADDRESS)

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
0	0	1	1	AC5	AC4	AC3	AC2	AC1	AC0

Z address (AC0-AC5) of the display data RAM is set in the display start line register and displayed at the top of the screen. When the display duty cycle is 1/64 or others (1/32-1/64), the data of total line number of LCD screen, from the line specified by display start line instruction, is displayed.

STATUS READ

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
0	1	BUSY	0	ON/OFF	RESET	0	0	0	0

・BUSY

When BUSY is 1, the Chip is executing internal operation and no instructions are accepted.

When BUSY is 0, the Chip is ready to accept any instructions.

・ON/OFF

When ON/OFF is 1, the display is OFF.

When ON/OFF is 0, the display is ON.

・RESET

When RESET is 1, the system is being initialized.

In this condition, no instructions except status read can be accepted.

When RESET is 0, initializing has finished and the system is in usual operation condition.

WRITE DISPLAY DATA

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
1	0	D7	D6	D5	D4	D3	D2	D1	D0

Writes data (D0-D7) into the display data RAM. After writing instruction, Y address is increased by 1automatically.

READ DISPLAY DATA

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
1	1	D7	D6	D5	D4	D3	D2	D1	D0

Reads data (D0-D7) from the display data RAM. After reading instruction, Y address is increased by 1 automatically.

13.Reliability

Content of Reliability Test (wide temperature, -20℃~70℃)
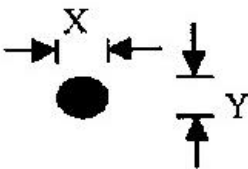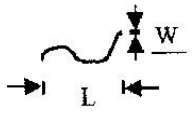
Environmental Test			
Test Item	**Content of Test**	**Test Condition**	**Note**
High Temperature storage	Endurance test applying the high storage temperature for a long time.	80℃ 200hrs	2
Low Temperature storage	Endurance test applying the high storage temperature for a long time.	-30℃ 200hrs	1,2
High Temperature Operation	Endurance test applying the electric stress (Voltage & Current) and the thermal stress to the element for a long time.	70℃ 200hrs	____
Low Temperature Operation	Endurance test applying the electric stress under low temperature for a long time.	-20℃ 200hrs	1
High Temperature/ Humidity Operation	The module should be allowed to stand at 60℃,90%RH max For 96hrs under no-load condition excluding the polarizer, Then taking it out and drying it at normal temperature.	60℃,90%RH 96hrs	1,2

Thermal shock resistance	The sample should be allowed stand the following 10 cycles of operation
	    -20℃ 25℃ 70℃
	
	    30min 5min 30min
	    1 cycle
		-20℃/70℃
		10 cycles
			____

Vibration test	Endurance test applying the vibration during transportation and using.	Total fixed amplitude : 1.5mm Vibration Frequency : 10~55Hz One cycle 60 seconds to 3 directions of X,Y,Z for Each 15 minutes	3
Static electricity test	Endurance test applying the electric stress to the terminal.	VS=800V,RS=1.5kΩ CS=100pF 1 time	____
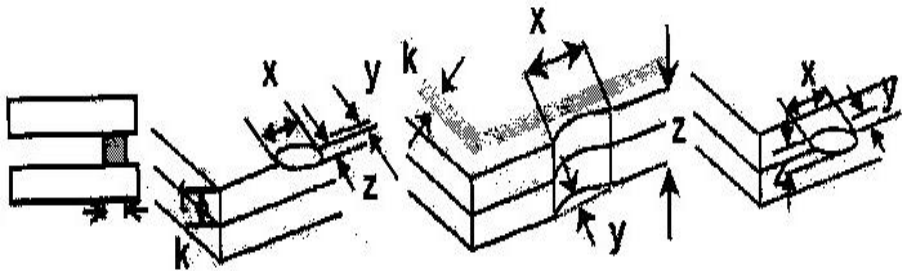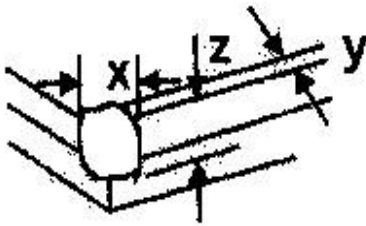
Note1: No dew condensation to be observed.
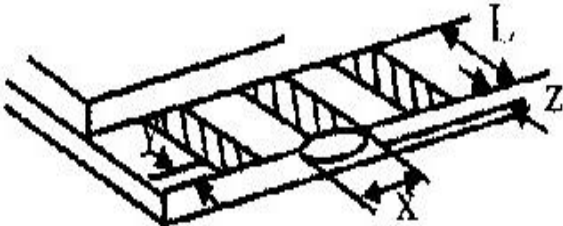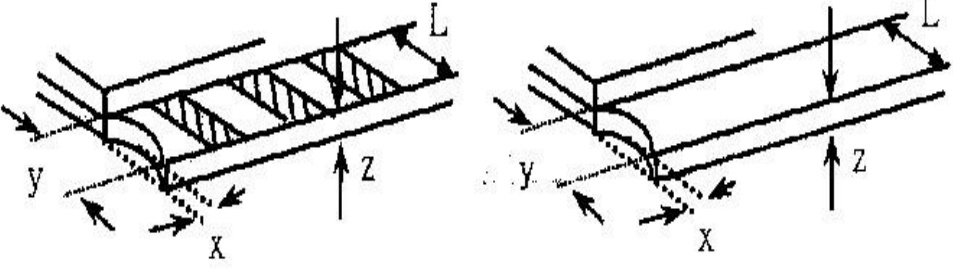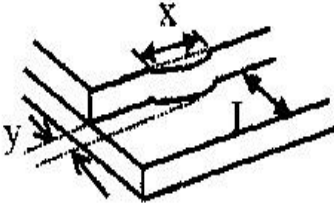
Note2: The function test shall be conducted after 4 hours storage at the normal

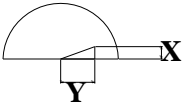   **Temperature and humidity after remove from the test chamber.**

Note3: Vibration test will be conducted to the product itself without putting it in a container.

14. Inspection specification

NO	Item	Criterion	AQL
01	Electrical Testing	1.1 Missing vertical, horizontal segment, segment contrast defect. 1.2 Missing character , dot or icon. 1.3 Display malfunction. 1.4 No function or no display. 1.5 Current consumption exceeds product specifications. 1.6 LCD viewing angle defect. 1.7 Mixed product types. 1.8 Contrast defect.	0.65
02	Black or white spots on LCD (display only)	2.1 White and black spots on display $\leqq 0.25$mm, no more than three white or black spots present. 2.2 Densely spaced: No more than two spots or lines within 3mm	2.5
03	LCD black spots, white spots, contamination (non-display)	3.1 Round type : As following drawing $\Phi =(x + y) / 2$ <table><tr><td>SIZE</td><td>Acceptable Q TY</td></tr><tr><td>$\Phi \leqq 0.10$</td><td>Accept no dense</td></tr><tr><td>$0.10 < \Phi \leqq 0.20$</td><td>2</td></tr><tr><td>$0.20 < \Phi \leqq 0.25$</td><td>1</td></tr><tr><td>$0.25 < \Phi$</td><td>0</td></tr></table>	2.5

3.1 Round type : As following drawing

$$\Phi =(x + y) / 2$$

SIZE	Acceptable Q TY
$\Phi \leqq 0.10$	Accept no dense
$0.10 < \Phi \leqq 0.20$	2
$0.20 < \Phi \leqq 0.25$	1
$0.25 < \Phi$	0

3.2 Line type : (As following drawing)

Length	Width	Acceptable Q TY
---	$W \leqq 0.02$	Accept no dense
$L \leqq 3.0$	$0.02 < W \leqq 0.03$	2
$L \leqq 2.5$	$0.03 < W \leqq 0.05$	
---	$0.05 < W$	As round type

04 Polarizer bubbles

If bubbles are visible, judge using black spot specifications, not easy to find, must check in specify direction.

Size Φ	Acceptable Q TY
$\Phi \leqq 0.20$	Accept no dense
$0.20 < \Phi \leqq 0.50$	3
$0.50 < \Phi \leqq 1.00$	2
$1.00 < \Phi$	0
Total Q TY	3

AQL 2.5

NO	Item	Criterion	AQL
05	Scratches	Follow NO.3 LCD black spots, white spots, contamination	
06	Chipped glass	Symbols Define: x: Chip length y: Chip width z: Chip thickness k: Seal width t: Glass thickness a: LCD side length L: Electrode pad length: 6.1 General glass chip : 6.1.1 Chip on panel surface and crack between panels:	2.5

6.1 General glass chip :
6.1.1 Chip on panel surface and crack between panels:

z: Chip thickness	y: Chip width	x: Chip length
$Z \leqq 1/2t$	Not over viewing area	$x \leqq 1/8a$
$1/2t < z \leqq 2t$	Not exceed $1/3k$	$x \leqq 1/8a$

⊙If there are 2 or more chips, x is total length of each chip.

6.1.2 Corner crack:

z: Chip thickness	y: Chip width	x: Chip length
$Z \leqq 1/2t$	Not over viewing area	$x \leqq 1/8a$
$1/2t < z \leqq 2t$	Not exceed $1/3k$	$x \leqq 1/8a$

⊙If there are 2 or more chips, x is the total length of each chip.

NO	Item	Criterion	AQL
06	Glass crack	Symbols : x: Chip length　　　y: Chip width　　　z: Chip thickness k: Seal width　　　t: Glass thickness　　a: LCD side length L: Electrode pad length 6.2 Protrusion over terminal : 6.2.1 Chip on electrode pad : **Table:** | y: Chip width	x: Chip length	z: Chip thickness
---	---	---
$y \leqq 0.5mm$	$x \leqq 1/8a$	$0 < z \leqq t$ | 6.2.2 Non-conductive portion: | y: Chip width	x: Chip length	z: Chip thickness
---	---	---
$y \leqq L$	$x \leqq 1/8a$	$0 < z \leqq t$ | ⊙If the chipped area touches the ITO terminal, over 2/3 of the ITO must remain and be inspected according to electrode terminal specifications. ⊙If the product will be heat sealed by the customer, the alignment mark not be damaged. 6.2.3 Substrate protuberance and internal crack. | y: width	x: length
---	---
$y \leqq 1/3L$	$x \leqq a$		2.5

NO	Item	Criterion	AQL
07	Cracked glass	The LCD with extensive crack is not acceptable.	2.5
08	Backlight elements	8.1 Illumination source flickers when lit. 8.2 Spots or scratched that appear when lit must be judged. Using LCD spot, lines and contamination standards. 8.3 Backlight doesn't light or color wrong.	0.65 2.5 0.65
09	Bezel	9.1 Bezel may not have rust, be deformed or have fingerprints, stains or other contamination. 9.2 Bezel must comply with job specifications.	2.5 0.65
10	PCB、COB	10.1 COB seal may not have pinholes larger than 0.2mm or contamination. 10.2 COB seal surface may not have pinholes through to the IC. 10.3 The height of the COB should not exceed the height indicated in the assembly diagram. 10.4 There may not be more than 2mm of sealant outside the seal area on the PCB. And there should be no more than three places. 10.5 No oxidation or contamination PCB terminals. 10.6 Parts on PCB must be the same as on the production characteristic chart. There should be no wrong parts, missing parts or excess parts. 10.7 The jumper on the PCB should conform to the product characteristic chart. 10.8 If solder gets on bezel tab pads, LED pad, zebra pad or screw hold pad, make sure it is smoothed down. 10.9 The Scraping testing standard for Copper Coating of PCB **X** **Y** **X * Y<=2mm²**	2.5 2.5 0.65 2.5 2.5 0.65 0.65 2.5 2.5
11	Soldering	11.1 No un-melted solder paste may be present on the PCB. 11.2 No cold solder joints, missing solder connections, oxidation or icicle. 11.3 No residue or solder balls on PCB. 11.4 No short circuits in components on PCB.	2.5 2.5 2.5 0.65

NO	Item	Criterion	AQL
12	General appearance	12.1 No oxidation, contamination, curves or, bends on interface Pin (OLB) of TCP.	2.5
		12.2 No cracks on interface pin (OLB) of TCP.	0.65
		12.3 No contamination, solder residue or solder balls on product.	2.5
		12.4 The IC on the TCP may not be damaged, circuits.	2.5
		12.5 The uppermost edge of the protective strip on the interface pin must be present or look as if it cause the interface pin to sever.	2.5
			2.5
		12.6 The residual rosin or tin oil of soldering (component or chip component) is not burned into brown or black color.	2.5
		12.7 Sealant on top of the ITO circuit has not hardened.	0.65
		12.8 Pin type must match type in specification sheet.	0.65
		12.9 LCD pin loose or missing pins.	0.65
		12.10 Product packaging must the same as specified on packaging specification sheet.	0.65
		12.11 Product dimension and structure must conform to product specification sheet.	