

Date: 2011/04/26

Specifications for Approval

Cı	ustomer	: Mc ' Tronic		
M	odel name	: GY1206P6SGV	N7G	REV: B
D	escription	: LCM (Y1206P	6SGW7 – DD682	– B0419B)
L	CD Specification	: LCD (SDD682	<u> 76 8222 11(</u>	<u> </u>
	ISSUE	ENG	QA	APPROVAL
	Lily Li	Bose Xie	Wallace	Michael
•				
	Customer Approval	Accept Reject Comme		/ .



REVISION RECORD (MODEL NO.: GY1206P6SGW7G)

Revision	Revision Date	Page	Contents
A B	2011/03/12 2011/04/26	Page	Contents Initial Release and Issue Full Specification. Improve the display effect.





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MODEL	GY1206P6SGW7G	1/18	PRODUCT SPECIFICATIONS	REV: B



1. FEATURES

The features of LCD are as follows

* Display mode : STN Blue, Negative, Transmissive

* Color : Display dot : White

Background: Blue

* Display Format : 128Dots × 64Dots graphic

* IC : Sitronix ST7565R-G

* Interface Input Data : 8-Bit Parallel 6800 / 4-Line SPI MPU Interface

* Driving Method : 1/65 Duty, 1/9 Bias

* Viewing Direction : 12 O'clock

* Backlight : LED (White)

* LCM technological conditions: RoHS

2. MECHANICAL SPECIFICATIONS

Item	Specification	Unit
Module Size	67.15(W) X 63.075(H) X 5.3(T)	mm
Viewing Area	61.0(W) X 31.4(H)	mm
Effective Display Area	57.57(W) X 28.77(H)	mm
Number of Dots	128 X 64 Dots	-
Dot Size	0.42(W) X 0.42(H)	mm
Dot Pitch	0.45(W) X 0.45(H)	mm

3. ELECTRICAL SPECIFICATIONS

3-1. Absolute Maximum Ratings (Vss=0V)

ltem	Symbol	Sta				
item	Symbol	Min.	Min. Typ. Max. 0.3 - 5.0 0.3 - 18.0		Unit	
Supply Voltage For Logic	Vdd	0.3	-	5.0	V	
Supply Voltage For LCD Drive	Vo,Vout	0.3	-	18.0	V	
Operating Temp.	Тор	-20	-	+70	°C	
Storage Temp.	Tst	-30	-	+80	°C	
Static Electricity	Be sue that you are ground when handing LCM					

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3. ELECTRICAL SPECIFICATIONS (Continued)

3-2-1. Electrical Characteristics

Item		Symbol	Test Condition	Min.	Тур.	Max.	Unit
Supply Voltage I	Supply Voltage For Logic		-	1.8	3.3	3.3	V
Supply Voltage	For LCD	V0-Vss	-	8.0	8.3	8.6	V
	"H" Level	V _{IH}		0.8V _{DD}	-	VDD	V
Input Voltage	"L" Level	V _{IL}	-	Vss	-	0.2VDD	V
Output Voltage	"H" Level	V _{OH}	I _{OUT} = -0.5mA	0.8V _{DD}	-	Vdd	V
Output voltage	"L" Level	V _{OL}	I _{OUT} = 0.5mA	Vss	-	0.2VDD	V
Current Consu	mption	I _{DD}	$V_{IN} = V_{DD}$			3.0	mA
AVG. X of 1931 C	I.E (LCM)	Х	Dots All off	0.13	0.17	0.21	-
AVG. Y of 1931 C.	I.E (LCM)	Y	DOIS All OII	0.10	0.14	0.18	-
Luminous Intensit	v (LCM)	L	Dots All on	200	300	-	cd/m ²
Luminous Intensit	y (LCIVI)	L	Dots All off	40	90	-	cd/m ²

NOTE: 1) Duty ratio=1/65, Bias=1/9 2) Measured in Dots ON-state

3-3.BACKLIGHT

3-3-1. Absolute Maximum Ratings

ltem	Symbol	Condition	Min.	Тур.	Max	Unit
Forward Current	IF	Ta= 25°ℂ	-	-	100	mA
Reverse Voltage	VR	1a-25 (-	-	10	V
Power Dissipation	PD	Ta= 25°℃	-	-	420	mW

3-3-2. Opto-electronic Characteristics

Item	Symbol	Condition	Min.	Тур.	Max	Unit
Forward Voltage	VF	IF= 60mA	3.8	4.0	4.2	V
Reverse current	IR	VR=10V	-	-	60	uA
Luminous intensity	LV	IF=60mA	450	600	-	cd/m²
Avg.X of 1931 C.I.E	X	IF=60mA	0.25	0.28	0.31	
Avg.Y of 1931 C.I.E	Y	IF-00IIIA	0.25	0.28	0.31	-

^{*} The brightness is measured without LCD panel

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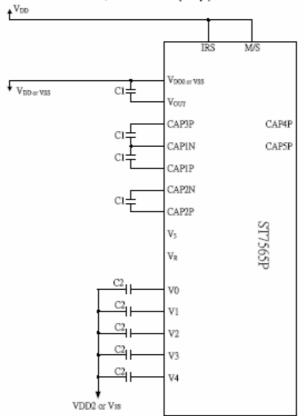


4. POWER SUPPLY and BLOCK DIAGRMA

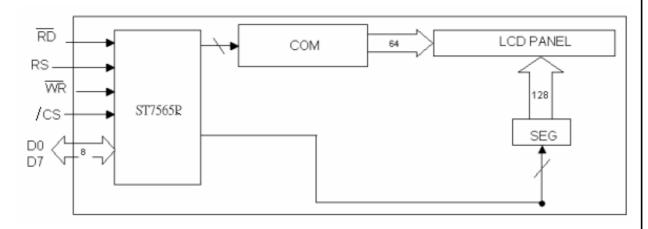
4-1 Power supply

(1) When the voltage regulator internal resistor is used.

(Example where VDD2 = VDD, with 4x step-up)



4-2 Block diagram

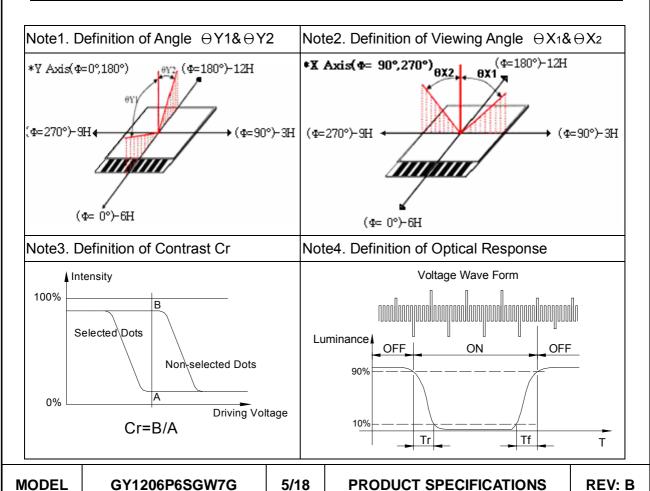


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5. ELECTRO - OPTICAL CHARACTERISTICS

Ite	m	Symbol	Temp.	Min.	Тур.	Max.	Unit	Conditions	Note	
	Ф=0°	⊕1			25					
Viewing	Ф=180°	⊖2	25 ℃		28		Dog		1.0	
Angle Cr <u>></u> 2	Ф=90°	⊖3	250		38		Deg.	-	1,2	
	Ф=270°	⊖4			40					
Viev	ving Dire	ction	12 O'clock							
Cont Rat		Cr	25℃	2.0	4.08	4.68	-	$\Phi = 0_{\circ}$	3	
Respo	onse	Tr	25 ℃	-	268	300	ms	Φ = 0° Φ = 0°	4	
Time(rise)	ır	0℃	-	1250	1650	1113		4	
Respo		Tf	25 ℃	-	188	300	ms	⊖= 0°	4	
Time	(fall)	11	0℃	-	1250	1650	1115	Φ = 0°	4	





6. INTERFACE PIN FUNCTION

Pin NO.	Symbol	1/0	Functions
1	/CS	I	This is the chip select signal.
2	/RES	I	When RES is set to "L", the setting are initialized.
3	A0	I	This is connect to the least significant bit of the Norman MPU address bus, and it determines whether the data bits are data or a command.
4	WR(WR)	I	The data bus are latched at the rising edge of the WR signal
5	RD(/RD)	I	The data bus is in output status when this signal is "L"
6~13	D0~ D7	I/O	This is an 8-bit bi-directional data bus that connects to an 8-bit or 16-bit standard MPU data bus.
14	V_{DD}	Power supply	Power supply
15	V _{SS}	Power supply	Ground
16	V _{OUT}	0	DC/DC voltage converter. Connect a capacitor between this terminal and v _{ss} or VDD
17	NC	1	No connect
18	CAP3+	0	DC/DC voltage converter. Connect a capacitor between this terminal and the CAP1N terminal.
19	CAP1-	0	DC/DC voltage converter. Connect a capacitor between this terminal and the CAP1P terminal.
20	CAP1+	0	DC/DC voltage converter. Connect a capacitor between this terminal and the CAP1N terminal.
21	CAP2+	0	DC/DC voltage converter. Connect a capacitor between this terminal and the CAP2N terminal.
22	CAP2-	0	DC/DC voltage converter. Connect a capacitor between this terminal and the CAP2P terminal.
23~27	V4~ V0	Power supply	This is a multi-level power supply for the liquid crystal drive.
28	P/S	I	This pin configures the interface to parallel mode or serial mode. P/S="H":Parallel data input/output. P/S="L":Serial data input.

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7. COMMAND LIST

Command			1	Section 1		inidi C		•				Function
	ΑĐ	/IRID	AWR	1000	100	-		D3		1000	The state of	
(1) Display ON/OFF	0	1	0	1	0	1	D	1	1	1	0 1	LCD display ON/OFF 0: OFF, 1: ON
(2) Display start line set	0	1	0	0	1	DI	spla	y sta	art a	didire	95	Sets the display RAM display sta line address
(3) Page address set	0	1	0	1	0	1	1	Pa	ge a	sddin	ess	Sets the display RAM page address
(4) Column address set upper bit	0	1	0	0	D	0			ımın	add	ress	Sets the most significant 4 bits o the display RAM column address
Column address set lower bit	0	1	0	0	0	0	0				icant ress	Sets the least significant 4 bits of the display RAM column address
(5) Status read	0	0	1		St	atus		0	0	0	0	Reads the status data
(6) Olsplay data write	1	1	D			1	Minto	e dat	3			Writes to the display RAM
(7) Display data read	7	0	1				Res	d dat	ta			Reads from the display RAM
(8) ADC select	0	1	0	1	0	1	0	0	0	0	0 1	Sets the display RAM address SEG output correspondence 0: normal, 1: reverse
(9) Olsplay normal/ reverse	0	1	O	1	0	1	0	0	1	1	0 1	Sets the LCD display normal/ reverse 0: normal, 1: reverse
(10) Display all points ON/OFF	0	1	0	1	0	1	0	0	1	0	0 1	Display all points 0: normal display 1: all points ON
(11) LCD blas set	0	1	0	1	0	1	0	0	0	1	0	Sets the LCD drive voltage blas ratio 0: 1/9 blas, 1: 1/7 blas (\$T7565F
(12) Read/modify/write	0	1	0	1	1	1	0	0	0	D	0	Column address increment At write: +1 At read: 0
(13) End	0	1	0	1	1	1	0	1	1	1	0	Clear read/modify/write
(14) Reset	0	1	0	-	1	1	8	0	0	1	0	Internal reset
(15) Common output mode select	0	1	0	1	1	0	0	1	•	-	•	Select COM output scan direction 0: normal direction 1: reverse direction
(16) Power control set	0	1	0	0	D	1	0	1		era ode	ting	Select internal power supply operating mode
(17) Vo voltage regulator internal resistor ratio set	0	1	O	0	0	1	0	0	III Series	estet etio	ar	Select Internal resistor ratio(Rb/Ra) mode
(18) Electronic volume mode set Electronic volume register set	0	1	0	1	0	_	o ctro	o nic v	_	O ne v	1 alue	Set the Vo output voltage electronic volume register
(19) Static Indicator ON/OFF Static Indicator	0	1	0	1	0	1	0	1	1	0	0 1	0: OFF, 1: ON
register set				D	0	0	0	0	0	0	Mode	Set the flashing mode
(20) Booster ratio set	0	1	0	1 0	1 0	1		1		ste	0 p-up ilue	select booster ratio 00: 2x,3x,4x 01: 5x 11: 6x
(21) Power saver												Display OFF and display all points ON compound command
(22) NOP	0	1	0	1	1	1	0	0	0	1	1	Command for non-operation
(23) Test	0	1	0	1	1	1	1				-	Command for IC test. Do not use this command

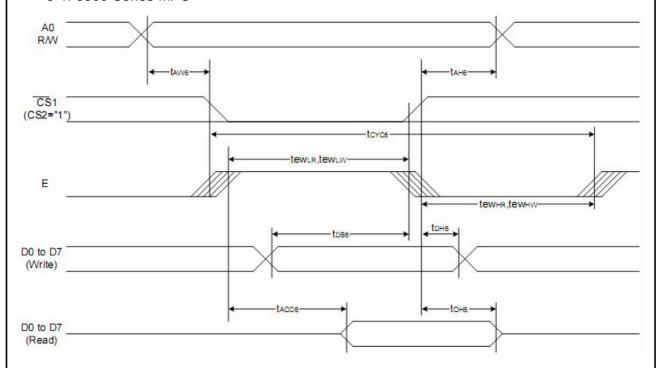
MODEL	GY1206P6SGW7G	7/18	PRODUCT SPECIFICATIONS	REV: B
MODEL	GY1206P6SGW/G	7/18	PRODUCT SPECIFICATIONS	KEV: B



Display the World

8.TIMING CHARACTERISTICS

8-1. 6800 Series MPU



(VDD = 3.3V, Ta = -30 to 85°C)

				(VDD = 3.3V	1a 30 1	(0 00 C)
Item	Signal	Symbol	Condition	Rat	ing	Units
115 TACAS	Signal	Symbol	Condition	Min.	Max.	Units
Address hold time		tan6		0	8—	
Address setup time	A0	taw6		0	-	100
System cycle time		tcyc6		240	_	
Enable L pulse width (WRITE)	WR	teww		80	<u> </u>	8
Enable H pulse width (WRITE)	VVK	tewnw		80	: -	
Enable L pulse width (READ)	RD	tewlr		80	-	ns
Enable H pulse width (READ)	- KU	tewnr		140		
WRITE Data setup time		tos6		40	185	
WRITE Address hold time	D0 to D7	ton6		0	_	
READ access time	DU 10 D7	taccs	CL = 100 pF	_	70	
READ Output disable time		toн6	CL = 100 pF	5	50	

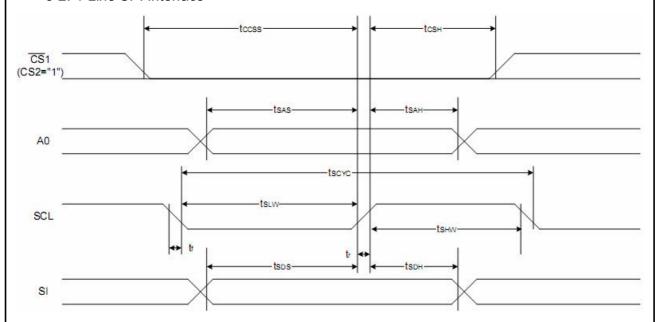
MODEL	GY1206P6SGW7G	8/18	PRODUCT SPECIFICATIONS	REV: B
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Display the World

8.TIMING CHARACTERISTICS

8-2. 4-Line SPI Interface



(VDD = 3.3V, Ta = -30 to 85°C)

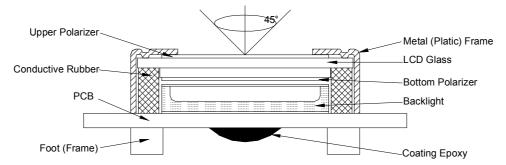
	0		01141	Rating		
Item	Signal	Symbol	Condition	Min.	Max.	Units
4-line SPI Clock Period		Tscyc		50	_	
SCL "H" pulse width	SCL	Tshw		25	_	
SCL "L" pulse width		Tstw		25		
Address setup time	40	Tsas		20	_	ns
Address hold time	A0	Tsah		10	_	
Data setup time	CI.	Tsds		20	_	
Data hold time	SI	Tsph		10		
CS-SCL time	cs	Tcss		20	-	
CS-SCL time	CS	Tcsh		40		

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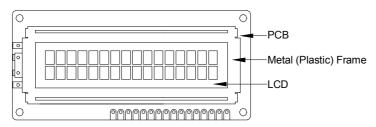


9. QUALITY SPECIFICATION

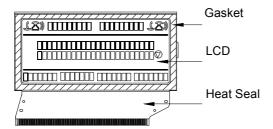
- 9 1. LCM Appearance and Electric inspection Condition
 - 1. Inspection will be done by placing LCM 30cm away from inspector's eyeballs under normal illumination.



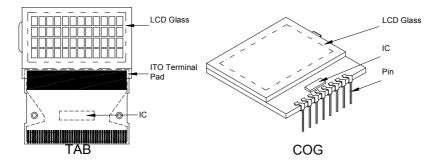
- 2. View Angle: with in 45° around perpendicular line.
- 9 2. Definition
- 1. COB



2. Heat Seal



3. TAB and COG





9. QUALITY SPECIFICATION (Continued)

9-3. Sampling Plan and Acceptance

1.Sampling Plan

MIL - STD - 105E (\parallel) ordinary single inspection is used.

2.Acceptance

Major defect: AQL = 0.25Minor defect: AQL = 0.65

9-4. Criteria

1.COB

Defect	Inspection Item	Inspection Standards	
Major	PCB copper flakes peeling off	Any copper flake in viewing Area should be greater than 1.0mm ²	Reject
Major	Height of coating epoxy	Exceed the dimension of drawing	Reject
Major	Void or hole of coating epoxy	Expose bonding wire or IC	Reject
Major	PCB cutting defect	Exceed the dimension of drawing	Reject

2.SMT

Defect	Inspection Item	Inspection Standa	ards
Minor	Component marking not readable		Reject
Minor	Component height	Exceed the dimension Of drawing	Reject
Major	Component solder defect (missing, extra, wrong component or wrong orientation		Reject
Minor	Component position shift component soldering pad X D Y	X < 3/4Z Y > 1/3D	Reject Reject
Minor	Component tilt component soldering pad	Y > 1/3D	Reject
Minor	Insufficient solder component PAD PCB	<i>θ</i> ≤ 20°	Reject

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9. QUALITY SECIFICATION (Continued)

- 9-4. Criteria (Continued)
 - 3. Metal (Plastic) Frame

Defect	Inspection Item	Inspection Standards					
Major	Crack / breakage	Any	Reject				
		W	L	Acceptable of Scratch			
		w<0.1mm	Any	Ignore			
		0.1 <u><</u> w<0.2mm	L <u><</u> 5.0mm	2			
Minor	Frame Scratch	0.2 <u><</u> w<0.3mm	L <u><</u> 3.0mm	1			
		w <u>></u> 0.3mm	Any	0			
		Note: 1. Above criteria applicable to scratch lines with distance greater than 5mm. 2. Scratch on the back side of frame (not visible) can be ignored.					
		,	<u> </u>	Acceptable of Dents / Pricks			
		Φ<	1.0mm	2			
	Frame Dent , Prick	1.0< ₫	1				
Minor	$\Phi = \frac{L + W}{2}$	1.5r	mm<Ф	0			
	2	Note: 1. Above criteria applicable to any two dent / pricks with distance greater than 5mm 2. Dent / prick on the back side of frame (n visible) can be ignored					
Minor	Frame Deformation	Excee	d the dimension of	drawing			
Minor	Metal Frame Oxidation		Any rust				

4. Flexible Film Connector (FFC)

Defect	Insp	ection Item	Inspection Standards		
Minor	Tilted soldering		Within the angle +5°	Acceptable	
Minor	Uneven s	older joint /bump		Reject	
Minor	Hole	Hole $\Phi = \frac{L + W}{2}$	Expose the conductive line	Reject	
IVIII IOI	TIOLE		Φ > 1.0mm	Reject	
Minor	Position shift	Y > 1/3D	Reject		
Minor	→ X		X > 1/2Z	Reject	

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9. QUALITY SPECIFICATION (Continued)

9-4. Criteria (Continued)

5. Screw

Defect	Inspection Item	Inspection Standards		
Major	Screw missing/loosen		Reject	
Minor	Screw oxidation	Any rust	Reject	
Minor	Screw deformation	Difficult to accept screw driver	Reject	

6. Heat seal \ TCP \ FPC

Defect	Inspection Item	Inspection Standards	
Major	Scratch expose conductive layer		Reject
Minor	HS Hole $\Phi = \frac{L + W}{2}$	⊕> 0.5mm	Reject
Major	Adhesion strength	Less than the specification	Reject
Minor	Position shift	Y > 1/3D	Reject
		X > 1/2Z	Reject
Major	Conductive line break		Reject

7. LED Backing Protective Film and Others

Defect	Inspection Item	Inspection Standards				
		Acceptable number of units				
		⊕ <u><</u> 0.10mm	Ignore			
	LED dirty, prick	0.10<⊕ <u><</u> 0.15mm	2			
Minor		0.15<⊕ <u><</u> 0.2mm	1			
		Φ>0.2mm	0			
		The distance between any two spots should be ≥ Any spot/dot/void outside of viewing area is accept				
Minor	Protective film tilt	t Not fully cover LCD F				
Major	COG coating	Not fully cover ITO circuit	Reject			

8. Electric Inspection

Defect Inspection Item		Inspection Standards	
Major	Short		Reject
Major	Open		Reject

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9. QUALITY SPECIFICATION (Continued)

- 9-4. Criteria (Continued)
- 9. Inspection Specification of LCD

Defect	Insp	pect Item		Ins	spection	S	tandard	S	
		* Glass Scratch	W	W ₅	<u><</u> 0.03	0.0	0.0 <u><</u> 0.0	5 V	V>0.05
		* Polarizer Scratch * Fiber and Linear	L	L	<u>.<5</u>		L<3		Any
Minor	Linear Defect		ACC. NO.	1			1 1		Reject
		material	Note	L is the le	ngth and V	V is th	e width of	the de	fect
		* Foreign material	Φ	Φ <u><</u> 0.1	0.1<⊕≤	0.15).15<⊕ <u><</u> 0	.2	Φ>0.2
	Black Spot and	between glass and polarizer or glass	ACC. NO.	3EA / 100mm²	2		1		0
Minor	Polarizer Pricked	and glass * Polarizer hole or protuberance by external force	Note	Φ is the	average di between tv				
		* Unobvious	Φ	Φ<	0.3	0.3	<⊕ <u><</u> 0.5	0.	5<⊕
	White Spot	transparant foreign material between	ACC. NO.	3EA / 1	00mm ²		1		0
Minor and Bubble polarizer		glass and glass or glass and polarizer * Air protuberance between polarizer and glass	Note	 Φ is the average diameter of the defect. Distance between two defects > 10mm. 					
	Segment Defect	w d	Φ	Φ <u><</u> 0.10	0.10<⊕ <u><</u> 0.20		0.20<⊕ <u><</u> 0.25		Φ>0.25
			ACC. NO.	3EA / 100mm²	2		1		0
Minor		T W		W is more	than 1/2	segme	ent width		Reject
		W	Note	$\Phi = \frac{L + W}{2}$ Distance between two defect is 10mm					
		A CO		Ф <u><</u> 0.10	0.10<⊕≤	<u><</u> 0.20	0.20<Φ <u>·</u>	<u><</u> 0.25	Ф>0.25
Minor	Protuberant Segment		W	Glue	W <u><</u> 1/2 W <u><</u> 0		W <u><</u> 1/2 W <u><</u> 0	Seg .2	Ignore
		Φ = (L + W) / 2	ACC. NO.	3EA / 100mm ²	2		1		0
			1. Seg	ment					
			Е	B B	<u><</u> 0.4mm	0.4 <e< td=""><td>3<u><</u>1.0mm</td><td>B>1</td><td>I.0mm</td></e<>	3 <u><</u> 1.0mm	B>1	I.0mm
	Assembly		B-	A B	-A<1/2B	В-	B-A<0.2 B-A		<0.25
Minor	Mis-alignment	nent had - ha	Jud	dge Acceptable Acceptable Acceptab			eptable		
			2. Dot	Matrix					
			Defo	ormation>2	2°				Reject
Minor	Stain on LCD Panel Surface		or a	similar on	ains can be e. Otherw ack spot" a	∕ise, j	udged ac	cordin	



10. RELIABILITY

NO.	Item	Condition	Criterion
1	High Temperature Operating	70°C, 96Hrs	
2	Low Temperature Operating	-20°ℂ, 96Hrs	
3	High Humidity	60°C, 90%RH, 96Hrs	
4	High Temperature Storage	80°C, 96Hrs	No defect in cosmetic
5	Low Temperature Storage	-30°C, 96Hrs	and operational function allowable.
		Random wave	Total current Consumption should
	Vibration	10 ~ 100Hz	be below double of initial value.
6		Acceleration: 2G	
		60 Minute	
		-10°C to 25°C to 50°C	
7	Thermal Shock	(60Min) (15Min) (60Min)	
		10Cycles	
	COD To ation or	Contract Discharge Voltage: +1 ~ 5kV and –1 ~ –5kV	There will be discharged ten times
8		Air Discharge Voltage: +1 ~ 8kV and –1 ~ -8kV	at every discharging voltage cycle. The voltage gap is 1kV.

Note: 1) Above conditions are suitable for GOLDENTEK standard products.

2) For restrict products, the test conditions listed as above must be revised.

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11. HANDLING PRECAUTION

(1) Mounting Method

The panel of the LCD Module consists of two thin glass plates with polarizers which easily get damaged since the Module is fixed by utilizing fitting holes in the printed circuit board. Extreme care should be taken when handling the LCD Modules.

(2) Caution of LCD handling & cleaning

When cleaning the display surface, use soft cloth with solvent (recommended below) and wipe lightly.

- Isopropyl alcohol
- Ethyl alcohol
- Trichlorotrifloroethane

Do not wipe the display surface with dry or hard materials that will damage the polarizer surface.

Do not use the following solvent:

- Water
- Ketone
- Aromatics

(3) Caution against static charge

The LCD Module use C-MOS LSI drivers, so we recommend that you connect any unused input terminal to VDD or VSS, do not input any signals before power is turned on. And ground your body, Work/assembly table. And assembly equipment to protect against static electricity.

(4) Packaging

- Modules use LCD elements, and must be treated as such. Avoid intense shock and falls from a height.
- To prevent modules from degradation. Do not operate or store them exposed directly to sunshine or high temperature/humidity.

(5) Caution for operation

 It is indispensable to drive LCD's within the specified voltage limit since the higher voltage than the limit shorten LCD life. An electrochemical reaction due to direct current causes LCD deterioration, Avoid the use of direct current drive.

MODEL	GY1206P6SGW7G	16/18	PRODUCT SPECIFICATIONS	REV: B



11. HANDLING PRECAUTION (Continued)

- Response time will be extremely delayed at lower temperature than the operating temperature range and on the other hand at higher temperature LCD's show dark color in them.

However those phenomena do not mean malfunction or out of order with LCD's. Which will come back in the specified operating temperature range.

- If the display area is pushed hard during operation, some font will be abnormally displayed but it resumes normal condition after turning off once.
- A slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit.

Usage under the relative condition of 40°C, 80%RH or less is required.

(6) Storage

In the case of storing for a long period of time (for instance ,for years) for the purpose or replacement use, The following ways are recommended.

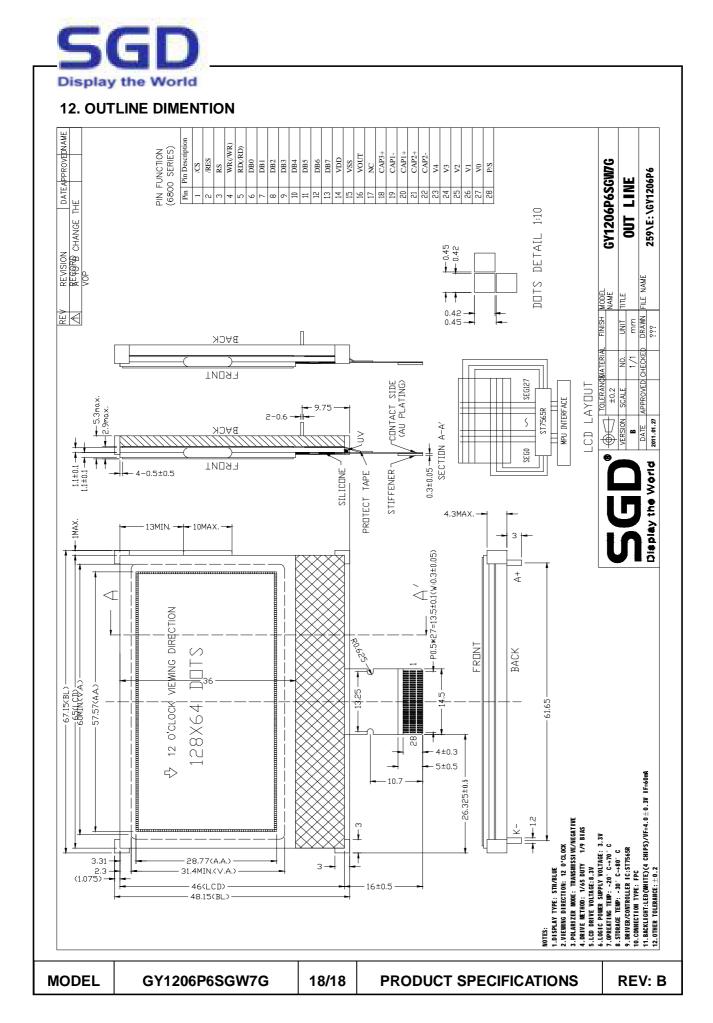
- Storage in a polyethylene bag with sealed so as not to enter fresh air outside in it, And with no desiccant.
- Placing in a dark place where neither exposure to direct sunlight nor light is. Keeping temperature in the specified storage temperature range.
- Storing with no touch on polarizer surface by the anything else. (It is recommended to store them as they have been contained in the inner container at the time of delivery)

(7) Safety

 It is recommendable to crash damaged or unnecessary LCD into pieces and wash off liquid crystal by using solvents such as acetone and ethanol.

Which should be burned up later.

- When any liquid crystal leaked out of a damaged glass cell comes in contact with your hands, please wash it off well with soap and water.





SAMPLE OUTGOING INSPECTION REPORT (LCM)

Data: 2011/04/26 NO.: QAB04019

Cu	stomer	Product NO.				С	Driving	y Vol	tage	Testin	Testing Condition			Quantity	
高	雄富相	GY1206P6SGW7G					VOP: 8.3V				25℃			3Pcs	
Inspection Result															
It	tems	Specification													
Display Mode		○ W / B Mode ○ B / W Mode ○ Yellow Mode ●Blue Mode ○ Gray N											Gray Mode		
Polarizer Type		0	Reflecti	ve			 Transflective 				● Transmissive				
Viewing direction		○ 3 O'clock ○ 6 O'c					clock 9 O'c				clock 12 O'clock				
Electrical / Appearance															
ltem		Inspection Method					Specification				Inspection Result				
Appearance		Spot Gauge Caliper					Final Inspection Criteria				а	• 0	K	○ NG	
Electrical		LCM Tester					Product Specification					• 0	K	○ NG	
Pattern		LCM Tester					Drawing					• 0	K	○ NG	
Dimension / Supply Current															
Item	Spec.(mn		NO.1	NO.2	NO.2 NO.3 NO.4			5	Res	sult	Fig.				
L1	67.15 <u>+</u> 0.2		67.20	67.18	67.19	67.21	67.1	9	OK	O NG					
L2	14.5 <u>+</u> 0.3		14.46	14.46	14.46	14.48	14.5	0	ОК	O NG			L1		
W1	48.15 <u>+</u> 0.2		48.23	48.21	48.22	48.17	48.1	5	OK	O NG	128X64 D		4 DOTS	FRONT	
W2	16.0 <u>+</u> 0.5		16.06	16.16	16.33	16.28	16.1	4	ОК	O NG					
Т	5.3mm Max		4.77	4.79	4.78	4.78	4.83	3	OK	○ NG					
IDD 3.0mA M		lax	1.50	1.50	1.50	1.50	1.50	0	OK	○ NG			-rs 		
Designed		ELIN			С	Checked			1			oroved	Wallace		

Doc. NO.: F10018A