

Date: 2011/08/10

Specifications for Approval

Customer	: Mc ' Tronic	
Model name	: GY1206P6FSB6G	REV: A
Description	: LCM (Y1206P6FSB6 – DD68	2 – B0802A)

LCD Specification: LCD (SDD682 - 81 - 8223 - 110723 - 1)

ISSUE	ENG	QA	APPROVAL	
Lily Li	Bose Xie	Wallace	Michael	

	Accept
	Reject Comment:
Customer	
Approval	
	Approved by:
	Approved by:



REVISION RECORD (MODEL NO.: GY1206P6FSB6G)

Revision	Revision Date	Page	Contents
A	2011/08/10		Initial Release and Issue Full Specification.





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MODEL	GY1206P6FSB6G	1/18	PRODUCT SPECIFICATIONS	REV: A



1. FEATURES

The features of LCD are as follows

* Display mode	: FSTN, Positive, High Transflective
* Color	: Display dot : Black
	Background: White
* Display Format	: 128Dots × 64Dots graphic
* IC	: Sitronix ST7565R-G
* Interface Input Data	: 8-Bit Parallel 6800 / 4-Line SPI MPU Interface
* Driving Method	: 1/65 Duty, 1/9 Bias
* Viewing Direction	: 6 O'clock
* Backlight	: LED (Blue)

* LCM technological conditions: RoHS

2. MECHANICAL SPECIFICATIONS

ltem	Specification	Unit
Module Size	67.15(W) X 63.075(H) X 5.3(T)	mm
Viewing Area	61.0(W) X 31.4(H)	mm
Effective Display Area	57.57(W) X 28.77(H)	mm
Number of Dots	128 X 64 Dots	-
Dot Size	0.42(W) X 0.42(H)	mm
Dot Pitch	0.45(W) X 0.45(H)	mm

3. ELECTRICAL SPECIFICATIONS

3-1. Absolute Maximum Ratings (Vss=0V)

	ltem	Sym	hal	Sta			
			Symbol		Тур.	Max.	Unit
S	upply Voltage For Logic	VD	D	0.3	-	5.0	V
Sup	ply Voltage For LCD Drive	V0,VOUT		0.3	-	18.0	V
	Operating Temp.	Тор		-20	-	+70	°C
	Storage Temp.	Tst		-30	-	+80	°C
	Static Electricity	Be sue that you are ground when handing LCM					
DDEL	GY1206P6FSB6G	2/18	PRO	DDUCT S	SPECIF	ICATIONS	RE



3. ELECTRICAL SPECIFICATIONS (Continued)

3-2-1.Electrical Characteristics

ltem		Symbol	Test Condition	Min.	Тур.	Max.	Unit
Supply Voltage For Logic		Vdd - Vss	-	1.8	3.3	3.3	V
Supply Voltage For LCD		V0-Vss	-	8.0	8.3	8.6	V
	"H" Level	V _{IH}		0.8Vdd	-	Vdd	V
Input Voltage	"L" Level	V IL	-	Vss	-	0.2Vdd	V
			I _{OUT} = -0.5mA	0.8Vdd	-	Vdd	V
Output Voltage	"L" Level	V _{ol}	I _{OUT} = 0.5mA	Vss	-	0.2Vdd	V
Current Consu	Imption	I _{DD}	$V_{\text{IN}} = V_{\text{DD}}$	-	1.50	3.0	mA
Peakwavelength		λρ	Dots All off				-
Luminous Intensity (LCM)		L	Dots All on			-	cd/m ²
		L	Dots All off			-	cd/m ²

NOTE: 1) Duty ratio=1/65, Bias=1/9 2) Measured in Dots ON-state

3-3.BACKLIGHT

3-3-1. Absolute Maximum Ratings

ltem	Symbol	Condition	Min.	Тур.	Max	Unit
Forward Current	IF	Ta= 25 ℃	-	-	100	mA
Reverse Voltage	VR	1a- 23 (-	-	10	V
Power Dissipation	PD	Ta= 25 ℃	-	-	410	mW

3-3-2. Opto-electronic Characteristics

ltem	Symbol	Condition	Min.	Тур.	Max	Unit
Forward Voltage	VF	IF=40mA	3.7	3.9	4.1	V
Reverse current	lr	Vr=10V	-	-	60	uA
Luminous intensity	LV	IF=40mA	40	60	-	cd/m²
Peakwavelength	λρ	IF=40mA	465	470	475	-

* The brightness is measured without LCD panel

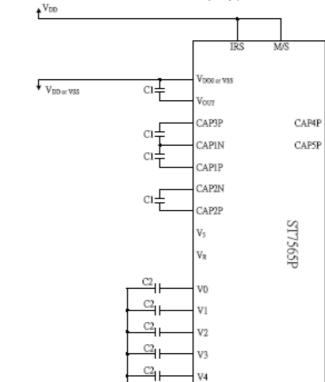
MODEL



4. POWER SUPPLY and BLOCK DIAGRMA

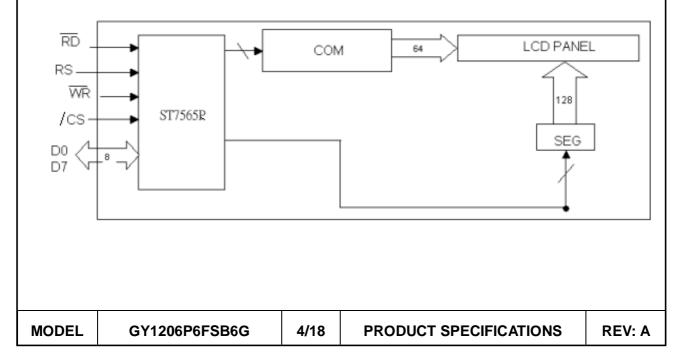
4-1 Power supply

- When the voltage regulator internal resistor is used.
- (Example where Voo2 = Voo, with 4x step-up)



VDD2 or Vss

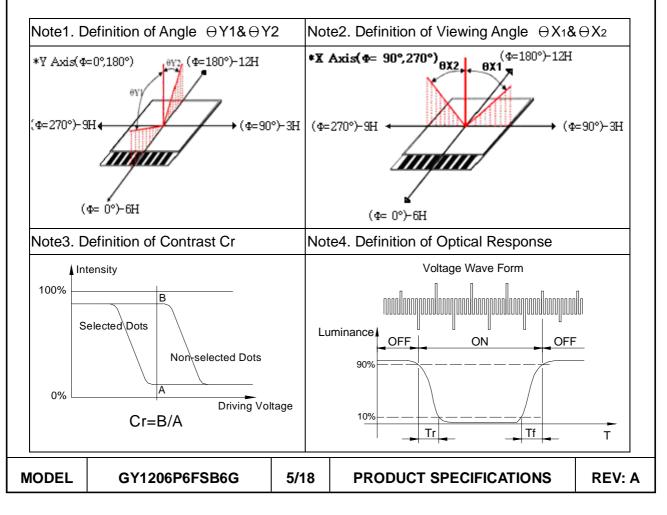
4-2 Block diagram





5. ELECTRO – OPTICAL CHARACTERISTICS

ltem		Symbol	Temp.	Min.	Тур.	Max.	Unit	Conditions	Note
Viewing	Φ=0°	θ1			33			-	
	Φ=180°	θ2	25 ℃		35		Deg		1.0
Angle Cr <u>></u> 2	Φ=90°	⊖3	230		28		Deg.		1,2
	Φ=270°	θ4			32				
Viev	Viewing Direction			6 O'clock					
	Contrast Ratio		25 ℃	2.0	5.08	5.18	-	$\Theta = 0^{\circ}$ $\Phi = 0^{\circ}$	3
Respo	Response		25°C - 168 300 θ=	ms	$\Theta = 0^{\circ}$	4			
Time(rise)	rise)	Tr	0 °C	-	1250	1650	1113	$\Phi = 0^{\circ}$	-
	Response		25 ℃	-	268	300	ms	$\Theta = 0^{\circ}$	4
Time	(fall)	Tf	0 °C	-	1250	1650	1115	$\Phi = 0^{\circ}$	4





6. INTERFACE PIN FUNCTION

Pin NO.	Symbol	1/0	Functions
1	/CS	I	This is the chip select signal.
2	/RES	I	When RES is set to "L", the setting are initialized.
3	A0	Ι	This is connect to the least significant bit of the Norman MPU address bus, and it determines whether the data bits are data or a command.
4	WR(/WR)	I	The data bus are latched at the rising edge of the WR signal
5	RD(/RD)	I	The data bus is in output status when this signal is "L"
6~13	D0~ D7	I/O	This is an 8-bit bi-directional data bus that connects to an 8-bit of 16-bit standard MPU data bus.
14	V_{DD}	Power supply	Power supply
15	V_{SS}	Power supply	Ground
16	V _{OUT}	0	DC/DC voltage converter. Connect a capacitor between this terminal and v_{ss} or VDD
17	NC	/	No connect
18	CAP3+	0	DC/DC voltage converter. Connect a capacitor between this terminal and the CAP1N terminal.
19	CAP1-	0	DC/DC voltage converter. Connect a capacitor between this terminal and the CAP1P terminal.
20	CAP1+	0	DC/DC voltage converter. Connect a capacitor between this terminal and the CAP1N terminal.
21	CAP2+	0	DC/DC voltage converter. Connect a capacitor between this terminal and the CAP2N terminal.
22	CAP2-	0	DC/DC voltage converter. Connect a capacitor between this terminal and the CAP2P terminal.
23~27	V4~ V0	Power supply	This is a multi-level power supply for the liquid crystal drive.
28	P/S	I	This pin configures the interface to parallel mode or serial mode. P/S="H":Parallel data input/output. P/S="L":Serial data input.
IODEL	GY1	206P6FS	B6G 6/18 PRODUCT SPECIFICATIONS REV:

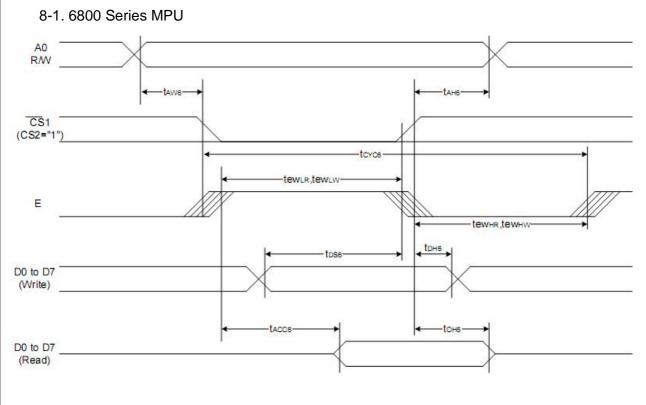


7. COMMAND LIST

Command				Cor	nma	ind C	lode					Function
	A0	/RD	/WR	D7	D6			03	D2			
1) Display ON/OFF	٥	1	0		0	1	D	1	1	1	0 1	LCD display ON/OFF 0: OFF, 1: ON
2) Display start line set	٥	1	Ð	D	1	DI	spia	y sta	art ai	didire	95	Sets the display RAM display start Ine address
3) Page address set	٥	1	Ю	1	0	1	1	Pa	ge a	ddin	ess	Sets the display RAM page address
 Column address set upper bit 	o	1	D	D	B	Ο	1				cant	Sets the most significant 4 bits of the display RAM column address.
Column address set lower bit	٥	1	0	0	0	0	0	Les	st si	gnif	iress icant iress	Sets the least significant 4 bits of the display RAM column address.
5) Status read	Ð	0	1		81	atus		в	0	0	0	Reads the status data
6) Olsplay data write	1	1	0			1	Nith	e dat	7			Writes to the display RAM
7) Olsplay data read	1	0	1				Rea	d dai	a			Reads from the display RAM
8) ADC select	٥	1	0	1	Θ	1	0	0	D	0	0 1	Sets the display RAM address SEG output correspondence 0: normal, 1: reverse
9) Display normal/ reverse	0	1	0	1	D	1	0	0	1	1	0 1	Sets the LCD display normal/ reverse 0: normal, 1: reverse
10) Display all points			_	1	D	1	0	0	1	0	0	Display all points
ON/OFF	Ð	1	0								1	0: normal display 1: all points ON
11) LCD blas set	٥	1	0	1	D	1	٥	0	0	1	0 1	Sets the LCD drive voltage blas ratio 0: 1/9 blas, 1: 1/7 blas (ST7565P)
12) Read/modify/write	O	1	0	1	1	1	D	0	0	D	0	Column address increment At write: +1 At read: 0
13) End	0	1	Ю	1	1	1	B	1	1	1	0	Clear read/modify/write
14) Reset	0	1	Ð	1	1	1	D	0	0	1	0	Internal reset
15) Common output mode select	o	1	0	1	1	٥	0	0 1			•	Select COM output scan direction 0: normal direction 1: reverse direction
16) Power control set	0	1	0	0	B	1	D	1		era ide	ting	Select Internal power supply operating mode
17) Vo voltage regulator Internal resistor ratio	٥	1	0	0	D	1	0	0	Re	sist tio	ar	Select Internal resistor ratio(Rb/Ra) mode
set 18) Electronic volume mode set Electronic volume register set	٥	1	0	1 0	0	0 Ele	0 ctro	0 nic v	0 olun	0 ne v	1 alue	Set the Vo output voltage electronic volume register
19) Static Indicator ON/OFF				1	B	1	0	1	1	0	0	0: OFF, 1: ON
Static Indicator	٥	1	D	D	0	0	D	0	D	D	1 Mode	Set the flashing mode
register set 20) Booster ratio set	٥	1	0	- 1 0	1 0	1	- 1 0	1		0 ste	0 p-up	select booster ratio 00: 2x,3x,4x 01: 5x
21) Power saver										W2	lue	11: 5x Display OFF and display all
	0	1	D	-	1	1	D				1	points ON compound command Command for non-operation
22) NOP 23) Test	0 0	1	0	1	1		1	0	- 0	-	1	Command for IC test. Do not
ODEL GY1206	6P6	FSB	6 G		7/1	8		PR	OD	UC	T SF	PECIFICATIONS REV:



8.TIMING CHARACTERISTICS



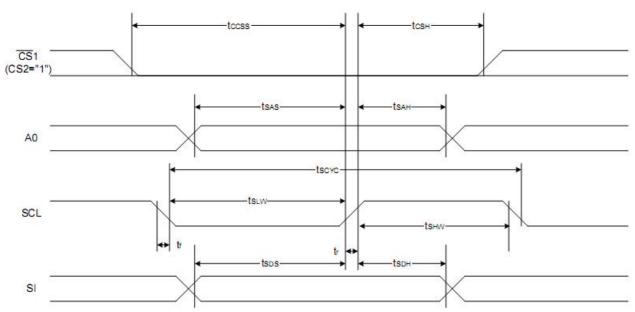
Units	ing	Rat	Condition	Cumhal	Cinnel	literes
Units	Max.	Min.	Condition	Symbol	Signal	Item
	3 <u>—4</u>	0		tah6		Address hold time
	—	0		taw6	A0	Address setup time
	I	240		tcyc6		System cycle time
	-	80		tewlw	WR	Enable L pulse width (WRITE)
	 .	80		t EWHW	VVIX	Enable H pulse width (WRITE)
ns	-	80		tewlr	RD	Enable L pulse width (READ)
		140		tewnr	- KD	Enable H pulse width (READ)
	8	40		tDS6		WRITE Data setup time
	—	0		tdh6	D0 to D7	WRITE Address hold time
	70	-	CL = 100 pF	tACC6		READ access time
	50	5	CL = 100 pF	toнe	7	READ Output disable time

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8.TIMING CHARACTERISTICS

8-2. 4-Line SPI Interface



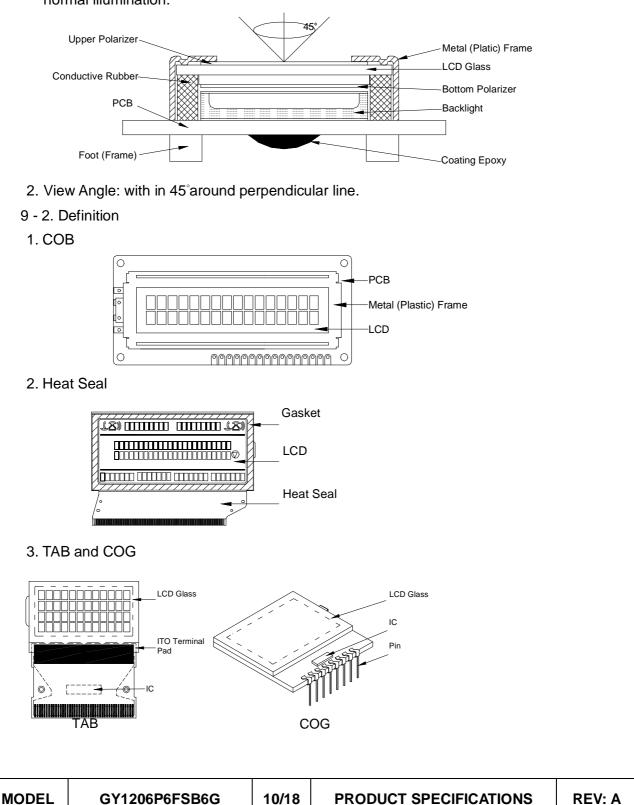
Item	Signal	Symbol	Condition	Rat	ing	Units
Item	Signal	Symbol	Condition	Min.	Max.	Units
ne SPI Clock Period		Tscyc		50	_	
L "H" pulse width	SCL	Tshw		25		
L"L" pulse width		Tslw		25	_	
Iress setup time	A0	TSAS		20	_	
Iress hold time	AU	Tsah		10	_	ns
a setup time	SI	Tsds		20	_	
a hold time	51	Тѕрн		10		
SCL time	CS	Tcss		20		
SCL time	US	Tcsh		40		

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9. QUALITY SPECIFICATION

- 9 1. LCM Appearance and Electric inspection Condition
 - 1. Inspection will be done by placing LCM 30cm away from inspector's eyeballs under normal illumination.





9. QUALITY SPECIFICATION (Continued)

- 9-3. Sampling Plan and Acceptance
- 1.Sampling Plan

MIL - STD - 105E (\parallel) ordinary single inspection is used.

2.Acceptance

Major defect:	AQL = 0.25
Minor defect:	AQL = 0.65

9-4. Criteria

1.COB

Defect	Inspection Item	Inspection Standards	
Major	PCB copper flakes peeling off	Any copper flake in viewing Area should be greater than 1.0mm ²	Reject
Major	Height of coating epoxy	Exceed the dimension of drawing	Reject
Major	Void or hole of coating epoxy	Expose bonding wire or IC	Reject
Major	PCB cutting defect	Exceed the dimension of drawing	Reject

2.SMT

Defect	Inspection Item	Inspection Stan	dards
Minor	Component marking not rea	idable	Reject
Minor	Component height	Exceed the dimensior Of drawing	Reject
Major	Component solder defect (missir wrong component or wrong orier	ntation	Reject
Minor	Component position shi component soldering X \rightarrow \rightarrow \rightarrow \rightarrow \rightarrow \rightarrow \rightarrow \rightarrow \rightarrow \rightarrow		Reject Reject
Minor	Component tilt compor	nent Y > 1/3D ∱ Y	Reject
Minor	Insufficient solder component	PAD θ ≤ 20° − PCB	Reject
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9. QUALITY SECIFICATION (Continued)

9-4. Criteria (Continued)

3. Metal (Plastic) Frame

Defect	Inspection Item	l Ir	nspection Standa	rds		
Major	Crack / breakage	Any	where	Reject		
		W	L	Acceptable of Scratch		
		w<0.1mm	Any	Ignore		
		0.1 <u><</u> w<0.2mm	L <u><</u> 5.0mm	2		
Minor	Frame Scratch	0.2 <u><</u> w<0.3mm	L <u><</u> 3.0mm	1		
		w <u>></u> 0.3mm	Any	0		
		Note : 1. Above criteria applicable to scratch lines with distance greater than 5mm. 2. Scratch on the back side of frame (not visible) can be ignored .				
				Acceptable of Dents / Pricks		
		Φ <u><</u>	1.0mm	2		
	Frame Dent , Prick	1.0<	⊳ <u><</u> 1.5mm	1		
Minor	$\Phi = \frac{L + W}{2}$	1.5r	nm< Φ	0		
	2	/ pricks with dis	criteria applicable tance greater than prick on the back gnored	5mm		
Minor	Frame Deformation	Excee	d the dimension of	drawing		
Minor	Metal Frame Oxidation		Any rust			

4. Flexible Film Connector (FFC)

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Defect	Insp	ection Item	Inspection Standa	rds
Minor	Tilte	d soldering	Within the angle $+5^{\circ}$	Acceptable
Minor	Uneven solder joint /bump			Reject
Minor	Hole	L+W	Expose the conductive line	Reject
IVIITIOI	HOIE	$\Phi = \frac{L + W}{2}$	Φ > 1.0mm	Reject
Minor	Position s	hift →Z ₁ ↓ □ D	Y > 1/3D	Reject
			X > 1/2Z	Reject

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9. QUALITY SPECIFICATION (Continued)

9-4. Criteria (Continued)

Defect	Inspection Item	Inspection Standards						
Major	Screw missing/loosen		Reject					
Minor	Screw oxidation	Any rust	Reject					
Minor	Screw deformation	Difficult to accept screw driver	Reject					
6. Heat seal 、TCP 、FPC								
Defect	Inspection Item	Inspection Standards						
Major	Scratch expose conductive layer		Reject					
Minor	HS Hole $\Phi = \frac{L+W}{2}$	Φ > 0.5mm	Reject					
Major	Adhesion strength	Less than the specification	Reject					
Minor	Position shift	Y > 1/3D	Reject					
		X > 1/2Z	Reject					
Major	Conductive line break		Reject					
7. LED Bac	king Protective Film and Others							

Defect	Inspection Item	Inspection Standards					
		Acceptable number of units					
Minor		⊕ <u><</u> 0.10mm	lgnore				
		0.10<⊕ <u><</u> 0.15mm	2				
	LED dirty, prick	0.15<⊕ <u><</u> 0.2mm	1				
		⊕>0.2mm	0				
		The distance between any two spots should be \geq 5mm Any spot/dot/void outside of viewing area is acceptable					
Minor	Protective film tilt	Not fully cover LCD	Reject				
Major	COG coating	Not fully cover ITO circuit R					
8. Electric	Inspection		1				

	•		
Defect	Inspection Item	Inspection Standards	
Major	Short		Reject
Major	Open		Reject

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9. QUALITY SPECIFICATION (Continued)

9-4. Criteria (Continued)

9. Inspection Specification of LCD

	Ins	pect Item			I	ns	pection		Standard	S	
		* Glass Scrato	h	W	١	_	0.03	0.	03 <w<u><0.0</w<u>	5 V	V>0.05
		* Polarizer Sci	-	L		L<	<5		L<3		Any
Minor	Linear Defect	* Fiber and Lir		ACC. NO.		1	I		1		Reject
		material		Note	L is the	len	gth and V	V is th	ne width of	the de	efect
		* Foreign mate	erial	Φ	Φ <u><</u> 0.	.1	0.1<⊕ <u><</u>	0.15	0.15<⊕ <u><</u> 0	.2	Φ >0.2
		between glass polarizer or glas	and	ACC. NO.	3EA 100mr	/ m²	2		1		0
Minor	Polarizer Pricked	and glass * Polarizer hol protuberance b external force		Note					er of the de fects > 10m		
		* Unobvious		Φ	(⊅ <u>≺</u> 0).3	0.3	<0.5	0.	5 <Φ
	White Spot	transparant for material betwee	en	ACC. NO.	3EA	/ 10	00mm ²		1		0
Minor and Bubble in polarizer		glass and glass glass and polar * Air protubera between polariz and glass	rizer ance	Note			-		er of the de fects > 10m		
	Segment Defect			Φ	Φ <u><</u> 0.1		0.10<⊕ <u>·</u>	<u><</u> 0.20	0.20<Φ <u><</u>	<u><</u> 0.25	⊕>0.25
				ACC. NO.	3EA / 100mr	/ n²	2		1		0
Minor					W is more than 1/2 segment width Reject				Reject		
				Note	$\Psi = -$	- 4	<u>~</u>	vo de	fect is 10m	m	
					Φ <u><</u> 0.1	10	0.10<⊕ <u><</u> 0.20 0.20<⊕ <u><</u> 0.		0.25	Φ>0.25	
Minor	Protuberant			W	Glue	-	W≤1/2 Seg W<0.2 W≤1/2 Seg W<0.2		Seg	Ignore	
	Segment	$\Phi = (L + W) / 2$		ACC. NO.	3EA 100mr	/ n ²	2		1		0
				1. Seg	ment						
				Е	3	B<	0.4mm	0.4<	B <u><</u> 1.0mm	B>′	1.0mm
	Assembly			B-	A		A<1/2B		-A<0.2		<0.25
Minor	Mis-alignment		A	Jud	•	Acc	eptable	Aco	ceptable	Acc	eptable
				2. Dot	Matrix						
					ormation			<u> </u>			Reject
Minor	Stain on LCD Panel Surface			ora	similar	one	. Otherw	/ise,	ed lightly w judged aco Vhite Spot"		
[14/18						CATIONS		



10. RELIABILITY

NO.	ltem	Condition	Criterion		
1	High Temperature Operating	70℃, 96Hrs			
2	Low Temperature Operating	-20℃, 96Hrs			
3	High Humidity	60℃, 90%RH, 96Hrs			
4	High Temperature Storage	80℃, 96Hrs	No defect in cosmetic and operational function allowable.		
5	Low Temperature Storage	-30℃, 96Hrs			
		Random wave	Total current Consumption should		
0		10 ~ 100Hz	be below double of initial value.		
6	Vibration	Acceleration: 2G			
		60 Minute			
		-10℃ to 25℃ to 50℃			
7	Thermal Shock	(60Min) (15Min) (60Min)			
		10Cycles			
6		Contract Discharge Voltage: +1 ~ 5kV and -1 ~ -5kV	There will be discharged ten times at every discharging voltage cycle. The voltage gap is 1kV.		
8	ESD Testing	Air Discharge Voltage: +1 ~ 8kV and –1 ~ -8kV			

Note: 1) Above conditions are suitable for GOLDENTEK standard products.

2) For restrict products, the test conditions listed as above must be revised.

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11. HANDLING PRECAUTION

(1) Mounting Method

The panel of the LCD Module consists of two thin glass plates with polarizers which easily get damaged since the Module is fixed by utilizing fitting holes in the printed circuit board. Extreme care should be taken when handling the LCD Modules.

(2) Caution of LCD handling & cleaning

When cleaning the display surface, use soft cloth with solvent (recommended below) and wipe lightly.

- Isopropyl alcohol
- Ethyl alcohol
- Trichlorotrifloroethane

Do not wipe the display surface with dry or hard materials that will damage the polarizer surface.

Do not use the following solvent:

- Water
- Ketone
- Aromatics
- (3) Caution against static charge

The LCD Module use C-MOS LSI drivers, so we recommend that you connect any unused input terminal to VDD or VSS, do not input any signals before power is turned on. And ground your body, Work/assembly table. And assembly equipment to protect against static electricity.

(4) Packaging

- Modules use LCD elements, and must be treated as such. Avoid intense shock and falls from a height.

- To prevent modules from degradation. Do not operate or store them exposed directly to sunshine or high temperature/humidity.
- (5) Caution for operation
 - It is indispensable to drive LCD's within the specified voltage limit since the higher voltage than the limit shorten LCD life. An electrochemical reaction due to direct current causes LCD deterioration, Avoid the use of direct current drive.

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11. HANDLING PRECAUTION (Continued)

- Response time will be extremely delayed at lower temperature than the operating temperature range and on the other hand at higher temperature LCD's show dark color in them.

However those phenomena do not mean malfunction or out of order with LCD's. Which will come back in the specified operating temperature range.

- If the display area is pushed hard during operation, some font will be abnormally displayed but it resumes normal condition after turning off once.
- A slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit.

Usage under the relative condition of 40°C, 80%RH or less is required.

(6) Storage

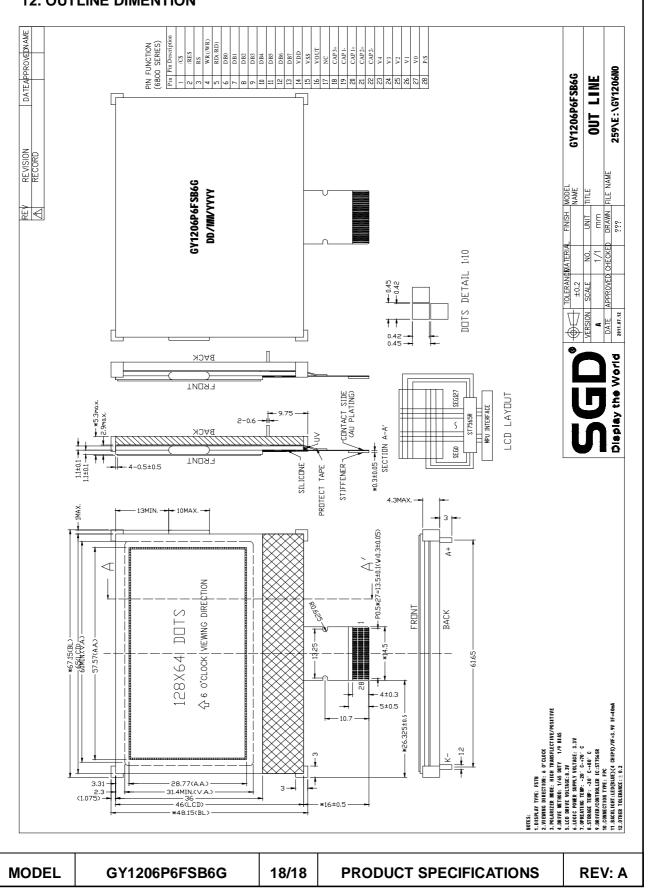
In the case of storing for a long period of time (for instance ,for years) for the purpose or replacement use, The following ways are recommended.

- Storage in a polyethylene bag with sealed so as not to enter fresh air outside in it, And with no desiccant.
- Placing in a dark place where neither exposure to direct sunlight nor light is. Keeping temperature in the specified storage temperature range.
- Storing with no touch on polarizer surface by the anything else. (It is recommended to store them as they have been contained in the inner container at the time of delivery)
- (7) Safety
 - It is recommendable to crash damaged or unnecessary LCD into pieces and wash off liquid crystal by using solvents such as acetone and ethanol.
 - Which should be burned up later.
 - When any liquid crystal leaked out of a damaged glass cell comes in contact with your hands, please wash it off well with soap and water.

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MODEL	GY1206P6FSB6G	17/18	PRODUCT SPECIFICATIONS	REV: A



12. OUTLINE DIMENTION





SAMPLE OUTGOING INSPECTION REPORT (LCM)

Data: 2011/08/10

NO.: QAB08011

Customer Product NO.			0.	C	Driving Voltage Testin			g Condition	Quantity			
盲	雄富相	GY1206P6FSB6G VOP: 8.3V					25℃ 22P					
Inspection Result											<u> </u>	
It	tems							Specifica	tion			
Displ	Display Mode W / B Mode B / W Mode Yellow Mode Blue Mode Gray Mode											Gray Mode
Polar	izer Type	\bigcirc	Reflecti	ve			● Tra	ansflective		⊖ Trans	missive	9
	ewing rection	\bigcirc	3 O'cloo	ck	•	6 O'c	lock		⊖ 9 O'c	lock) 12 C	°clock
						Elect	rical / A	ppearanc	e			
	ltem		Inspe	ection N	lethod			Specifica	tion	Ins	pectio	n Result
Арр	earance		Spot	Gauge	Caliper		Fina	l Inspectio	n Criteria		ЭK	⊖ NG
Ele	ectrical		L	.CM Tes	ster		Pro	duct Spec	ification	• 0	ЭK	⊖ NG
Ρ	attern		L	.CM Tes	ster			Drawin	g	• 0	ЭК	⊖ NG
		1			D	imens	ion / Si	apply Cur	rent			
ltem	Spec.(m	m)	NO.1	NO.2	NO.3	NO.4	NO.5	Res	sult		Fig.	
L1	67.15 <u>+</u> 0).2	66.99	67.12	67.06	67.08	67.06	• ОК	\bigcirc NG			
L2	14.5 <u>+</u> 0.	.3	14.43	14.45	14.52	14.48	14.43	• ок	⊖ NG		—u	
W1	48.15 <u>+</u> 0).2	48.16	48.18	48.19	48.16	48.17	• ок	⊖ NG	128X€ ▼ 128X€	54 DOTS	F RDNT
W2	16.0 <u>+</u> 0.	.5	16.08	15.88	16.23	15.92	16.28	• ок	⊖ NG			
т	5.3mm N	lax	4.95	4.93	4.92	4.93	4.95	• ок	⊖ NG	88		
IDD	3.0mA N	lax	1.50	1.50	1.50	1.50	1.50	• ок	⊖ NG		—L2 —	-
De	signed		EL	IN	с	hecke	d			Approved		Wallace
	-									••		

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