![SGD - Display the World]

Specifications for Approval

Customer : Mc ' Tronic

Model name : GY1206N0FVE6G   REV: B

Description : LCM (Y1206N0FVE6 – DD682 – B0417B)

LCD Specification: LCD (SDD682 – 74 – 8222 – 110409 – 1)

ISSUE	ENG	QA	APPROVAL
Lily Li	Bose Xie	Wallace	Michael

Customer Approval	☐ **Accept** ☐ **Reject** **Comment:** **Approved by:** _____

REVISION RECORD (MODEL NO.: GY1206N0FVE6G)

Revision	Revision Date	Page	Contents
A	2011/03/12		Initial Release and Issue Full Specification.
B	2011/04/26		Improve the display effect .

CONTENTS

MODEL	GY1206N0FVE6G	1/18	PRODUCT SPECIFICATIONS	REV: B

1. FEATURES

The features of LCD are as follows

* Display mode   : FSTN, Negative, Transmissive

* Color   : Display dot : White

                        Background: Black

* Display Format   : 128Dots × 64Dots graphic

* IC   : Sitronix ST7565R-G

* Interface Input Data : 8-Bit Parallel 6800 / 4-Line SPI MPU Interface

* Driving Method   : 1/65 Duty, 1/9 Bias

* Viewing Direction   : 6 O'clock

* Backlight   : LED (**Red**)

* LCM technological conditions: **RoHS**

2. MECHANICAL SPECIFICATIONS

Item	Specification	Unit
Module Size	67.15(W) X 63.075(H) X 5.3(T)	mm
Viewing Area	61.0(W) X 31.4(H)	mm
Effective Display Area	57.57(W) X 28.77(H)	mm
Number of Dots	128 X 64 Dots	-
Dot Size	0.42(W) X 0.42(H)	mm
Dot Pitch	0.45(W) X 0.45(H)	mm

3. ELECTRICAL SPECIFICATIONS

3-1. Absolute Maximum Ratings (Vss=0V)

Item	Symbol	Standard Value			Unit
		Min.	Typ.	Max.	
Supply Voltage For Logic	V_{DD}	0.3	-	5.0	V
Supply Voltage For LCD Drive	V_0, V_{OUT}	0.3	-	18.0	V
Operating Temp.	T_{OP}	-20	-	+70	°C
Storage Temp.	T_{ST}	-30	-	+80	°C
Static Electricity	Be sue that you are ground when handing LCM				

3. ELECTRICAL SPECIFICATIONS (Continued)

3-2-1.Electrical Characteristics

Item		Symbol	Test Condition	Min.	Typ.	Max.	Unit
Supply Voltage For Logic		$V_{DD} - V_{SS}$	-	1.8	3.3	3.3	V
Supply Voltage For LCD		$V0-V_{ss}$	-	8.0	8.3	8.6	V
Input Voltage	"H" Level	V_{IH}	-	$0.8V_{DD}$	-	V_{DD}	V
	"L" Level	V_{IL}		V_{SS}	-	$0.2V_{DD}$	V
Output Voltage	"H" Level	V_{OH}	I_{OUT}= -0.5mA	$0.8V_{DD}$	-	V_{DD}	V
	"L" Level	V_{OL}	I_{OUT}= 0.5mA	V_{SS}	-	$0.2V_{DD}$	V
Current Consumption		I_{DD}	$V_{IN} = V_{DD}$	-	1.50	3.0	mA
AVG. X of 1931 C.I.E (LCM)		X	Dots All off	0.65	0.69	0.73	-
AVG. Y of 1931 C.I.E (LCM)		Y		0.26	0.30	0.34	-
Luminous Intensity (LCM)		L	Dots All on	20	-	40	cd/m^2
			Dots All off	0	5.0	10.0	cd/m^2

NOTE: 1) Duty ratio=1/65, Bias=1/9   2) Measured in Dots ON-state

3-3.BACKLIGHT

3-3-1. Absolute Maximum Ratings

Item	Symbol	Condition	Min.	Typ.	Max	Unit
Forward Current	IF	Ta= 25℃	-	-	140	mA
Reverse Voltage	VR		-	-	10	V
Power Dissipation	PD	Ta= 25℃	-	-	574	mW

3-3-2. Opto-electronic Characteristics

Item	Symbol	Condition	Min.	Typ.	Max	Unit
Forward Voltage	VF	IF= 70mA	3.7	3.9	4.1	V
Reverse current	IR	VR=10V	-	-	70	uA
Luminous intensity	LV	IF=70mA	**70**	-	-	cd/m²
Peakwavelength	λp	IF=70mA	620	-	630	nm
Emitting Color		IF=70mA	Red			

* The brightness is measured without LCD panel

MODEL	GY1206N0FVE6G	3/18	PRODUCT SPECIFICATIONS	REV: B

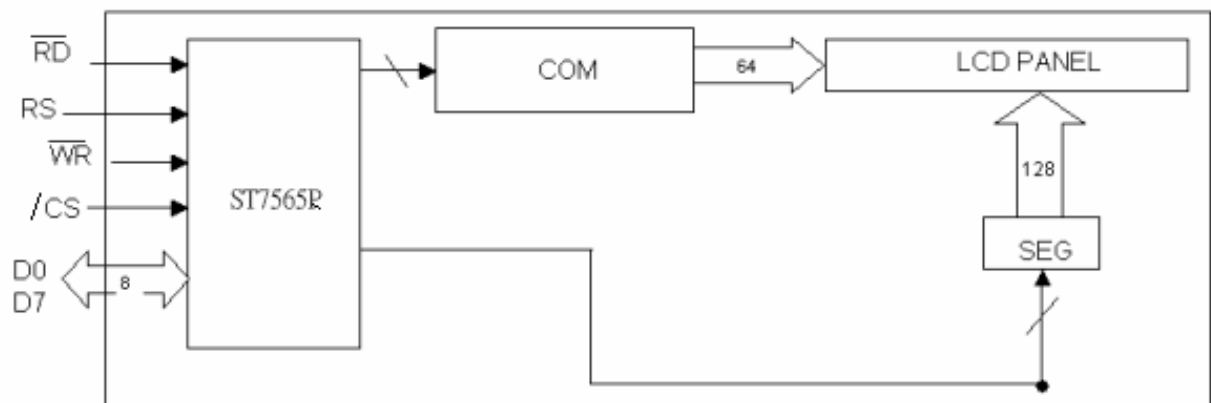# 4. POWER SUPPLY and BLOCK DIAGRMA

4-1 Power supply

(1) When the voltage regulator internal resistor is used.
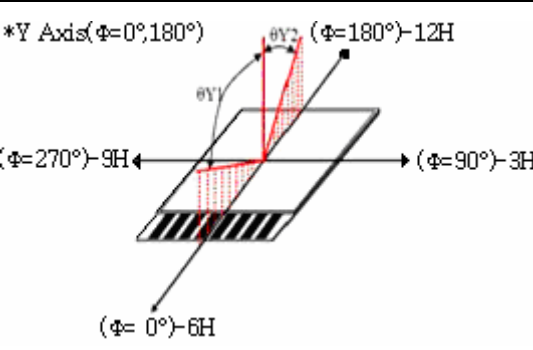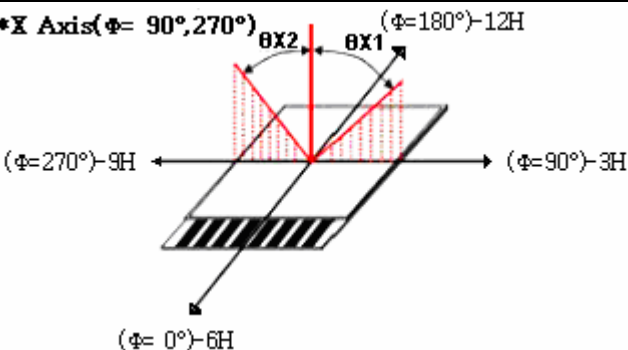(Example where $V_{DD}2 = V_{DD}$, with 4x step-up)
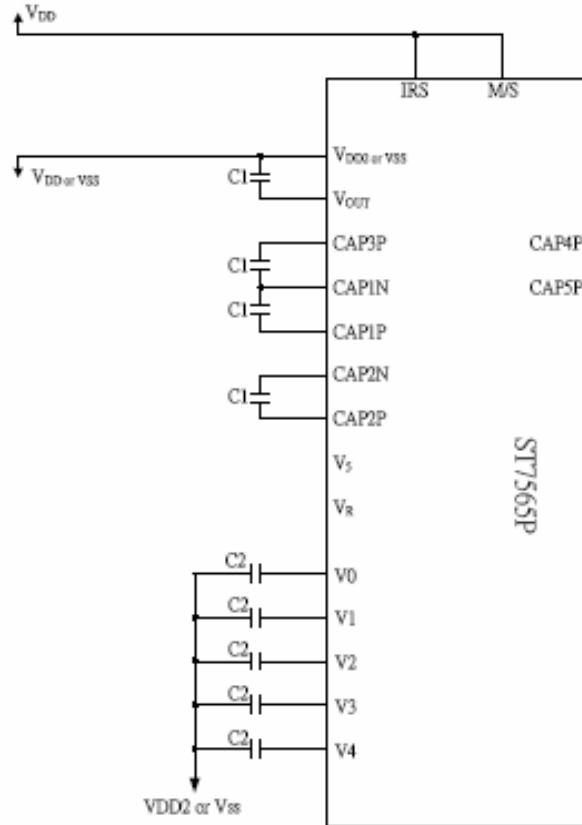
4-2 Block diagram
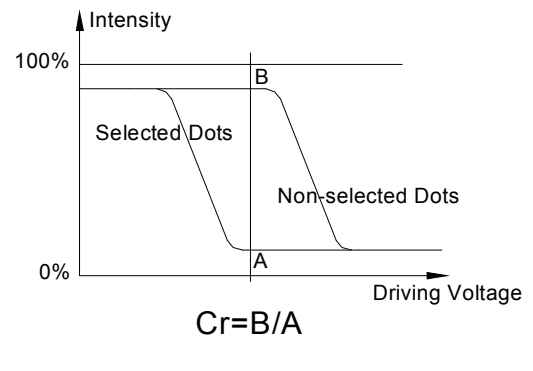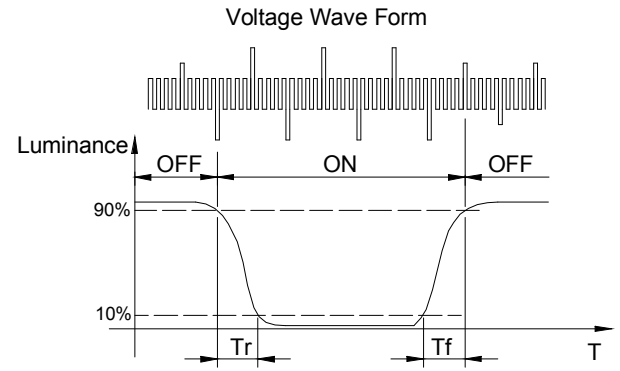
5. ELECTRO – OPTICAL CHARACTERISTICS

Item		Symbol	Temp.	Min.	Typ.	Max.	Unit	Conditions	Note
Viewing Angle Cr≥2	Φ=0°	Θ1	25℃	--	38	--	Deg.	-	1,2
	Φ=180°	Θ2		--	35	--			
	Φ=90°	Θ3		--	40	--			
	Φ=270°	Θ4		--	38	--			
Viewing Direction			6 O'clock						
Contrast Ratio		Cr	25℃	2.0	7.08	7.38	-	Θ = 0° Φ = 0°	3
Response Time(rise)		Tr	25℃	-	338	500	ms	Θ = 0° Φ = 0°	4
			0℃	-	1250	1650			
Response Time(fall)		Tf	25℃	-	218	500	ms	Θ = 0° Φ = 0°	4
			0℃	-	1250	1650			

Note1. Definition of Angle ΘY1&ΘY2	Note2. Definition of Viewing Angle ΘX1&ΘX2
Note3. Definition of Contrast Cr	Note4. Definition of Optical Response
Cr=B/A	

MODEL	GY1206N0FVE6G	5/18	PRODUCT SPECIFICATIONS	REV: B

6. INTERFACE PIN FUNCTION

Pin NO.	Symbol	I / O	Functions
1	/CS	I	This is the chip select signal.
2	/RES	I	When RES is set to "L", the setting are initialized.
3	A0	I	This is connect to the least significant bit of the Norman MPU address bus, and it determines whether the data bits are data or a command.
4	WR(/WR)	I	The data bus are latched at the rising edge of the WR signal
5	RD(/RD)	I	The data bus is in output status when this signal is "L"
6~13	D0~ D7	I/O	This is an 8-bit bi-directional data bus that connects to an 8-bit or 16-bit standard MPU data bus.
14	V_{DD}	Power supply	Power supply
15	V_{SS}	Power supply	Ground
16	V_{OUT}	O	DC/DC voltage converter. Connect a capacitor between this terminal and v_{ss} or VDD
17	NC	/	No connect
18	CAP3+	O	DC/DC voltage converter. Connect a capacitor between this terminal and the CAP1N terminal.
19	CAP1-	O	DC/DC voltage converter. Connect a capacitor between this terminal and the CAP1P terminal.
20	CAP1+	O	DC/DC voltage converter. Connect a capacitor between this terminal and the CAP1N terminal.
21	CAP2+	O	DC/DC voltage converter. Connect a capacitor between this terminal and the CAP2N terminal.
22	CAP2-	O	DC/DC voltage converter. Connect a capacitor between this terminal and the CAP2P terminal.
23~27	V4~ V0	Power supply	This is a multi-level power supply for the liquid crystal drive.
28	P/S	I	This pin configures the interface to parallel mode or serial mode. P/S="H":Parallel data input/output. P/S="L":Serial data input.

7. COMMAND LIST

Command	A0	/RD	/WR	D7	D6	D5	D4	D3	D2	D1	D0	Function
(1) Display ON/OFF	0	1	0	1	0	1	0	1	1	1	0 / 1	LCD display ON/OFF 0: OFF, 1: ON
(2) Display start line set	0	1	0	0	1	Display start address						Sets the display RAM display start line address
(3) Page address set	0	1	0	1	0	1	1	Page address				Sets the display RAM page address
(4) Column address set upper bit	0	1	0	0	0	0	1	Most significant column address				Sets the most significant 4 bits of the display RAM column address.
Column address set lower bit	0	1	0	0	0	0	0	Least significant column address				Sets the least significant 4 bits of the display RAM column address.
(5) Status read	0	0	1	Status		0	0	0	0			Reads the status data
(6) Display data write	1	1	0	Write data								Writes to the display RAM
(7) Display data read	1	0	1	Read data								Reads from the display RAM
(8) ADC select	0	1	0	1	0	1	0	0	0	0	0 / 1	Sets the display RAM address SEG output correspondence 0: normal, 1: reverse
(9) Display normal/reverse	0	1	0	1	0	1	0	0	1	1	0 / 1	Sets the LCD display normal/reverse 0: normal, 1: reverse
(10) Display all points ON/OFF	0	1	0	1	0	1	0	0	1	0	0 / 1	Display all points 0: normal display 1: all points ON
(11) LCD bias set	0	1	0	1	0	1	0	0	0	1	0 / 1	Sets the LCD drive voltage bias ratio 0: 1/9 bias, 1: 1/7 bias (ST7565P)
(12) Read/modify/write	0	1	0	1	1	1	0	0	0	0	0	Column address increment At write: +1 At read: 0
(13) End	0	1	0	1	1	1	0	1	1	1	0	Clear read/modify/write
(14) Reset	0	1	0	1	1	1	0	0	0	1	0	Internal reset
(15) Common output mode select	0	1	0	1	1	0	0	0 / 1	*	*	*	Select COM output scan direction 0: normal direction 1: reverse direction
(16) Power control set	0	1	0	0	0	1	0	1	Operating mode			Select internal power supply operating mode
(17) Vo voltage regulator internal resistor ratio set	0	1	0	0	0	1	0	0	Resistor ratio			Select internal resistor ratio(Rb/Ra) mode
(18) Electronic volume mode set	0	1	0	1	0	0	0	0	0	0	1	Set the Vo output voltage electronic volume register
Electronic volume register set				0	0	Electronic volume value						
(19) Static indicator ON/OFF	0	1	0	1	0	1	0	1	1	0	0 / 1	0: OFF, 1: ON
Static indicator register set				0	0	0	0	0	0	0	Mode	Set the flashing mode
(20) Booster ratio set	0	1	0	1	1	1	1	1	0	0	0	select booster ratio 00: 2x,3x,4x 01: 5x 11: 6x
				0	0	0	0	0	0	step-up value		
(21) Power saver												Display OFF and display all points ON compound command
(22) NOP	0	1	0	1	1	1	0	0	0	1	1	Command for non-operation
(23) Test	0	1	0	1	1	1	1	*	*	*	*	Command for IC test. Do not use this command

8.TIMING CHARACTERISTICS

8-1. 6800 Series MPU
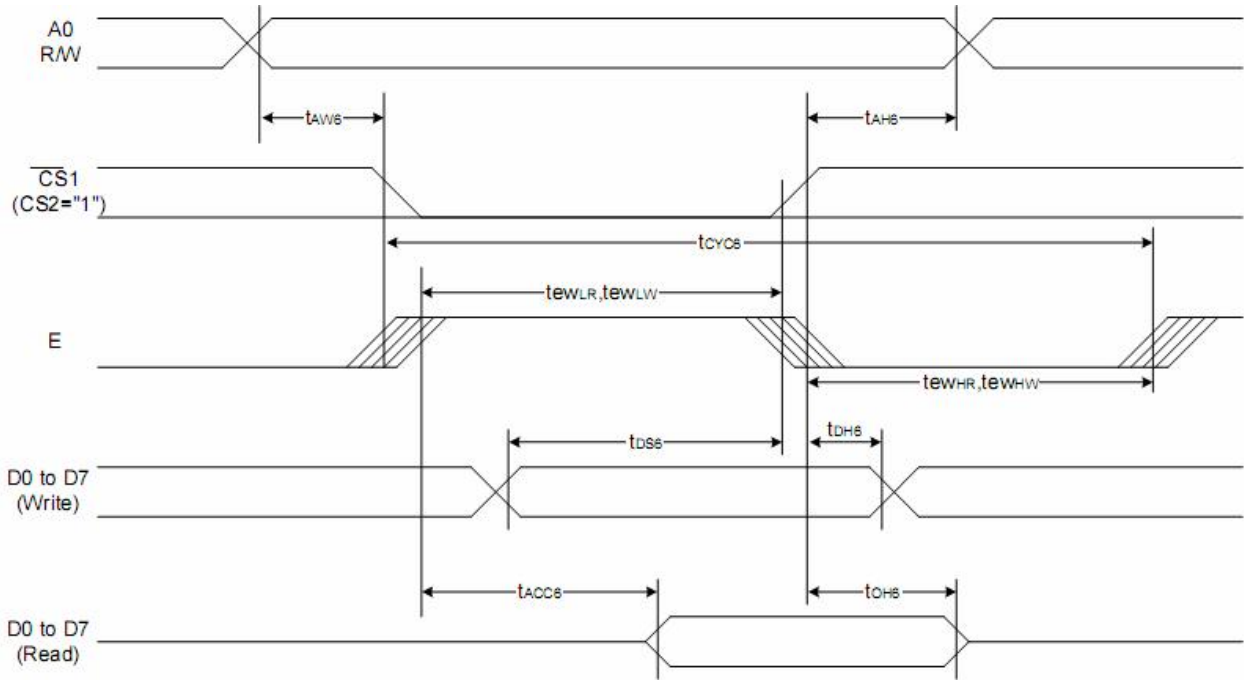
(V$_{DD}$ = 3.3V, Ta = −30 to 85°C)

Item	Signal	Symbol	Condition	Rating Min.	Rating Max.	Units
Address hold time	A0	t$_{AH6}$		0	—	ns
Address setup time		t$_{AW6}$		0	—	
System cycle time		t$_{CYC6}$		240	—	
Enable L pulse width (WRITE)	WR	t$_{EWLW}$		80	—	
Enable H pulse width (WRITE)		t$_{EWHW}$		80	—	
Enable L pulse width (READ)	RD	t$_{EWLR}$		80	—	
Enable H pulse width (READ)		t$_{EWHR}$		140		
WRITE Data setup time	D0 to D7	t$_{DS6}$		40	—	
WRITE Address hold time		t$_{DH6}$		0	—	
READ access time		t$_{ACC6}$	C$_L$ = 100 pF	—	70	
READ Output disable time		t$_{OH6}$	C$_L$ = 100 pF	5	50	

8.TIMING CHARACTERISTICS

8-2. 4-Line SPI Interface
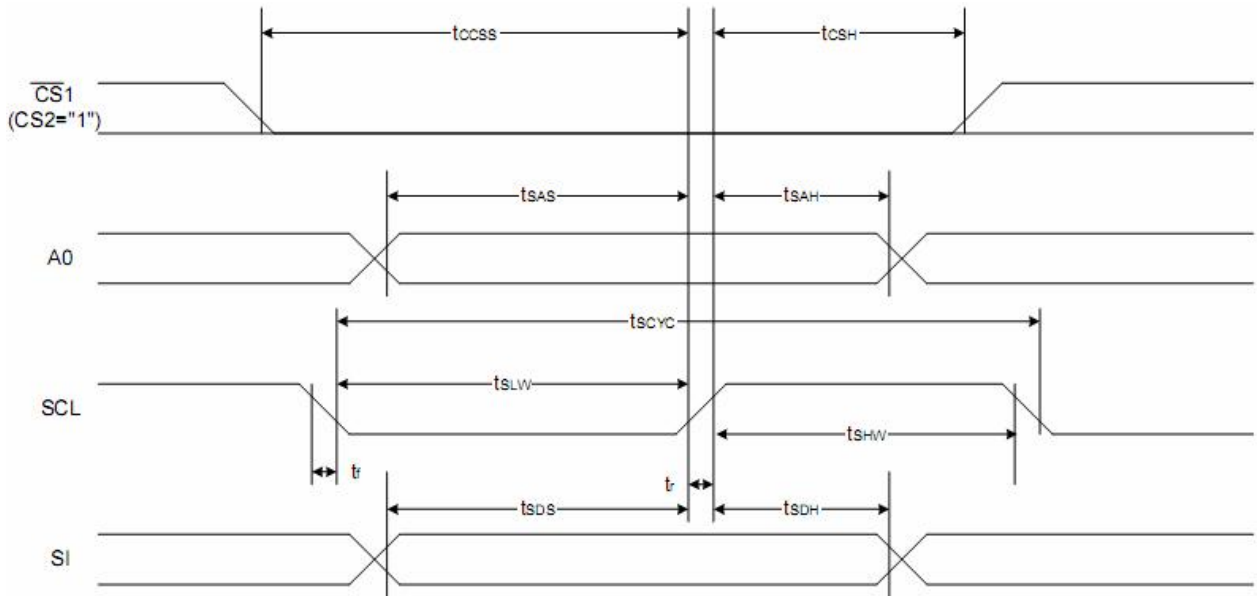
(V_DD = 3.3V,Ta = −30 to 85°C)

Item	Signal	Symbol	Condition	Rating Min.	Rating Max.	Units
4-line SPI Clock Period	SCL	T_{scyc}		50	—	ns
SCL "H" pulse width	SCL	T_{shw}		25	—	ns
SCL "L" pulse width	SCL	T_{SLW}		25	—	ns
Address setup time	A0	T_{SAS}		20	—	ns
Address hold time	A0	T_{sah}		10	—	ns
Data setup time	SI	T_{sds}		20	—	ns
Data hold time	SI	T_{SDH}		10	—	ns
CS-SCL time	CS	T_{css}		20	—	ns
CS-SCL time	CS	T_{csh}		40	—	ns

9. QUALITY SPECIFICATION

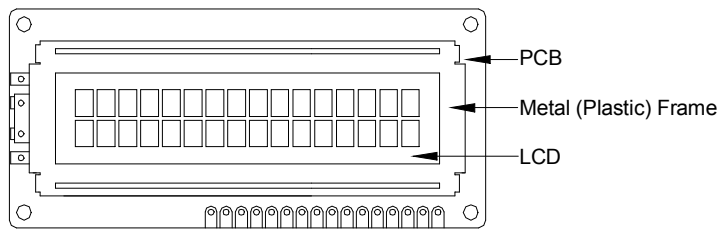9 - 1. LCM Appearance and Electric inspection Condition

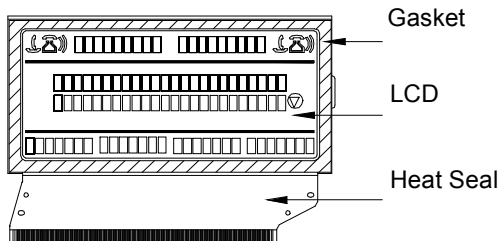1. Inspection will be done by placing LCM 30cm away from inspector's eyeballs under normal illumination.
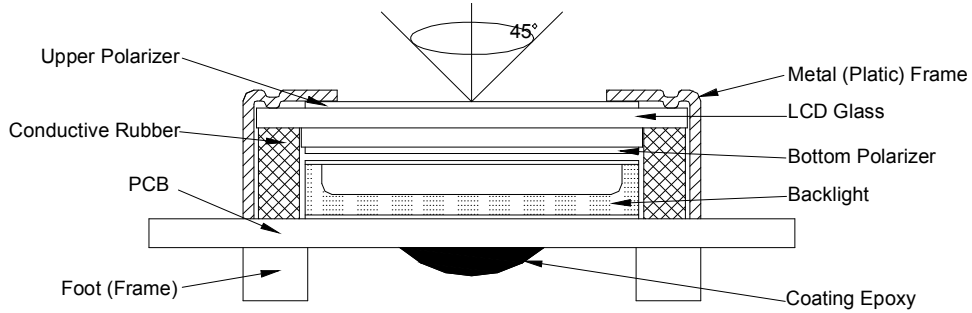
2. View Angle: with in 45° around perpendicular line.
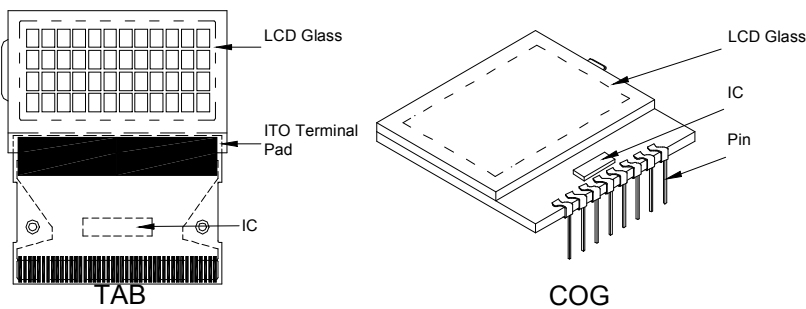
9 - 2. Definition

1. COB

2. Heat Seal

3. TAB and COG

TAB COG

MODEL	GY1206N0FVE6G	10/18	PRODUCT SPECIFICATIONS	REV: B

9. QUALITY SPECIFICATION (Continued)

9-3. Sampling Plan and Acceptance

1. Sampling Plan

MIL - STD - 105E (‖) ordinary single inspection is used.
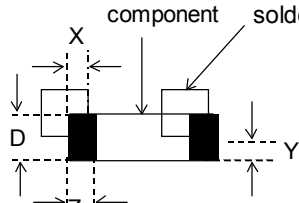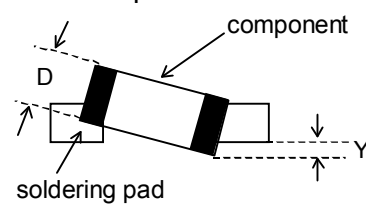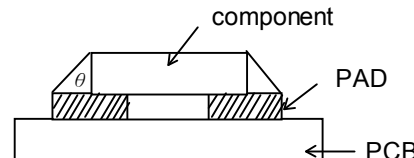
2. Acceptance

Major defect: AQL = 0.25

Minor defect: AQL = 0.65

9-4. Criteria

1. COB

Defect	Inspection Item	Inspection Standards	
Major	PCB copper flakes peeling off	Any copper flake in viewing Area should be greater than $1.0mm^2$	Reject
Major	Height of coating epoxy	Exceed the dimension of drawing	Reject
Major	Void or hole of coating epoxy	Expose bonding wire or IC	Reject
Major	PCB cutting defect	Exceed the dimension of drawing	Reject

2. SMT

Defect	Inspection Item	Inspection Standards	
Minor	Component marking not readable		Reject
Minor	Component height	Exceed the dimension Of drawing	Reject
Major	Component solder defect (missing , extra, wrong component or wrong orientation		Reject
Minor	Component position shift	$X < 3/4Z$ $Y > 1/3D$	Reject Reject
Minor	Component tilt	$Y > 1/3D$	Reject
Minor	Insufficient solder	$\theta \le 20°$	Reject

MODEL	GY1206N0FVE6G	11/18	PRODUCT SPECIFICATIONS	REV: B

9-4. Criteria (Continued)

3. Metal (Plastic) Frame

Defect	Inspection Item	Inspection Standards		
Major	Crack / breakage	Anywhere		Reject
Minor	Frame Scratch	W	L	Acceptable of Scratch
		w<0.1mm	Any	Ignore
		$0.1 \leq w < 0.2mm$	$L \leq 5.0mm$	2
		$0.2 \leq w < 0.3mm$	$L \leq 3.0mm$	1
		$w \geq 0.3mm$	Any	0
		Note : 1. Above criteria applicable to scratch lines with distance greater than 5mm. 2. Scratch on the back side of frame (not visible) can be ignored .		
Minor	Frame Dent , Prick $\Phi = \dfrac{L + W}{2}$			Acceptable of Dents / Pricks
		$\Phi \leq 1.0mm$		2
		$1.0 < \Phi \leq 1.5mm$		1
		$1.5mm < \Phi$		0
		Note : 1. Above criteria applicable to any two dents / pricks with distance greater than 5mm 2. Dent / prick on the back side of frame (not visible) can be ignored		
Minor	Frame Deformation	Exceed the dimension of drawing		
Minor	Metal Frame Oxidation	Any rust		

4. Flexible Film Connector (FFC)

Defect	Inspection Item		Inspection Standards	
Minor	Tilted soldering		Within the angle +5°	Acceptable
Minor	Uneven solder joint /bump			Reject
Minor	Hole	$\Phi = \dfrac{L + W}{2}$	Expose the conductive line	Reject
			$\Phi > 1.0mm$	Reject
Minor	Position shift		$Y > 1/3D$	Reject
			$X > 1/2Z$	Reject

9. QUALITY SPECIFICATION (Continued)

9-4. Criteria (Continued)

5. Screw

Defect	Inspection Item	Inspection Standards	
Major	Screw missing/loosen		Reject
Minor	Screw oxidation	Any rust	Reject
Minor	Screw deformation	Difficult to accept screw driver	Reject

6. Heat seal 、TCP 、FPC

Defect	Inspection Item	Inspection Standards	
Major	Scratch expose conductive layer		Reject
Minor	HS Hole $\Phi = \dfrac{L + W}{2}$	$\Phi > 0.5mm$	Reject
Major	Adhesion strength	Less than the specification	Reject
Minor	Position shift	$Y > 1/3D$	Reject
Minor		$X > 1/2Z$	Reject
Major	Conductive line break		Reject

7. LED Backing Protective Film and Others

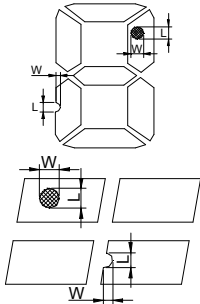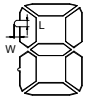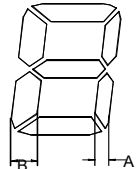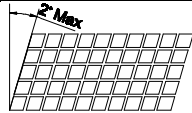| Defect	Inspection Item	Inspection Standards	
Minor	LED dirty, prick	Acceptable number of units	
		$\Phi \leq 0.10mm$	Ignore
		$0.10 < \Phi \leq 0.15mm$	2
		$0.15 < \Phi \leq 0.2mm$	1
		$\Phi > 0.2mm$	0
		The distance between any two spots should be $\geq 5mm$ Any spot/dot/void outside of viewing area is acceptable	
Minor	Protective film tilt	Not fully cover LCD	Reject
Major	COG coating	Not fully cover ITO circuit	Reject

8. Electric Inspection

Defect	Inspection Item	Inspection Standards	
Major	Short		Reject
Major	Open		Reject

9. Inspection Specification of LCD

Defect	Inspect Item		Inspection Standards			
Minor	Linear Defect	∗ Glass Scratch ∗ Polarizer Scratch ∗ Fiber and Linear material	W	W≤0.03	0.03<W≤0.05	W>0.05
			L	L<5	L<3	Any
			ACC. NO.	1	1	Reject
			Note	L is the length and W is the width of the defect		
Minor	Black Spot and Polarizer Pricked	∗ Foreign material between glass and polarizer or glass and glass ∗ Polarizer hole or protuberance by external force	Φ	Φ≤0.1 / 0.1<Φ≤0.15 / 0.15<Φ≤0.2		Φ>0.2
			ACC. NO.	3EA / 100mm² / 2 / 1		0
			Note	Φ is the average diameter of the defect. Distance between two defects > 10mm.		
Minor	White Spot and Bubble in polarizer	∗ Unobvious transparant foreign material between glass and glass or glass and polarizer ∗ Air protuberance between polarizer and glass	Φ	Φ≤0.3	0.3<Φ≤0.5	0.5<Φ
			ACC. NO.	3EA / 100mm²	1	0
			Note	Φ is the average diameter of the defect. Distance between two defects > 10mm.		

Defect	Inspect Item		Inspection Standards			
Minor	Segment Defect		Φ	Φ≤0.10	0.10<Φ≤0.20 / 0.20<Φ≤0.25	Φ>0.25
			ACC. NO.	3EA / 100mm²	2 / 1	0
			Note	W is more than 1/2 segment width → Reject; $\Phi = \dfrac{L + W}{2}$; Distance between two defect is 10mm		
Minor	Protuberant Segment	$\Phi = (L + W)/2$	Φ	Φ≤0.10	0.10<Φ≤0.20 / 0.20<Φ≤0.25	Φ>0.25
			W	Glue	W≤1/2 Seg W≤0.2 / W≤1/2 Seg W≤0.2	Ignore
			ACC. NO.	3EA / 100mm²	2 / 1	0
Minor	Assembly Mis-alignment		1. Segment			
			B	B≤0.4mm	0.4<B≤1.0mm	B>1.0mm
			B-A	B-A<1/2B	B-A<0.2	B-A<0.25
			Judge	Acceptable	Acceptable	Acceptable
			2. Dot Matrix			
			Deformation>2°			Reject
Minor	Stain on LCD Panel Surface		Accept when stains can be wiped lightly with a soft cloth or a similar one. Otherwise, judged according to the above items: "Black spot" and "White Spot"			

10. RELIABILITY

NO.	Item	Condition	Criterion
1	High Temperature Operating	70℃, 96Hrs	No defect in cosmetic and operational function allowable. Total current Consumption should be below double of initial value.
2	Low Temperature Operating	-20℃, 96Hrs	
3	High Humidity	60℃, 90%RH, 96Hrs	
4	High Temperature Storage	80℃, 96Hrs	
5	Low Temperature Storage	-30℃, 96Hrs	
6	Vibration	Random wave 10 ~ 100Hz Acceleration: 2G 60 Minute	
7	Thermal Shock	-10℃ to 25℃ to 50℃ (60Min) (15Min) (60Min) 10Cycles	
8	ESD Testing	Contract Discharge Voltage: +1 ~ 5kV and –1 ~ –5kV	There will be discharged ten times at every discharging voltage cycle. The voltage gap is 1kV.
		Air Discharge Voltage: +1 ~ 8kV and –1 ~ -8kV	

Note: 1) Above conditions are suitable for GOLDENTEK standard products.

2) For restrict products, the test conditions listed as above must be revised.

11. HANDLING PRECAUTION

(1) Mounting Method

The panel of the LCD Module consists of two thin glass plates with polarizers
which easily get damaged since the Module is fixed by utilizing fitting holes
in the printed circuit board. Extreme care should be taken when handling
the LCD Modules.

(2) Caution of LCD handling & cleaning

When cleaning the display surface, use soft cloth with solvent (recommended
below) and wipe lightly.

- Isopropyl alcohol

- Ethyl alcohol

- Trichlorotrifloroethane

Do not wipe the display surface with dry or hard materials that will damage the
polarizer surface.

Do not use the following solvent:

- Water

- Ketone

- Aromatics

(3) Caution against static charge

The LCD Module use C-MOS LSI drivers, so we recommend that you connect
any unused input terminal to VDD or VSS, do not input any signals before power
is turned on. And ground your body, Work/assembly table. And assembly
equipment to protect against static electricity.

(4) Packaging

-    Modules use LCD elements, and must be treated as such. Avoid intense shock
and falls from a height.

- To prevent modules from degradation. Do not operate or store them exposed
directly to sunshine or high temperature/humidity.

(5) Caution for operation

- It is indispensable to drive LCD's within the specified voltage limit since the higher
voltage than the limit shorten LCD life. An electrochemical reaction due to direct
current causes LCD deterioration, Avoid the use of direct current drive.

11. HANDLING PRECAUTION (Continued)

   - Response time will be extremely delayed at lower temperature than the operating
     temperature range and on the other hand at higher temperature LCD's show dark
     color in them.

   However those phenomena do not mean malfunction or out of order with LCD's.
   Which will come back in the specified operating temperature range.

   - If the display area is pushed hard during operation, some font will be abnormally
     displayed but it resumes normal condition after turning off once.

   - A slight dew depositing on terminals is a cause for electro-chemical reaction
     resulting in terminal open circuit.

   Usage under the relative condition of 40°C, 80%RH or less is required.

(6) Storage

   In the case of storing for a long period of time (for instance ,for years) for the
   purpose or replacement use, The following ways are recommended.

   - Storage in a polyethylene bag with sealed so as not to enter fresh air outside in it,
     And with no desiccant.

   - Placing in a dark place where neither exposure to direct sunlight nor light is.
     Keeping temperature in the specified storage temperature range.

   - Storing with no touch on polarizer surface by the anything else. (It is
     recommended to store them as they have been contained in the inner container at
     the time of delivery)

(7) Safety

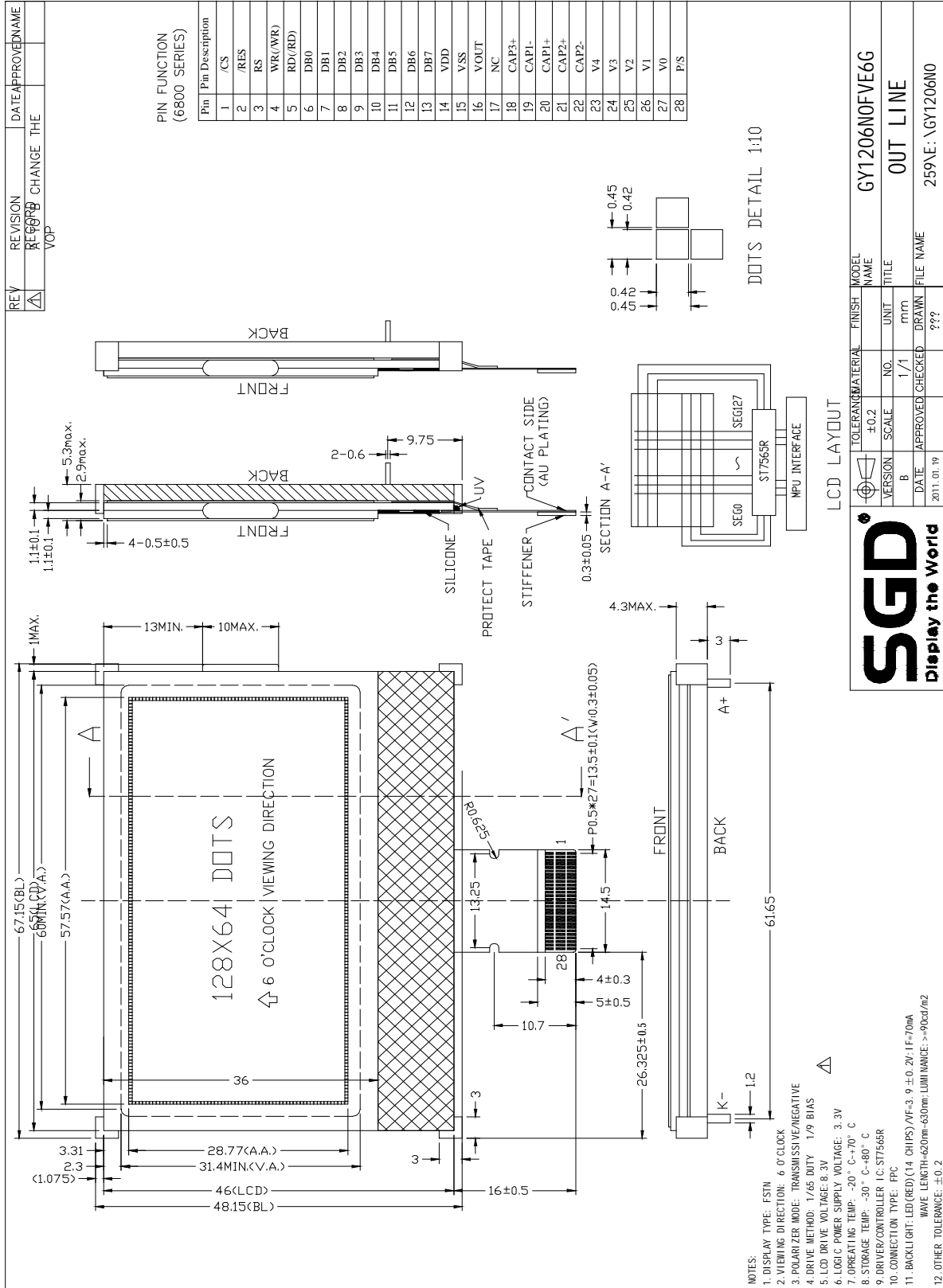   - It is recommendable to crash damaged or unnecessary LCD into pieces and wash
     off liquid crystal by using solvents such as acetone and ethanol.

   Which should be burned up later.

   - When any liquid crystal leaked out of a damaged glass cell comes in contact with
     your hands, please wash it off well with soap and water.

| MODEL | GY1206N0FVE6G | 17/18 | PRODUCT SPECIFICATIONS | REV: B |

12. OUTLINE DIMENTION

PIN FUNCTION (6800 SERIES)

Pin	Pin Description
1	/CS
2	/RES
3	RS
4	WR(/WR)
5	RD(/RD)
6	DB0
7	DB1
8	DB2
9	DB3
10	DB4
11	DB5
12	DB6
13	DB7
14	VDD
15	VSS
16	VOUT
17	NC
18	CAP3+
19	CAP1-
20	CAP1+
21	CAP2+
22	CAP2-
23	V4
24	V3
25	V2
26	V1
27	V0
28	P/S

REV REVISION DATE APPROVE NAME

RECORD CHANGE THE VOP

DOTS DETAIL 1:10
0.45
0.42
0.42
0.45

BACK
FRONT

5.3max.
2.9max.
1.1±0.1
1.1±0.1
4-0.5±0.5

BACK
FRONT
9.75
2-0.6
UV
SILICONE
PROTECT TAPE
STIFFENER
CONTACT SIDE (AU PLATING)
SECTION A-A'
0.3±0.05

LCD LAYOUT
SEG0 ~ SEG127
ST7565R
MPU INTERFACE

4.3MAX.
3
A+
FRONT
BACK
61.65
K-
1.2

1MAX.
13MIN.
10MAX.
A
A'
128X64 DOTS
↑ 6 O'CLOCK VIEWING DIRECTION
67.15(BL)
66MIN.(V.A.)
57.57(A.A.)
36
R0.625
13.25
14.5
1
28
P0.5*27=(13.5±0.1(w/0.3±0.05)
4±0.3
5±0.5
10.7
26.325±0.5
3

3.31
2.3
(1.075)
28.77(A.A.)
31.4MIN.(V.A.)
46(LCD)
48.15(BL)
3
16±0.5

NOTES:
1. DISPLAY TYPE: FSTN
2. VIEWING DIRECTION: 6 O'CLOCK
3. POLARIZER MODE: TRANSMISSIVE/NEGATIVE
4. DRIVE METHOD: 1/65 DUTY 1/9 BIAS
5. LCD DRIVE VOLTAGE: 8.3V
6. LOGIC POWER SUPPLY VOLTAGE: 3.3V
7. OPERATING TEMP: -20°C~+70°C
8. STORAGE TEMP: -30°C~+80°C
9. DRIVER/CONTROLLER IC: ST7565R
10. CONNECTION TYPE: FPC
11. BACKLIGHT: LED(RED)(14 CHIPS)/VF=3.9±0.2V;IF=70mA
    WAVE LENGTH=620nm~630nm;LUMINANCE:>>90cd/m2
12. OTHER TOLERANCE: ±0.2

GY1206N0FVE6G
MODEL NAME
OUT LINE
TITLE
259\E:\GY1206N0
FILE NAME
TOLERANCE ±0.2
MATERIAL
FINISH
MODEL NAME
UNIT mm
SCALE 1/1
NO.
DRAWN ???
APPROVED CHECKED
VERSION B
DATE 2011.01.19

MODEL	GY1206N0FVE6G	18/18	PRODUCT SPECIFICATIONS	REV: B

SGD
Display the World
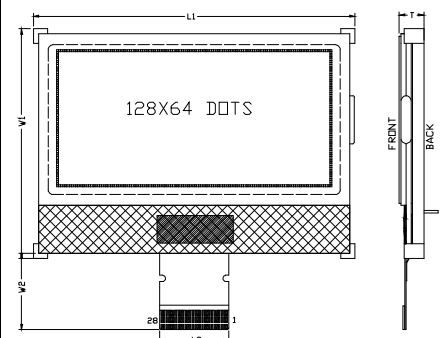
SAMPLE OUTGOING INSPECTION REPORT (LCM)

Data: 2011/04/26   NO. : QAB04017

Customer	Product NO.	Driving Voltage	Testing Condition	Quantity
高雄富相	GY1206N0FVE6G	VOP: 8.3V	25℃	8Pcs

Inspection Result

Items	Specification				
Display Mode	○ W / B Mode	● B / W Mode	○ Yellow Mode	○ Blue Mode	○ Gray Mode
Polarizer Type	○ Reflective		○ Transflective		● Transmissive
Viewing direction	○ 3 O'clock	● 6 O'clock		○ 9 O'clock	○ 12 O'clock

Electrical / Appearance

Item	Inspection Method	Specification	Inspection Result	
Appearance	Spot Gauge Caliper	Final Inspection Criteria	● OK	○ NG
Electrical	LCM Tester	Product Specification	● OK	○ NG
Pattern	LCM Tester	Drawing	● OK	○ NG

Dimension / Supply Current

Item	Spec.(mm)	NO.1	NO.2	NO.3	NO.4	NO.5	Result		Fig.
L1	67.15±0.2	67.08	67.11	67.13	67.10	67.07	● OK	○ NG	
L2	14.5±0.3	14.51	14.59	14.50	14.53	14.48	● OK	○ NG	
W1	48.15±0.2	48.17	48.12	48.20	48.19	48.18	● OK	○ NG	128X64 DOTS
W2	16.0±0.5	16.13	16.17	16.22	16.25	16.15	● OK	○ NG	
T	5.3mm Max	5.01	4.99	4.91	5.03	4.93	● OK	○ NG	
IDD	3.0mA Max	1.50	1.50	1.50	1.50	1.50	● OK	○ NG	

Designed	ELIN	Checked	/	Approved	Wallace

Doc. NO.: F10018A   .