

ZETTLER DISPLAYS

XIAMEN ZETTLER ELECTRONICS CO., LTD.

SPECIFICATIONS FOR LIQUID CRYSTAL DISPLAY

CUSTOMER APPROVAL			
※ PART NO.: <u>AQM1248A-RN-FBW-P(ZETTLER DISPLAYS) VER2.2</u>			
APPROVAL		COMPANY CHOP	
CUSTOMER COMMENTS			

DISPLAYTRONIC ENGINEERING APPROVAL		
DESIGN BY	CHECKED BY	APPROVED BY
WJQ	LH	GZH

REVISION RECORD

REVISION	REVISION DATE	PAGE	CONTENTS
VER1.0	2008-06-27		FIRST ISSUE
VER2.0	2008-07-29		CORRECT PIN DESCRIPTION
VER2.1	2014-10-17		ADD PATTERN LAYOUT
VER2.2	2014-10-20	P5	ADD PATTERN LAYOUT FOR SOLDERING

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1.0 GENERAL SPECS

1. Overall Module Size (W*H*T)	34.8*20.3*10.0(MAX,include pin length)mm
2 Viewing Area (W*H)	32*11mm
2. Dot Size(W*H)	0.20*0.17mm
3. Dot Pitch(W*H)	0.22*0.19mm
4. Driving Method	1/49Duty, 1/6Bias
5. Controller IC	ST7565R
6. LCD Type	FSTN,Positive ,Reflective
7. Viewing Direction	6 O'Clock
8. Backlight	NO
9. Operating Temperature	Wide (-10°C ~ 50°C)
10. Rohs	Conforms

2.0 ABSOLUTE MAXIMUM RATINGS

Item	Symbol	Min	Typ	Max	Unit
Operating temperature	Top	-10	-	50	°C
Storage temperature	Tst	-20	-	60	°C
Input voltage	Vin	Vss		Vdd	V
Supply voltage for logic	Vdd- Vss	-	3.0	-	V
Supply voltage for LCD drive	Vdd- Vo	-	7.5	-	V

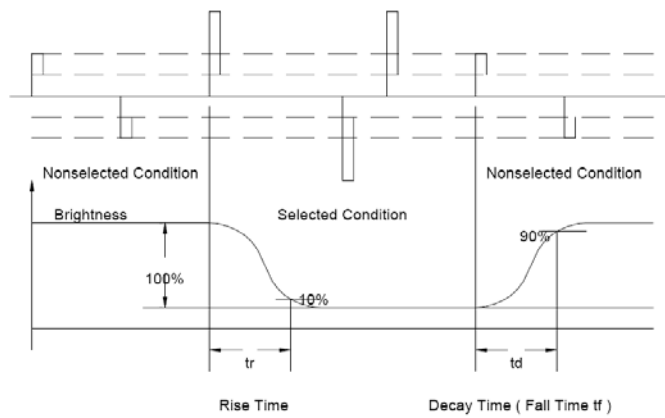
3.0 ELECTRICAL CHARACTERISTICS

Item	Symbol	Condition	Min	Typ	Max	Unit
Power Supply Voltage	VDD	Ta=25°C	2.4	3.0	3.6	V
Power Supply Current	Idd	Vdd=3.0V	-	-	1.5	mA
Input voltage (high)	Vih	H level	0.8Vdd	-	Vdd	V
Input voltage (low)	Vil	L level	Vss	-	0.2Vdd	V
Recommended LC Driving Voltage	Vdd -Vo	-20°C	-	-	-	V
		25°C	-	7.5	-	
		70°C	-	-	-	

4.0 OPTICAL CHARACTERISTICS (Ta=25°C, Vdd= 3.0V±0.25V, FSTN LC fluid)

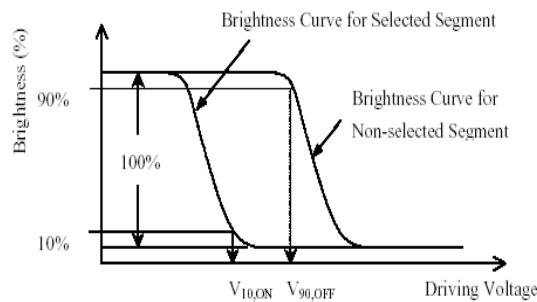
Item	Symbol	Condition	Min	Typ	Max	Unit
Viewing angle	$\theta=90^\circ$	$Cr \geq 2.0$	35	45	-	deg
	$\theta=270^\circ$		35	45	-	
	$\theta=0^\circ$		40	50	-	
	$\theta=180^\circ$		15	25	-	
Contrast Ratio	Cr	$\phi=0^\circ$	-	7	-	
Response time (rise)	Tr	$\theta=0^\circ$	-	100	150	ms
Response time (fall)	Tf	VLCD=7.5V	-	200	300	ms

(1). Definition of Optical Response Time

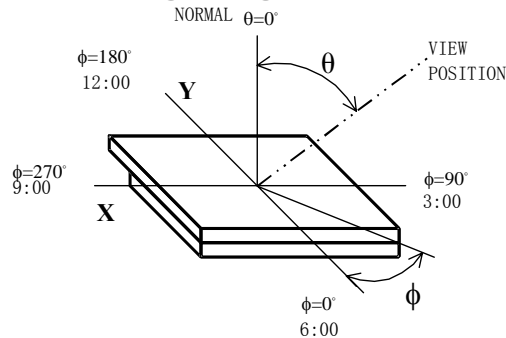


(2). Definition of Driving Voltage (Vlcd)

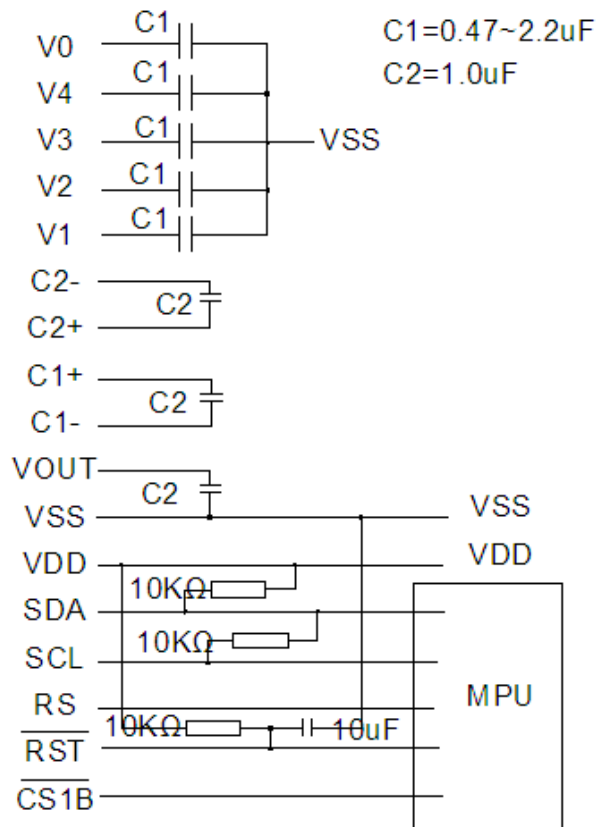
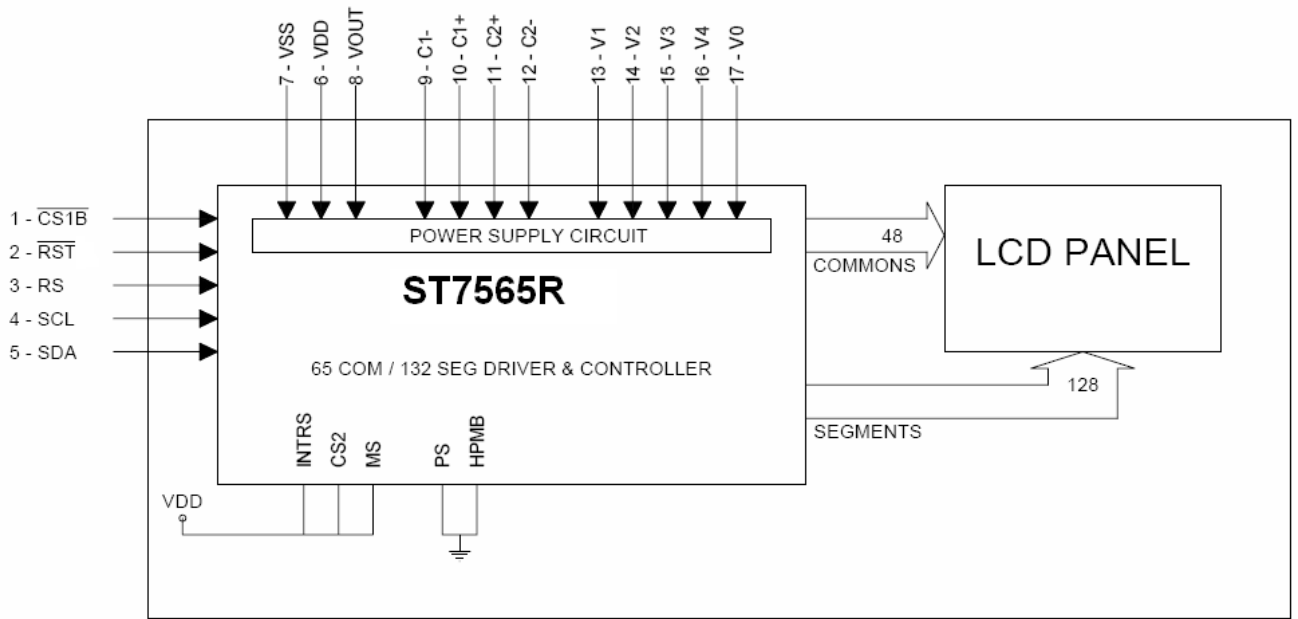
$$V_{lcd} = (V_{10,ON} + V_{90,OFF}) / 2$$



(3). Definition of Viewing Angle θ and Φ



5.0 BLOCK DIAGRAM

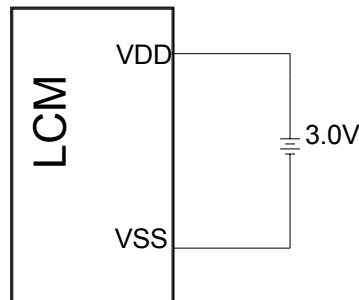


PATTERN LAYOUT FOR SOLDERING

6.0 PIN ASSIGNMENT

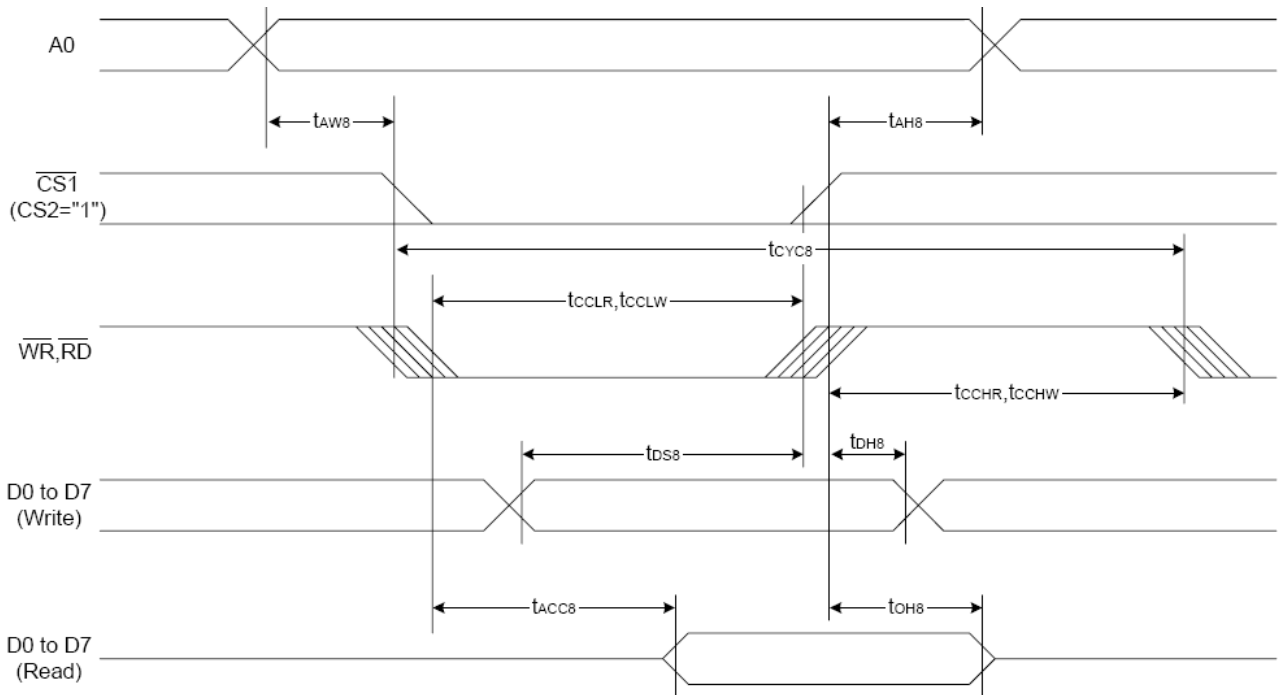
PIN	SYMBOL	I/O	FUNCTION
1	$\overline{\text{CS1B}}$	I	Chip select input pin Data / instruction I/O is enabled only when CS1B is "L", When chip select is non-active, DB0 to DB7 may be high impedance.
2	$\overline{\text{RST}}$	I	Reset input pin When RST is "L", initialization is executed.
3	RS	I	Register select input pin RS = "H": DB0 to DB7 are display data RS = "L": DB0 to DB7 are Instruction
4	SCL	I/O	Serial interface selected (PS ="L"); DB6: serial input clock (SCLK) DB7: serial input data (SID) When chip select is not active, DB6 and DB7 may be high impedance.
5	SDA		
6	VDD	Supply	Power supply
7	VSS	Supply	Ground
8	VOUT	I/O	Voltage converter input/output pin.
9	C1-	O	Capacitor 1 negative connection pin for voltage converter.
10	C1+	O	Capacitor 1 positive connection pin for voltage converter.
11	C2+	O	Capacitor 2 positive connection pin for voltage converter.
12	C2-	O	Capacitor 2 negative connection pin for voltage converter.
13	V1	I/O	LCD driver supply voltages. The voltage determined by LCD pixel is impedance-converted by an operational amplifier for application. Voltages should have the following relationship: $V0 \geq V1 \geq V2 \geq V3 \geq V4 \geq VSS$
14	V2		
15	V3		
16	V4		
17	V0		

7.0 POWER SUPPLY



8.0 TIMING CHARACTERISTICS

ST7565P Serial Interface Characteristics:



(VDD = 3.3V, Ta = -30 to 85°C)

Item	Signal	Symbol	Condition	Rating		Units
				Min.	Max.	
Address hold time	A0	tAH8		0	—	Ns
Address setup time		tAW8		0	—	
System cycle time		tCYC8		240	—	
Enable L pulse width (WRITE)	WR	tCCLW		80	—	
Enable H pulse width (WRITE)		tCCHW		80	—	
Enable L pulse width (READ)	RD	tCCLR		140	—	
Enable H pulse width (READ)		tCCHR		80	—	
WRITE Data setup time	D0 to D7	tDS8		40	—	
WRITE Address hold time		tDH8		0	—	
READ access time		tACC8	CL = 100 pF	—	70	
READ Output disable time		tOH8	CL = 100 pF	5	50	

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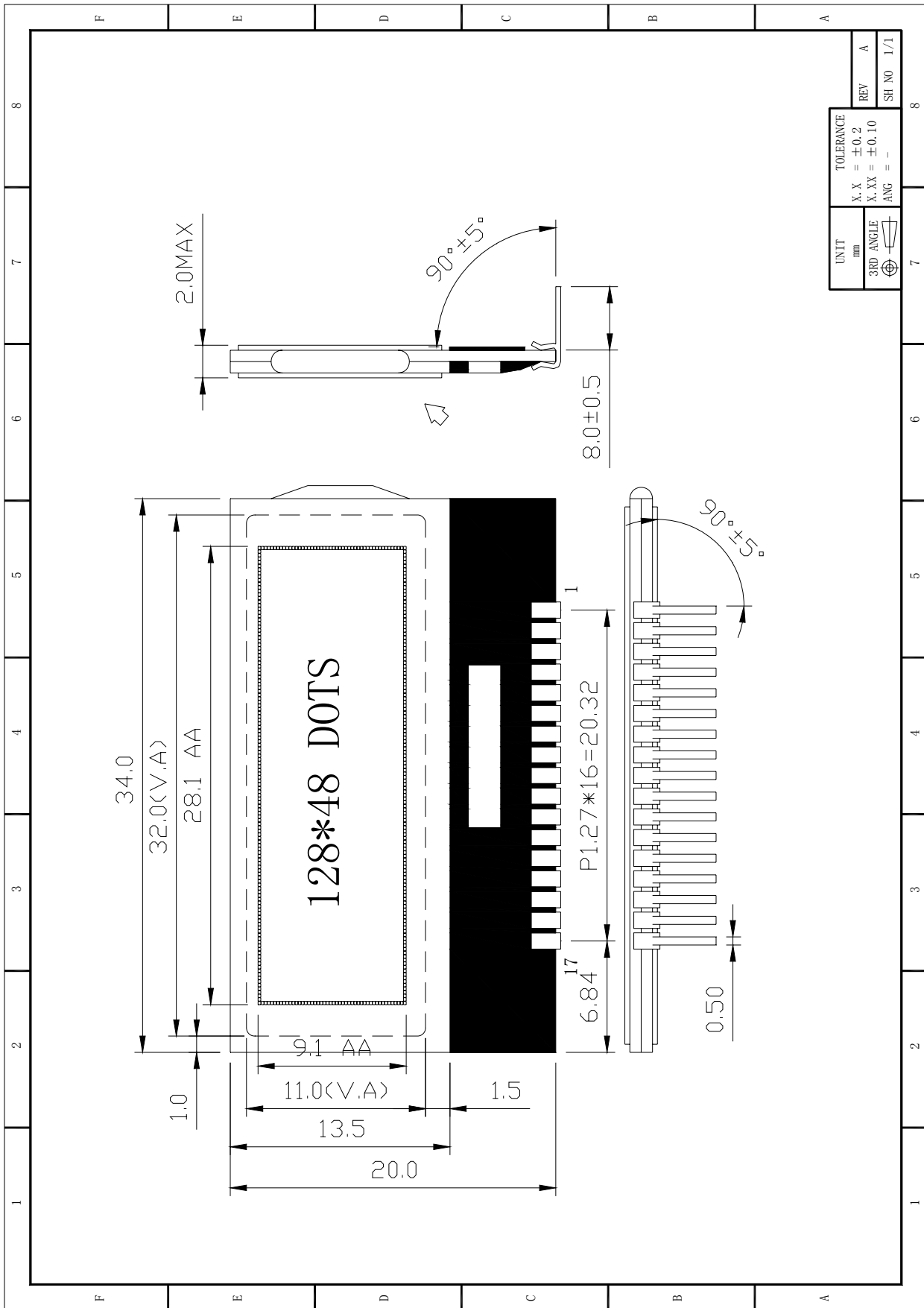
(V_{DD} = 2.7V, Ta = -30 to 85°C)

Item	Signal	Symbol	Condition	Rating		Units
				Min.	Max.	
Address hold time	A0	t _{AH8}		0	—	ns
Address setup time		t _{AW8}		0	—	
System cycle time		t _{CYC8}		400	—	
Enable L pulse width (WRITE)	WR	t _{CCLW}		220	—	
Enable H pulse width (WRITE)		t _{CCHW}		180	—	
Enable L pulse width (READ)	RD	t _{CCLR}		220	—	
Enable H pulse width (READ)		t _{CCHR}		180	—	
WRITE Data setup time	D0 to D7	t _{DS8}		40	—	
WRITE Address hold time		t _{DH8}		0	—	
READ access time		t _{ACC8}	C _L = 100 pF	—	140	
READ Output disable time		t _{OH8}	C _L = 100 pF	10	100	

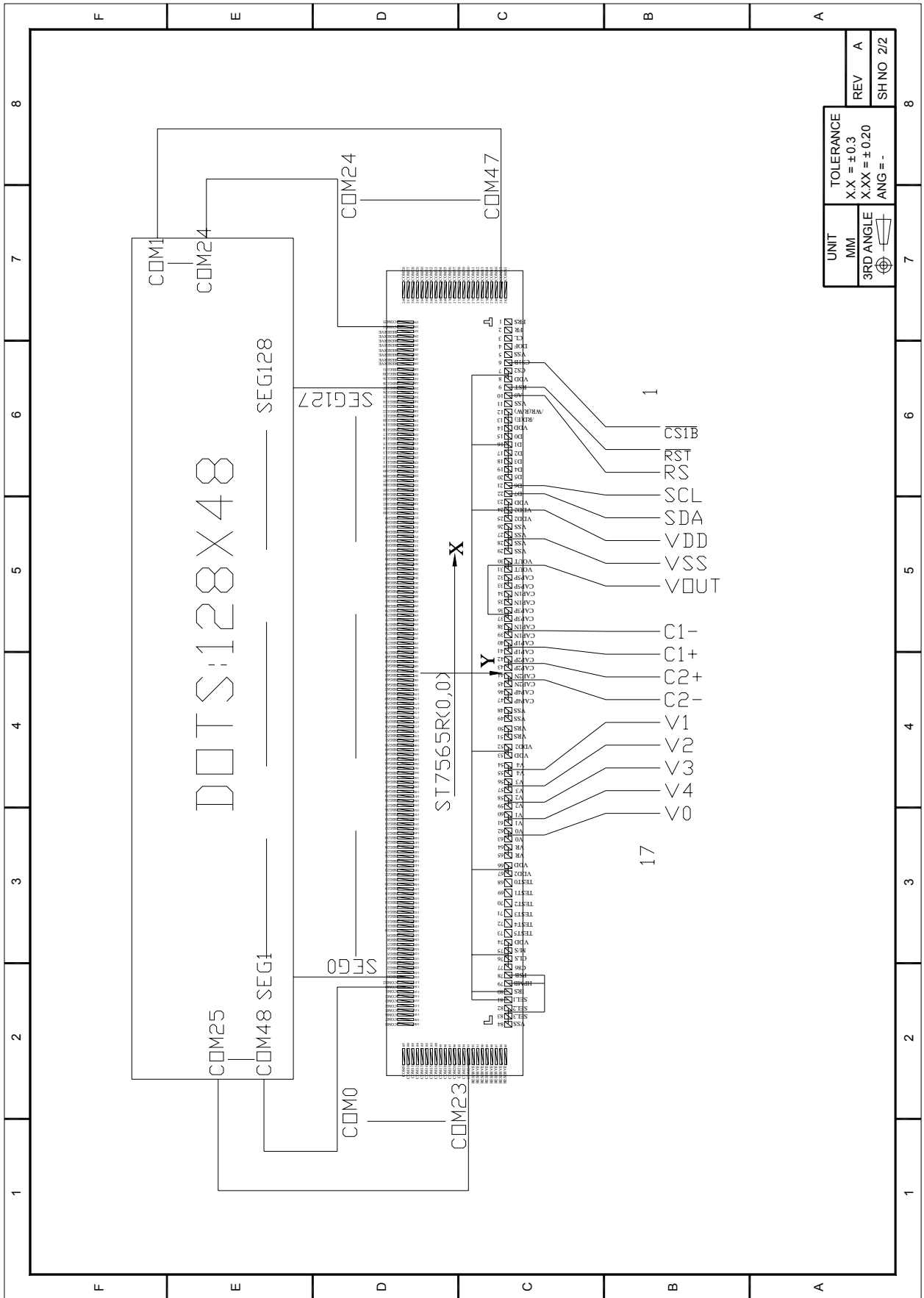
9.0 COMMAND SETTING DESCRIPTION (Refer to the datasheet of ST7565R)

Command	Command Code										Function		
	A0	/RD	/WR	D7	D6	D5	D4	D3	D2	D1		D0	
(1) Display ON/OFF	0	1	0	1	0	1	0	1	1	1	0	1	LCD display ON/OFF 0: OFF, 1: ON
(2) Display start line set	0	1	0	0	1	Display start address						1	Sets the display RAM display start line address
(3) Page address set	0	1	0	1	0	1	Page address					1	Sets the display RAM page address
(4) Column address set upper bit	0	1	0	0	0	0	1	Most significant column address				0	Sets the most significant 4 bits of the display RAM column address.
Column address set lower bit				0	0	0	0	Least significant column address				0	Sets the least significant 4 bits of the display RAM column address.
(5) Status read	0	0	1	Status				0	0	0	0	0	Reads the status data
(6) Display data write	1	1	0	Write data							0	Writes to the display RAM	
(7) Display data read	1	0	1	Read data							0	Reads from the display RAM	
(8) ADC select	0	1	0	1	0	1	0	0	0	0	0	0	Sets the display RAM address SEG output correspondence 0: normal, 1: reverse
(9) Display normal/reverse	0	1	0	1	0	1	0	0	1	1	0	1	Sets the LCD display normal/ reverse 0: normal, 1: reverse
(10) Display all points ON/OFF	0	1	0	1	0	1	0	0	1	0	0	0	Display all points 0: normal display 1: all points ON
(11) LCD bias set	0	1	0	1	0	1	0	0	0	1	0	1	Sets the LCD drive voltage bias ratio 0: 1/9 bias, 1: 1/7 bias (ST7565R)
(12) Read-modify-write	0	1	0	1	1	1	0	0	0	0	0	0	Column address increment At write: +1 At read: 0
(13) End	0	1	0	1	1	1	0	1	1	1	0	0	Clear read/modify/write
(14) Reset	0	1	0	1	1	1	0	0	0	1	0	0	Internal reset
(15) Common output mode select	0	1	0	1	1	0	0	0	*	*	*	*	Select COM output scan direction 0: normal direction 1: reverse direction
(16) Power control set	0	1	0	0	0	1	0	1	Operating mode			0	Select internal power supply operating mode
(17) V ₀ voltage regulator internal resistor ratio set	0	1	0	0	0	1	0	0	Resistor ratio			0	Select internal resistor ratio(Rb/Ra) mode
(18) Electronic volume mode set	0	1	0	1	0	0	0	0	0	0	0	1	Set the V ₀ output voltage electronic volume register
Electronic volume register set				0	0	Electronic volume value						0	
(19) Sleep mode set	0	1	0	1	0	1	0	1	1	0	0	0	0: Sleep mode, 1: Normal mode
(20) Booster ratio set	0	1	0	1	1	1	1	1	0	0	0	0	select booster ratio 00: 2x,3x,4x 01: 5x 11: 6x
(21) NOP	0	1	0	1	1	1	0	0	0	1	1	1	Command for non-operation
(22) Test	0	1	0	1	1	1	1	*	*	*	*	*	Command for IC test. Do not use this command

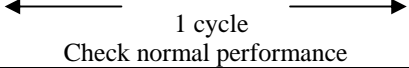
10.0 MECHANICAL DIAGRAM



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11.0 RELIABILITY TEST

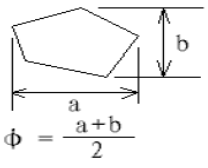
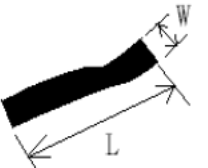
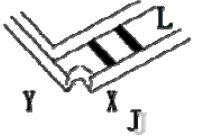
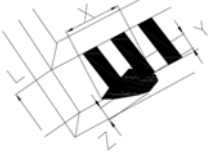


NO	Test Item	Description	Test Condition	Remark	
1	Environmental Test	High temperature storage	Applying the high storage temperature Under normal humidity for a long time Check normal performance	80 °C 96hrs	
2		Low temperature storage	Applying the low storage temperature Under normal humidity for a long time Check normal performance	-30°C 96hrs	
3		High temperature Operation	Apply the electric stress(Voltage and current) Under high temperature for a long time	70 °C 96hrs	Note1
4		Low temperature Operation	Apply the electric stress Under low temperature for a long time	-20°C 96hrs	Note1 Note2
5		High temperature/High Humidity Storage	Apply high temperature and high humidity storage for a long time	90% RH 40°C 96hrs	Note2
6		Temperature Cycle	Apply the low and high temperature cycle -30°C <> 25°C <> 80°C <> 25°C 30min 10min 30min 10min  1 cycle Check normal performance	-30°C/80°C 10 cycle	
7	Mechanical Test	Vibration test(Package state)	Applying vibration to product check normal performance	Freq:10~55~10Hz Amplitude:0.75mm 1cycle time:1min X.Y.Z every direction for 15 cycles	
8		Shock test(package state)	Applying shock to product check normal performance	Drop them through 70cm height to strike horizontal plane	
9	Other				

Remark


Note1:Normal operations condition (25°C±5°C).

Note2:Pay attention to keep dewdrops from the module during this test.
Ds subjected to the test must not have dew condensation.

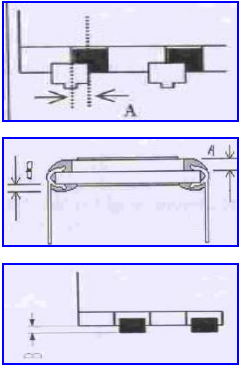
12.0 APPEARANCE CRITERIA

Item	Description	Picture	Specification		MA	MI	Inspection Method	
Dot defects (black/white dot)	Scratches black dot white dot on the polarizer dirty spot and bubble between the polarizer and glass in the display area.	 <p>$\phi = \frac{a+b}{2}$</p> <p>J:the distance between dot and dot.</p>	≤ 0.1	Ignored	●		Visual/ contrast by Inspection standard film	
			$0.1 < \phi \leq 0.20$	2				$J > 5$
			$0.20 < \phi \leq 0.25$	1				$J > 10$
			$0.25 < \phi \leq 0.30$	0				
			$0 < \phi > 0.3$	0				
black/white line defect (straight line or curve etc. Line type defects)	Fibres in active area, scratches and black line on the glass or polarizer.	 <p>J:the distance between dot and dot.</p>	$W \leq 0.01$	Ignored	●		Visual/ contrast by Inspection standard film	
			$W \leq 0.02 \quad L \leq 5$	2				$J > 5$
			$W \leq 0.03 \quad L \leq 4$	1				$J > 10$
			$W \leq 0.04 \quad L \leq 3$	0				$J > 10$
			$W \leq 0.05 \quad L \leq 2$	0				
Chip on corner	sidestep on the lower glass	 <p>Y:width of chip X:length of chip L:width of sidestep J:distance between electrode and the farthest edge.</p>	$Y \leq 1/2L, X \leq 1$	Ignored		●	Visual/ contrast by Inspection standard film	
			$Y \leq 1/2L, X \leq 2$	2				
			$Y \leq 1/2L, X \leq 3$	1				
			$Y \leq 1/2L, X \leq 1/3J$	0				$J \leq 3$
			$Y \leq 1/2L, X \leq 2/3J$	0				$J \leq 3$
Crack		 <p>Y:width of crack X:length of crack L:width of sidestep T:depth of crack Z:thickness of single glass</p>	$Y \leq 1/5L \quad X \leq 5 \quad Z \leq 1/2T$	Ignored		●	Visual/ contrast by Inspection standard film	
			$Y \leq 1/4L \quad X \leq 5 \quad Z \leq 1/2T$	2				
			$Y \leq 1/3L \quad X \leq 5 \quad Z \leq 1/2T$	1				
			$Y \leq 1/3L \quad X \leq 10 \quad Z \leq 1/2T$	0				
			$Y \leq 1/3L \quad X \leq 15 \quad Z \leq 1/2T$	0				
Crack			Cracks in any area	rejected	●		Visual	
Polarizer			≤ 0.8	Accepted		●	Visual/	

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		be applicable for up/bottom polarizer	0.8<L≤1.0	Rejected			contrast by Inspection standard film
			1.0<L≤1.5	Rejected			
			1.5<L≤2.0	Rejected			
			Any seeable polarizer slanting or excursion in active area will be rejected. The polarizer edge should be even and be line. Any indentation within 1/3 of silkscreen line will be rejected. Wrong direction, missing or extra, incorrect sticking for polarizer and dirty surface(grease) on polarizer will be rejected. seeable black silkscreen line from the arond can be accepted. Refer to the drawing size requirement.				
End seal		 <p>L:The distance from the block to edge of glass.</p>	UV glue of seal on the glass surface	Rejected	●		Visual/contrast by Inspection standard film
			The UV glue of seal overflow into the active area.	Rejected			
			Direction of end seal is different from the drawing.	Rejected			
			Glue capacity of end seal < (1/3)*L	Rejected			
			the height and length of end seal is out of the drawing requirements.	Rejected			
Silkscreen line			silkscreen line overflow into the active area.	Rejected	●		Visual/contrast by Inspection standard film
			silkscreen line deviated in active area.	Rejected			
			bubble of silkscreen line ≥ 1/3 width of silkscreen line	Rejected			

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PIN			<p>Glue on PIN: there is glue on the PIN without pin clip will be rejected. PIN glue solidification: PIN glue doesn't solidify completely. The sunken or glue stain by touching will be rejected.</p> <p>PIN deflection: if deflection angle $> \pm 5^\circ$, rejected; contrarily, please refer to the drawing requirement. Without continuous glue on pins will be rejected. PIN glue stains on polarizer or inleakage polarizer and glass, rejected. PIN glue exceeds the up polarizer, rejected.</p> <p>Missing or extra, broken pin, rejected.</p> <p>PIN loosen: no permission for pin loose or drop. Clip</p> <p>PIN:pin center exceeds 1/3 ITO width, rejected. No pin glue, rejected. UV glue range: UV glue must be exceeded over 1~1.5 pin distance from both side. if not, rejected. PIN length and direction must be same with the drawing requirements.</p>	●	Visual/contrast by Inspection standard film	
Protective film			LCD protective film can not stick on the polarizer and the product protective film raised $\leq 1/3$ length or width of polarizer from same direction of axis and its total length should be $\leq 15\text{mm}$. This defect can be accepted.	●	Visual	
Rainbow			rainbow is not in active area.	Accepted	●	Visual/contrast by golden sample
			Rainbow in active area.	Rejected		
			with obvious discoloration and uneven color.	Rejected		
background color			There are obvious different background color from the same product lot.	Rejected	●	Visual/contrast by golden sample

NOTE:

Inspection condition:

Viewing distance for cosmetic inspection is 30cm with bare eyes, and under an environment of 800 lux(20W*2---40W) light intensity, all directions for inspecting the sample should be within 45° against perpendicular line.

13.0 PRECAUTION FOR USING LCM

1. When design the product with this LCD Module, make sure the viewing angle matches to its purpose of usage.
2. As LCD panel is made of glass substrate, Dropping the LCD module or banging it against hard objects may cause cracking or fragmentation. Especially at corners and edges.
3. Although the polarizer of this LCD Module has the anti-glare coating, always be careful not to scratch its surface. Use of a plastic cover is recommended to protect the surface of polarizer.
4. If the LCD module is stored at below specified temperature, the LC material may freeze and be deteriorated. If it is stored at above specified temperature, the molecular orientation of the LC material may change to Liquid state and it may not revert to its original state. Excessive temperature and humidity could cause polarizer peel off or bubble. Therefore, the LCD module should always be stored within specified temperature range.
5. Saliva or water droplets must be wiped off immediately as those may leave stains or cause color changes if remained for a long time. Water vapor will cause corrosion of ITO electrodes.
6. If the surface of LCD panel needs to be cleaned, wipe it swiftly with cotton or other soft cloth. If it is not still clean enough, blow a breath on the surface and wipe again.
7. The module should be driven according to the specified ratings to avoid malfunction and permanent damage. Applying DC voltage cause a rapid deterioration of LC material. Make sure to apply alternating waveform by continuous application of the M signal. Especially the power ON/OFF sequence should be kept to avoid latch-up of driver LSIs and DC charge up to LCD panel.
8. Mechanical Considerations
 - a) LCM are assembled and adjusted with a high degree of precision. Avoid excessive shocks and do not make any alterations or modifications. The following should be noted.
 - b) Do not tamper in any way with the tabs on the metal frame.
 - c) Do not modify the PCB by drilling extra holes, changing its outline, moving its components or modifying its pattern.
 - d) Do not touch the elastomer connector; especially insert a backlight panel (for example, EL).
 - e) When mounting a LCM makes sure that the PCB is not under any stress such as bending or twisting. Elastomer contacts are very delicate and missing pixels could result from slight dislocation of any of the elements.
 - f) Avoid pressing on the metal bezel, otherwise the elastomer connector could be deformed and lose contact, resulting in missing pixels.

9. Static Electricity

a) Operator

Wear the electrostatics shielded clothes because human body may be statically charged if not wear shielded clothes. Never touch any of the conductive parts such as the LSI pads; the copper leads on the PCB and the interface terminals with any parts of the human body.

b) Equipment

There is a possibility that the static electricity is charged to the equipment, which has a function of peeling or friction action (ex: conveyor, soldering iron, working table). Earth the equipment through proper resistance (electrostatic earth: 1×10^8 ohm).

Only properly grounded soldering irons should be used.

If an electric screwdriver is used, it should be well grounded and shielded from commutator sparks.

The normal static prevention measures should be observed for work clothes and working benches; for the latter conductive (rubber) mat is recommended.

c) Floor

Floor is the important part to drain static electricity, which is generated by operators or equipment.

There is a possibility that charged static electricity is not properly drained in case of insulating floor. Set the electrostatic earth (electrostatic earth: 1×10^8 ohm).

d) Humidity

Proper humidity helps in reducing the chance of generating electrostatic charges. Humidity should be kept over 50%RH.

e) Transportation/storage

The storage materials also need to be anti-static treated because there is a possibility that the human body or storage materials such as containers may be statically charged by friction or peeling.

The modules should be kept in antistatic bags or other containers resistant to static for storage.

f) Soldering

Solder only to the I/O terminals. Use only soldering irons with proper grounding and no leakage.

Soldering temperature : $280^{\circ} \text{C} \pm 10^{\circ} \text{C}$

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Soldering time: 3 to 4 sec.

Use eutectic solder with resin flux fill.

If flux is used, the LCD surface should be covered to avoid flux spatters. Flux residue should be removed afterwards.

g) Others

The laminator (protective film) is attached on the surface of LCD panel to prevent it from scratches or stains. It should be peeled off slowly using static eliminator.

Static eliminator should also be installed to the workbench to prevent LCD module from static charge.

10. Operation

- a) Driving voltage should be kept within specified range; excess voltage shortens display life.
 - b) Response time increases with decrease in temperature.
 - c) Display may turn black or dark blue at temperatures above its operational range; this is (however not pressing on the viewing area) may cause the segments to appear "fractured".
 - d) Mechanical disturbance during operation (such as pressing on the viewing area) may cause the segments to appear "fractured".
11. If any fluid leaks out of a damaged glass cell, wash off any human part that comes into contact with soap and water. The toxicity is extremely low but caution should be exercised at all the time.
 12. Disassembling the LCD module can cause permanent damage and it should be strictly avoided.
 13. LCD retains the display pattern when it is applied for long time (Image retention). To prevent image retention, do not apply the fixed pattern for a long time. Image retention is not a deterioration of LCD. It will be removed after display pattern is changed.
 14. Do not use any materials, which emit gas from epoxy resin (hardener for amine) and silicone adhesive agent (dealcohol or deoxym) to prevent discoloration of polarizer due to gas.
 15. Avoid the exposure of the module to the direct sunlight or strong ultraviolet light for a long time.