# ZETTLER DISPLAYS

# SPECIFICATIONS FOR LIQUID CRYSTAL DISPLAY

	CUSTOMER APP	ROVAL	
<b>※ PART NO.</b> : <u>A</u>	ACM1602KU-NLW-BBW	-XT(ZETTLER D	DISPLAYS) VER 2.0
APPROVAL		COMPANY CHOP	
CUSTOMER			
COMMENTS			

ZETTLER DISPLAYS ENGINEERING APPROVAL					
DESIGNED BY	CHECKED BY	APPROVED BY			
ZZK	LH	GZH			

REVISION RECORD						
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VER2.0	19/9-2014		INCREASING THE WEIGHT OF THE PACKING			

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## **1.0 GENERAL SPECS**

1. Display Format	16*2Character
2. Power Supply	5.0V
3. Module Outline Dimension	80.0mm(W) x 36.0mm(H) x max 13mm(D)
4. Viewing Area(W*H)	64.5mm(W) x 16.4mm(H)
5. Dot Size (W*H)	0.56mm(W) x 0.61mm(H)
6. Dot Pitch (W*H)	0.61mm(W) x 0.66mm(H)
7. Character Size (W*H)	3.00mm(W) x 5.23mm(H)
8. Character Pitch (W*H)	3.51mm(W) x 5.75mm(H)
9. Viewing Direction	6:00 O'Clock
10. Driving Method	1/16Duty,1/5Bias
11. Control IC	ST7066U-0T OR EQUIV
12. Display Mode	STN(BLUE)/Negative/ Transmissive
13. Backlight	LED-SIDE (WHITE)
14. Operating Temperature	-20°C ~ 70°C
15. Storage Temperature	-30°C ~ 80°C
16. RoHS	ROHS compliant
17. Other	

## 2.0 ABSOLUTE MAXIMUM RATINGS

Item	Symbol	Min	Тур	Мах	Unit
Operating temperature	Тор	-20		70	°C
Storage temperature	Tst	-30		80	°C
Input voltage	Vin	Vss-0.3		Vdd+0.3	V
Supply voltage for logic	Vdd- Vss	2.7		5.5	V
Supply voltage for LCD driving	Vdd- V0	3.0		8.0	V

## **3.0 ELECTRICAL CHARACTERISTICS**

#### 3.1 Electrical Characteristics Of LCM

Item Symbol		Condition	Min	Тур	Max	Unit
Power Supply Voltage	Vdd	25°C	4.8	5.0	5.2	V
Power Supply Current	ldd	Vdd=5.0V, fosc=270kHz			2	mA
Input voltage (high)	Vih	Pins:(E,RS,R/W,DB0-DB7)	0.7Vdd		Vdd	V
Input voltage (low)	Vil	VDD=5V	-0.3		0.6	V
		-20°C	4.6	4.8	5.0	
Recommended LC Driving Voltage	Vdd –V0	25°C	4.3	4.5	4.7	V
		70°C	4.0	4.2	4.4	

#### 3.2 The Characteristics Of LED Backlight

3.2.1 Electrical-Optical Characteristics Of LED Backlight (Ta=25°C)	
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ltem Symb		Condition	Min	Тур	Max	Unit
Forward Voltage <sup>(1)</sup>	Vf	lf=30mA	4.6	5.0	5.4	V
Reverse Voltage	Vr	-			5	V
Luminance <sup>(2)</sup>	Lv	lf=30mA	365	490		cd/m <sup>2</sup>
Uniformity <sup>(3)</sup>	Δ	(Lvmin/Lvmax)%	70%			-
Peak wave length	λp					nm
Chroma coordinate	х		0.26		0.30	um
oniona coordinate	у		0.27		0.31	um
Lifetime <sup>(4)</sup>	-	lf=30mA	-	20000	-	Hours

NOTE:

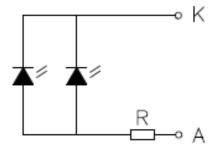
(1) Forward voltage means voltage applied directly to the backlight

(2)The luminance is the average value of 5 points,The measurement instrument is BM-7 luminance colorimeter.The diameter of aperture is  $\Phi$  5mm

(3) Luminance means the backlight brightness without LCD.

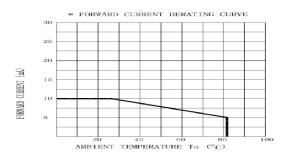
(4) Backlight lifetime means luminance value larger than half of the original after 20000 hours' continuous working.

#### 3.2.2 Backlight Control Circuit FOR LCM (2 pcs LED)

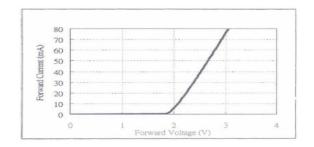


#### 3.2.3 LED Characteristics Curves (for single led)

1. Forward current vs. Ambient temperature



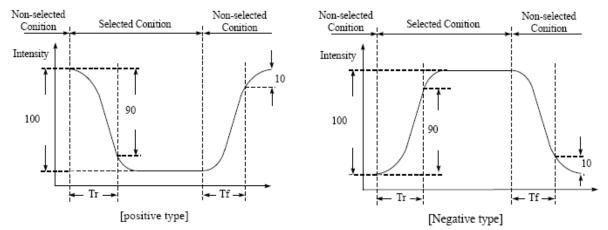
2. Forward current vs. Forward voltage



## 4.0 OPTICAL CHARACTERISTICS (Ta=25°C)

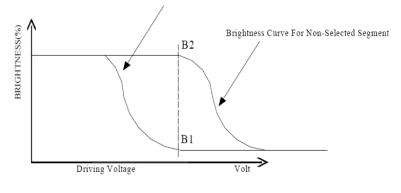
ltem	Symbol	Condition	Min	Тур	Max	Unit
Viewing angle (Left - right)	θ2	$Cr \geq 2.0$	-35	-	35	deg
Viewing angle (Up-down)	θ1	$Cr \geq 2.0$	-25	-	40	deg
Contrast Ratio	Cr	θ <b>1=0°</b> , θ <b>2=0</b> °	-	6	-	
Response time (rise)	Tr	θ <b>1=0°</b> , θ <b>2=0</b> °	-	180	300	ms
Response time (fall)	Tf	θ1=0°, θ2=0°	-	150	250	ms

## (1). Definition of Optical Response Time



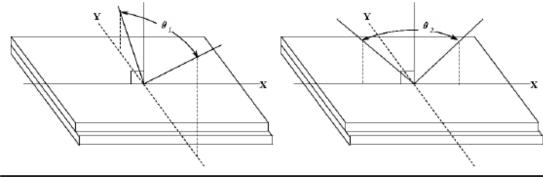
## (2). Definition of Contrast Ratio

#### Brightness Curve For Selected Segment



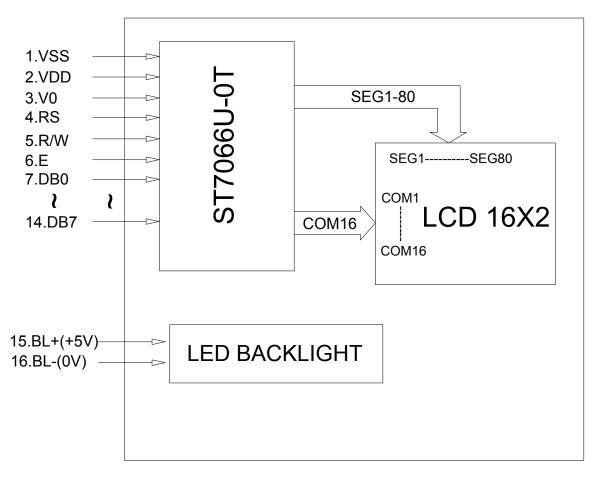
Cr= Brightness of Non-selected Segment(B2) Brightness of selected Segment(B1)

#### (3). Definition of Viewing Angle $\theta$ 2 and $\theta$ 1



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## **5.0 BLOCK DIAGRAM**

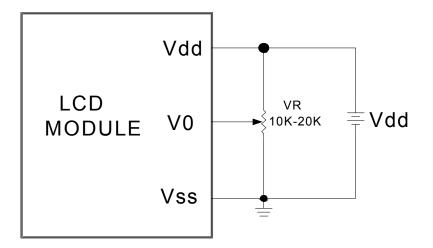


## 6.0 PIN ASSIGNMENT

Pin No.	Symbol	Function
1	Vss	Ground
2	Vdd	+5.0V
3	V0	LCD contrast adjust
4	RS	Register select
5	R/W	Read / Write Signal
6	E	Enable Signal
7	DB0	Data bit 0
8	DB1	Data bit 1
9	DB2	Data bit 2
10	DB3	Data bit 3
11	DB4	Data bit 4
12	DB5	Data bit 5
13	DB6	Data bit 6
14	DB7	Data bit 7
15	BL+(+5V)	Power Supply for BL+
16	BL-	Power Supply for BL-

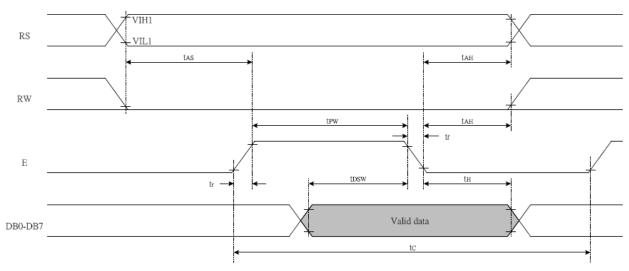
ZETTLER DISPLAYS

## 7.0 POWER SUPPLY

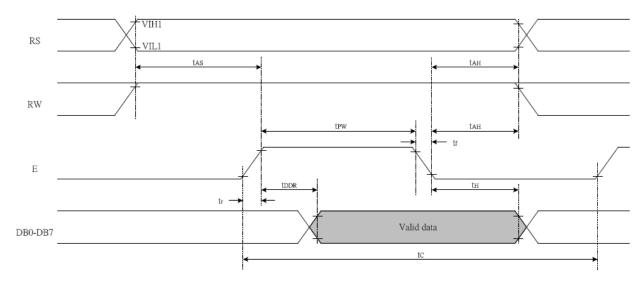


## **8.0 TIMING CHARACTERISTICS**

#### Writing data from MPU to ST7066U

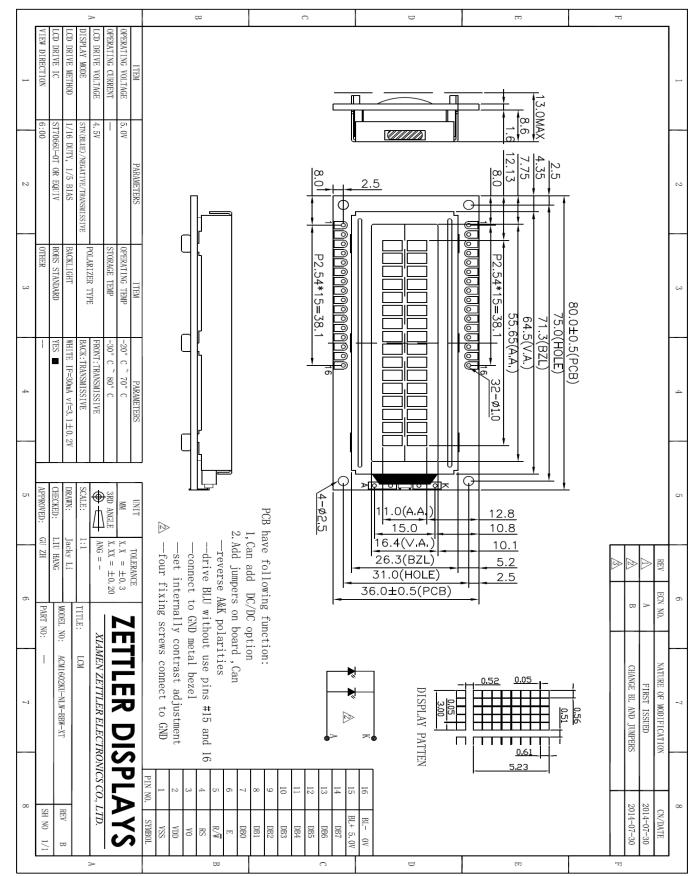


#### Reading data from ST7066U to MPU



Write Mod	e (Writing data from MPU t	o ST706	6U)		
Enable Cycle Time	Pin E	1200	-	-	ns
Enable Pulse Width	Pin E	140	-	-	ns
Enable Rise/Fall Time	Pin E	-	-	25	ns
Address Setup Time	Pins: RS,RW,E	0	-	-	ns
Address Hold Time	Pins: RS,RW,E	10	-	-	ns
Data Setup Time	Pins: DB0 - DB7	40	-	-	ns
Data Hold Time	Pins: DB0 - DB7	10	-	-	ns
Read Mode	e (Reading Data from ST70	66U to N	1PU)		
Enable Cycle Time	Pin E	1200	-	-	ns
Enable Pulse Width	Pin E	140	-	-	ns
Enable Rise/Fall Time	Pin E	-	-	25	ns
Address Setup Time	Pins: RS,RW,E	0	-	-	ns
Address Hold Time	Pins: RS,RW,E	10	-	-	ns
Data Setup Time	Pins: DB0 - DB7	-	-	100	ns
Data Hold Time	Pins: DB0 - DB7	10	-	-	ns
	Enable Cycle Time Enable Pulse Width Enable Rise/Fall Time Address Setup Time Data Setup Time Data Setup Time Data Hold Time <i>Read Mode</i> Enable Cycle Time Enable Pulse Width Enable Rise/Fall Time Address Setup Time Data Setup Time	Enable Cycle TimePin EEnable Pulse WidthPin EEnable Rise/Fall TimePin EAddress Setup TimePins: RS,RW,EAddress Hold TimePins: RS,RW,EData Setup TimePins: DB0 - DB7Data Hold TimePins: DB0 - DB7Read Mode (Reading Data from ST70)Enable Cycle TimePin EEnable Pulse WidthPin EEnable Rise/Fall TimePin EAddress Setup TimePins: RS,RW,EAddress Setup TimePin EAddress Setup TimePins: RS,RW,EAddress Hold TimePins: RS,RW,EData Setup TimePins: RS,RW,EData Setup TimePins: RS,RW,EData Setup TimePins: RS,RW,EData Setup TimePins: DB0 - DB7	Enable Cycle TimePin E1200Enable Pulse WidthPin E140Enable Rise/Fall TimePin E-Address Setup TimePins: RS,RW,E0Address Hold TimePins: RS,RW,E10Data Setup TimePins: DB0 - DB740Data Hold TimePins: DB0 - DB710Read Mode(Reading Data from ST7066U to N)Enable Cycle TimePin E1200Enable Pulse WidthPin E140Enable Rise/Fall TimePin E-Address Setup TimePins: RS,RW,E0Address Hold TimePins: RS,RW,E10Data Setup TimePins: RS,RW,E10Data Setup TimePins: RS,RW,E10Data Setup TimePins: RS,RW,E10Data Setup TimePins: DB0 - DB7-	Enable Pulse WidthPin E140Enable Rise/Fall TimePin E-Address Setup TimePins: RS,RW,E0Address Hold TimePins: RS,RW,E10Data Setup TimePins: DB0 - DB740Data Hold TimePins: DB0 - DB710Read Mode(Reading Data from ST7060U to MPU)Enable Cycle TimePin E1200Enable Rise/Fall TimePin E-Address Setup TimePin E140Enable Rise/Fall TimePin E-Address Setup TimePins: RS,RW,E0Address Hold TimePins: RS,RW,E0Address Hold TimePins: RS,RW,E10Address Hold TimePins: DB0 - DB7-	Enable Cycle TimePin E1200-Enable Pulse WidthPin E140-Enable Rise/Fall TimePin E-25Address Setup TimePins: RS,RW,E0-Address Hold TimePins: RS,RW,E10-Data Setup TimePins: DB0 - DB740-Data Hold TimePins: DB0 - DB710-Read Mode(Reading Data from ST7060U to MPU)Enable Cycle TimePin E1200-Enable Pulse WidthPin E140-Enable Rise/Fall TimePin E-25Address Setup TimePins: RS,RW,E0-Enable Rise/Fall TimePin E-25Address Hold TimePins: RS,RW,E0-Inable Rise/Fall TimePin EInable Rise/Fall TimePin EInable Rise/Fall TimePins: RS,RW,E0-Inable Rise Hold TimePins: RS,RW,E10-Inable Rise Hold Ti

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#### ACM1602KU-NLW-BBW-XT (ZETTLER DISPLAYS) CHARACTER MODULE VER2.0

#### 9.0 MECHANICAL DIAGRAM

## **10.0 RELIABILITY TEST**

NO	Te	est Item	Description	<b>Test Condition</b>	Remark
1		High temperature storage	Applying the high storage temperature Under normal humidity for a long time Check normal performance	80 °C 96hrs	
2		Low temperature storage	Applying the low storage temperature Under normal humidity for a long time Check normal performance	-30°C 96hrs	
3		High temperature Operation	Apply the electric stress(Voltage and current) Under high temperature for a long time	70 °C 96hrs	Note1
4	Environmenta 1	Low temperature Operation	Apply the electric stress Under low temperature for a long time	-20ºC 96hrs	Note1 Note2
5	Test	High temperature/High Humidity Storage	Apply high temperature and high humidity storage for a long time	90% RH 40⁰C 96hrs	Note2
6		Temperature Cycle	Apply the low and high temperature cycle -10°C <>25°C <>60°C <>25°C 30min 10min 30min 10min ↓ 1 cycle Check normal performance	-30ºC/80ºC 10 cycle	
7	Mechanical Test	Vibration test(Package state)	Applying vibration to product check normal performance	Freq:10~55~10Hz Amplitude:0.75mm 1cycle time:1min X.Y.Z every direction for 15 cycles	
8		Shock test(package state)	Applying shock to product check normal performance	Drop them through 70cm height to strike horizontal plane	
9	Other				

#### Remark

Note1:Normal operations condition (25°C±5°C).

Note2:Pay attention to keep dewdrops from the module during this test.

## **11.0 DISPLAY INSTRUCTION TABLE**

#### Instruction Table:

				Inst	ructi	on C	Code	)				Description	
Instruction	RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	Description	Time (270KHz)	
Clear Display	0	0	0	0	0	0	0	0	0	1	Write "20H" to DDRAM. and set DDRAM address to "00H" from AC	1.52 ms	
Return Home	0	0	0	0	0	0	0	0	1	x	Set DDRAM address to "00H" from AC and return cursor to its original position if shifted. The contents of DDRAM are not changed.	1.52 ms	
Entry Mode Set	0	0	0	0	0	0	0	1	I/D	s	Sets cursor move direction and specifies display shift. These operations are performed during data write and read.	37 us	
Display ON/OFF	0	0	0	0	0	0	1	D	С	В	D=1:entire display on C=1:cursor on B=1:cursor position on	37 us	
Cursor or Display Shift	0	0	0	0	0	1	S/C	R/L	x	x	Set cursor moving and display shift control bit, and the direction, without changing DDRAM data.	37 us	
Function Set	0	0	0	0	1	DL	N	F	x	x	DL:interface data is 8/4 bits N:number of line is 2/1 F:font size is 5x11/5x8	37 us	
Set CGRAM address	0	0	0	1	AC5	AC4	AC3	AC2	AC1	AC0	Set CGRAM address in address counter	37 us	
Set DDRAM address	0	0	1	AC6	AC5	AC4	AC3	AC2	AC1	AC0	Set DDRAM address in address counter	37 us	
Read Busy flag and address	0	1	BF	AC6	AC5	AC4	AC3	AC2	AC1	AC0	Whether during internal operation or not can be known by reading BF. The contents of address counter can also be read.	0 us	
Write data to RAM	1	0	D7	D6	D5	D4	D3	D2	D1	D0	Write data into internal RAM (DDRAM/CGRAM)	37 us	
Read data from RAM	1	1	D7	D6	D5	D4	D3	D2	D1	D0	Read data from internal RAM (DDRAM/CGRAM)	37 us	

## **12.0 STANDARD CHARACTER PATTERNS**

67-64 63-60	0000	0001	0010	0011	0100	0101	0110	0111	1000	1001	1010	1011	1100	1101	1110	1111
0000	CG RAM (1)															
0001	(2)															
0010	3															
0011	(4)															
0100	(5)															
0101	(6)															
0110	7)															
0111	(8)															
1000	(1)															
1001	(2)															
1010	3															
1011	(4)															
1100	(5)															
1101	(6)															
1110	7)															
1111 Note: T	(8)															

Note: The character generator RAM is the RAM with which the user can rewrite character patterns by program.

## 13.0 APPEARANCE CRITERIA

Item	Description	Picture	Specification	Specification			MA	MI	Inspection Method
Dot defects (black/wh	Scratches		≤0.1		Ignored				Visual/
	black dot white dot on		0.1<		2	J>5			contrast by
ite dot)	the polarizer dirty spot		0.20< ∳ ≤0.25		1	J>10			Inspection standard
	and bubble between the polarizer and	J:the distance	0.25< <b>∮</b> ≤0.30		0				film
	glass in the display area.	between dot and dot.	0φ>0.3		0		]		
black/white	Fibres in	×₩	W≪0.01		Ignored				Visual/
line defect	active area, scratches	() Y	W≤0.02 L≤5		2	J>5			contrast by
(straight line or	and black line on the		W≤0.03 L≤4		1	J>10			Inspection standard
curve etc. Line type	glass or	₩ L	W≪0.04 L≪3		0	J>10			film
defects)	polarizer.	J:the distance between dot and dot.	W≤0.05 L≤2		0				
Chip on corner	sidestep on the lower		Y≤1/2L, X≤1		Ignored				Visual/ contrast by Inspection
control	glass	Y:width of chip X:length of chip L:width of sidestep	Y≪1/2L, X≪2		2				
			Y≤1/2L, X≤3		1			•	standard film
			Y≤1/2L, X≤1/3	9J	0	J≪3	]		
		J:distance between electrode and the farthermost edge.	Y≤1/2L, X≤2/3	Ŋ	0	J≪3			
Crack			Y≤1/5L X≤5 Z ≤1/2T		Ignored				Visual/ contrast
			Y≤1/4L X≤5 ≤1/2T	Z	2				by Inspection standard
		Y:width of crack X:length of crack	Y≤1/3L X≤5 2 1/2T	Y≤1/3L X≤5 Z≤				•	film
		L:width of sidestep T:deepth of crack Z:thickness of single glass	Y≤1/3L X≤10 Z ≤1/2T		0				
			Y≤1/3L X≤15 Z ≤1/2T		0				
Crack			Cracks in any area	reje	ected		•		Visual
Polarizer			≤0.8	Ace	cepted			•	Visual/

	be applicable for	0.8 <l≤1.0< th=""><th>Rejected</th><th></th><th>contrast</th></l≤1.0<>	Rejected		contrast
	up/bottom polarizer	1.0 <l≤1.5< td=""><td>Rejected</td><td></td><td>by Inspection</td></l≤1.5<>	Rejected		by Inspection
		1.5 <l≤2.0< td=""><td>Rejected</td><td></td><td>standard film</td></l≤2.0<>	Rejected		standard film
		Any seeable pola excursion in activ			
			ge should be even and ntion within 1/3 of ill be rejected.		
		incorrect sticking	missing or extra,   for polarizer and dirty on polarizer will be		
		seeable black sil arond can be acc	kscreen line from the cepted.		
		Refer to the drav	ving size requirement.		
End seal		UV glue of seal on the glass surface	Rejected		Visual/ contrast by
	L:The distance from the block to edge of glass.	The UV glue of seal overflow into the active area.	Rejected		Inspection standard film
		Direction of end seal is different from the drawing.	Rejected	•	
		Glue capacity of end seal < (1/3)*L	Rejected		
		the height and length of end seal is out of the drawing requirements.	Rejected		
Silkscreen line		silkscreen line overflow into the active area.	Rejected		Visual/ contrast by Inspection
		silkscreen line deviated in active area.	Rejected		standard film
		bubble of silkscreen line ≥ 1/3 witdth of silkscreen line	Rejected		

PIN		Glue on PIN: there is glue on the PIN without pin clip will be rejected. PIN glue solidification: PIN glue doesn't solidify completely. The sunken or glue stain by touching will be rejected. PIN deflection: if deflection angle $> \pm$ 5°, rejected; contrarily, please refer to the drawing requirement. Without continuous glue on pins will be rejected. PIN glue stains on polarizer or inleakage polarizer and glass, rejected. PIN glue exceeds the up polarizer, rejected. Missing or extra, broken pin, rejected. PIN loosen: no permission for pin loose or drop. Clip PIN:pin center exceeds 1/3 ITO width, rejected. No pin glue, rejected. UV glue range: UV glue must be exceeded over 1~1.5 pin distance from both side. if not, rejected. PIN length and direction must be same with the drawing requirements.				Visual/ contrast by Inspection standard film
Protective film		LCD protective film can not stick on the polarizer and the product protective film raised $\leq 1/3$ length or width of polarizer from same direction of axis and its total length should be $\leq 15$ mm. This defect can be accepted.			•	Visual
Rainbow		rainbow is not in active area.	Accepted			Visual/co ntrast by
		Rainbow in active area.	Rejected			golden sample
		with obvious Rejected discoloration and uneven color.				
backgroud color		There are obvious different background color from the same product lot.	Rejected		•	Visual/co ntrast by golden sample

### NOTE:

Inspection condition:

Viewing distance for cosmetic inspection is 30cm with bare eyes, and under an environment of 800 lux(20W\*2---40W) light intensity, all directions for inspecting the sample should be within 45° against perpendicular line.

## 14.0 PRECAUTION FOR USING LCM

- 1. When design the product with this LCD Module, make sure the viewing angle matches to its purpose of usage.
- As LCD panel is made of glass substrate, Dropping the LCD module or banging it against hard objects may cause cracking or fragmentation. Especially at corners and edges.
- 3. Although the polarizer of this LCD Module has the anti-glare coating, always be careful not to scratch its surface. Use of a plastic cover is recommended to protect the surface of polarizer.
- 4. If the LCD module is stored at below specified temperature, the LC material may freeze and be deteriorated. If it is stored at above specified temperature, the molecular orientation of the LC material may change to Liquid state and it may not revert to its original state. Excessive temperature and humidity could cause polarizer peel off or bubble. Therefore, the LCD module should always be stored within specified temperature range.
- 5. Saliva or water droplets must be wiped off immediately as those may leave stains or cause color changes if remained for a long time. Water vapor will cause corrosion of ITO electrodes.
- 6. If the surface of LCD panel needs to be cleaned, wipe it swiftly with cotton or other soft cloth. If it is not still clean enough, blow a breath on the surface and wipe again.
- 7. The module should be driven according to the specified ratings to avoid malfunction and permanent damage. Applying DC voltage cause a rapid deterioration of LC material. Make sure to apply alternating waveform by continuous application of the M signal. Especially the power ON/OFF sequence should be kept to avoid latchup of driver LSIs and DC charge up to LCD panel.
- 8. Mechanical Considerations
  - a) LCM are assembled and adjusted with a high degree of precision. Avoid excessive shocks and do not make any alterations or modifications. The following should be noted.
  - b) Do not tamper in any way with the tabs on the metal frame.
  - c) Do not modify the PCB by drilling extra holes, changing its outline, moving its components or modifying its pattern.
  - d) Do not touch the elastomer connector; especially insert a backlight panel (for example, EL).
  - e) When mounting a LCM makes sure that the PCB is not under any stress such as bending or twisting. Elastomer contacts are very delicate and missing pixels could result from slight dislocation of any of the elements.
  - f) Avoid pressing on the metal bezel, otherwise the elastomer connector could be deformed and lose contact, resulting in missing pixels.
- 9. Static Electricity
  - a) Operator

Ware the electrostatics shielded clothes because human body may be statically charged if not ware shielded clothes. Never touch any of the conductive parts such as the LSI pads; the copper leads on the PCB and the interface terminals with any parts of the human body.

b) Equipment

There is a possibility that the static electricity is charged to the equipment, which has a function of peeling or friction action (ex: conveyer, soldering iron, working table). Earth the equipment through proper resistance (electrostatic earth:  $1x10^8$  ohm).

Only properly grounded soldering irons should be used.

If an electric screwdriver is used, it should be well grounded and shielded from commutator sparks.

The normal static prevention measures should be observed for work clothes and working benches; for the latter conductive (rubber) mat is recommended.

c) Floor

#### Floor is the important part to drain static electricity, which is generated by operators or equipment.

There is a possibility that charged static electricity is not properly drained in case of insulating floor. Set the electrostatic earth (electrostatic earth:  $1x10^8$  ohm).

d) Humidity

Proper humidity helps in reducing the chance of generating electrostatic charges. Humidity should be kept over 50%RH.

e) Transportation/storage

The storage materials also need to be anti-static treated because there is a possibility that the human body or storage materials such as containers may be statically charged by friction or peeling.

The modules should be kept in antistatic bags or other containers resistant to static for storage. f) Soldering

Solder only to the I/O terminals. Use only soldering irons with proper grounding and no leakage. Soldering temperature : 280° C  $\pm$  10° C

Soldering time: 3 to 4 sec.

Use eutectic solder with resin flux fill.

If flux is used, the LCD surface should be covered to avoid flux spatters. Flux residue should be removed afterwards.

#### g) Others

## The laminator (protective film) is attached on the surface of LCD panel to prevent it from scratches or stains. It should be peeled off slowly using static eliminator.

Static eliminator should also be installed to the workbench to prevent LCD module from static charge. 10. Operation

- a) Driving voltage should be kept within specified range; excess voltage shortens display life.
- b) Response time increases with decrease in temperature.
- c) Display may turn black or dark blue at temperatures above its operational range; this is (however not pressing on the viewing area) may cause the segments to appear "fractured".
- d) Mechanical disturbance during operation (such as pressing on the viewing area) may cause the segments to appear "fractured".
- 11. If any fluid leaks out of a damaged glass cell, wash off any human part that comes into contact with soap and water. The toxicity is extremely low but caution should be exercised at all the time.
- 12. Disassembling the LCD module can cause permanent damage and it should be strictly avoided.
- 13. LCD retains the display pattern when it is applied for long time (Image retention). To prevent image retention, do not apply the fixed pattern for a long time. Image retention is not a deterioration of LCD. It will be removed after display pattern is changed.
- 14. Do not use any materials, which emit gas from epoxy resin (hardener for amine) and silicone adhesive agent (dealcohol or deoxym) to prevent discoloration of polarizer due to gas.
- 15. Avoid the exposure of the module to the direct sunlight or strong ultraviolet light for a long time.

The brightness of LCD module may be affected by the routing of CCFL cables due to leakage to the chassis through coupling effect. The inverter circuit needs to be designed taking the level of leakage current into consideration. Thorough evaluation is needed for LCD module and inverter built into its host equipment to ensure specified brightness.

## **15.0 JUMPER SCHEME AND THEIR SETTING**

	Jump					
Bezel connect GND	$R11=0\Omega$					
Screw connect GND	$R12=0\Omega$					
Drive BLU without use pins #15 and 16	$RA6/RA5 = 0\Omega (RA1/RA2/RA3/RA4:NC)$					
BLU is driven by pins #15(+) and 16(-)	$RA2/RA4 = 0\Omega (RA1/RA3/RA5/RA6:NC)$					
BLU is driven by pins #15(-) and 16(+)	$RA1/RA3 = 0\Omega (RA2/RA4/RA5/RA6:NC)$					
Set internally contrast adjustment	R7/R8=Suitable resistance (R6:NC)					
Set the external contrast adjustment	$R6=0\Omega (R7/R8:NC)$					
Set the negative voltage driver	Ud=NJU7660; C9/C10=10Uf/16V(Tantalum capacitor); R9/R10=Suitable resistance (R6/R7/R8:NC)					

## **16.0 PACKAGING DESCRIPTION**

			Anti	stat c bag	
Item	Size(L*W	*H)	Qua	ntity	Note
1.Master Carton	550*315*3	05mm	1		
2.Inner Carton	300*270*5	6mm	1	0	
Quantity Per Inner Carton	50	r Carton 500			
N.W	17.65(kg)	G.W			19.65(kg)