# ZETTLER DISPLAYS

XIAMEN ZETTLER ELECTRONICS CO., LTD.

# SPECIFICATIONS FOR LIQUID CRYSTAL DISPLAY

	CUSTOMER APP	ROVAL	
» ДАРТ NO			AVC) VED1 1
APPROVAL	ACM0802DA-NLW-BBW-BI	COMPANY	<u>2A13) VERI.1</u>
		СНОР	
CUSTOMER COMMENTS			

ZETTLER DISPLAYS ENGINEERING APPROVAL						
DESIGNED BY CHECKED BY APPROVED BY						
Jacky	LJF	GUZH				

REVISION	RECORD		
REVISION	REVISION DATE	PAGE	CONTENTS
VER1.0	2021-09-08		FIRST ISSUE
VER1.1	2021-09-23	P4	CHANGE LUMANCE OF BACKLIGHT

### **※ CONTENTS**

- 1.0 GENERAL SPECS
- 2.0 ABSOLUTE MAXIMUM RATINGS
- 3.0 ELECTRICAL CHARACTERISTICS
- 4.0 OPTICAL CHARACTERISTICS
- 5.0 BLOCK DIAGRAM
- 6.0 PIN ASSIGNMENT
- 7.0 POWER SUPPLY
- 8.0 TIMING CHARACTERISTICS
- 9.0 MECHANICAL DIAGRAM
- 10.0 RELIABILITY TEST
- 11.0 DISPLAY INSTRUCTION TABLE
- 12.0 STANDARD CHARACTER PATTERNS
- 13.0 APPEARANCE CRITERIA
- 14.0 PRECAUTION FOR USING LCM

# **1.0 GENERAL SPECS**

1. Display Format	8*2 Character
2. Power Supply	5.0V(Single power supply without DC-DC,adjustable Vop)
3. Overall Module Size	58.0mm(W) x 32.0mm(H) x max 13.5mm(D)
4. Viewing Aera(W*H)	38.0mm(W) x 16.0mm(H)
5. Dot Size (W*H)	0.56mm(W) x 0.66mm(H)
6. Dot Pitch (W*H)	0.60mm(W) x 0.70mm(H)
7. Character Size (W*H)	2.96mm(W) x 5.56mm(H)
8. Character Pitch (W*H)	3.55mm(W) x 5.94mm(H)
9. Viewing Direction	6:00 O'Clock
10. Driving Method	1/16Duty,1/5Bias
11. Controller IC	ST7066U-0A or compatible
12. Display Mode	STN (BLUE) / Negative/ Transmissive
13. Backlight Options	White LED/Side
14. Operating temperature	-20°C ~ 70°C
15. Storage temperature	-30°C ~ 80°C
16. RoHS	RoHS compliant

# 2.0 ABSOLUTE MAXIMUM RATINGS

Item	Symbol	Min	Тур	Max	Unit
Operating temperature	Тор	-20		70	°C
Storage temperature	Tst	-30		80	°C
Input voltage	Vin	Vss-0.3		Vdd+0.3	V
Supply voltage for logic	Vdd- Vss	2.7		5.5	V
Supply voltage for LCD drive	Vdd- V0	3.0		8.0	V

# **3.0 ELECTRICAL CHARACTERISTICS**

#### 3.1 Electrical Characteristics Of LCM

Item	Symbol	Condition	Min	Тур	Max	Unit
Power Supply Voltage	Vdd	25°C		5.0		V
Power Supply Current	ldd	Vdd=5.0V, fosc=270kHz		1.5	2.0	mA
Input voltage (high)	Vih	Pins:(E,RS,R/W,DB0-DB7)	2.5		Vdd	V
Input voltage (low)	Vil	VDD=5V	-0.3		0.6	V
		-20°C			5.0	
Recommended LC		25°C	4.3	4.5	4.7	V
Driving Voltage	Vdd –V0	70°C	4.0			

#### 3.2 The Characteristics Of LED Backlight

3.2.1	<b>Electrical-Optical</b>	Characteristics Of LED Backlight (Ta=25°C)	)
-------	---------------------------	--	---

ltem	Symbol	Condition	Min	Тур	Max	Unit
Forward Voltage <sup>(1)</sup>	Vf	lf=15mA	2.8	3.0	3.2	V
Reverse Voltage	Vr	-			5	V
Luminance <sup>(2)</sup>	Lv	lf=15mA	440	550		cd/m <sup>2</sup>
Uniformity <sup>(3)</sup>	Δ	(Lvmin/Lvmax)%	70%			-
Peak wave length	λp	-			-	nm
Chroma coordinate	x	lf=15mA	0.26		0.32	um
	у	lf=15mA	0.26		0.32	um

#### NOTE:

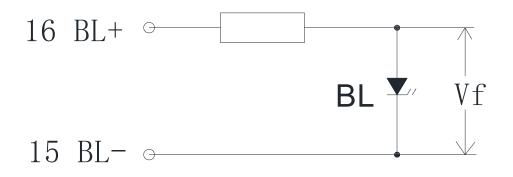
(1) Forward voltage means voltage applied directly to the LED

(2)The luminance is the average value of 5 points,The measurement instrument is BM-7 luminance colorimeter.The diameter of aperture is  $\Phi$ 5mm

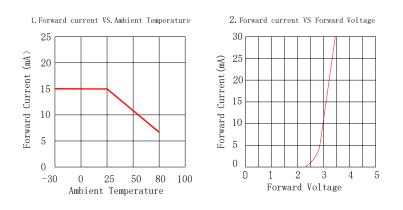
(3) Luminance means the backlight brightness without LCD.

(4) Backlight lifetime means luminance value larger than half of the original after 20000 hours' continuous working.

#### 3.2.2 Backlight Control Circuit For LCM (1x1=1 pcs LED)



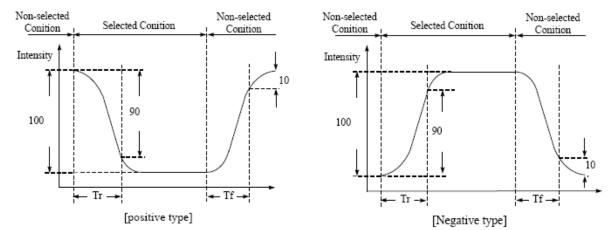
#### 3.2.3 LED Characteristics Curves (for single led)



# 4.0 OPTICAL CHARACTERISTICS (Ta=25°C)

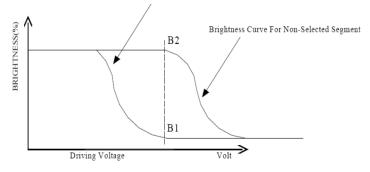
ltem	Symbol	Condition	Min	Тур	Мах	Unit
Viewing angle (Left - right)	θ2	$Cr \geq 2.0$	-35	-	35	deg
Viewing angle (Up-down)	θ1	$Cr \geq 2.0$	-25	-	40	deg
Contrast Ratio	Cr	θ1=0°, θ2=0°	-	6	-	
Response time (rise)	Tr	θ1=0°, θ2=0°	-	180	300	ms
Response time (fall)	Tf	θ1=0°, θ2=0°	-	150	250	ms

#### (1). Definition of Optical Response Time



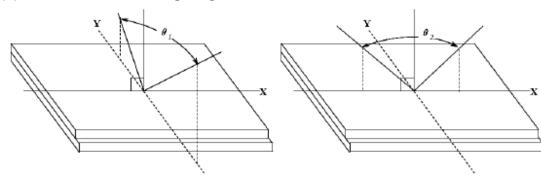
#### (2). Definition of Contrast Ratio



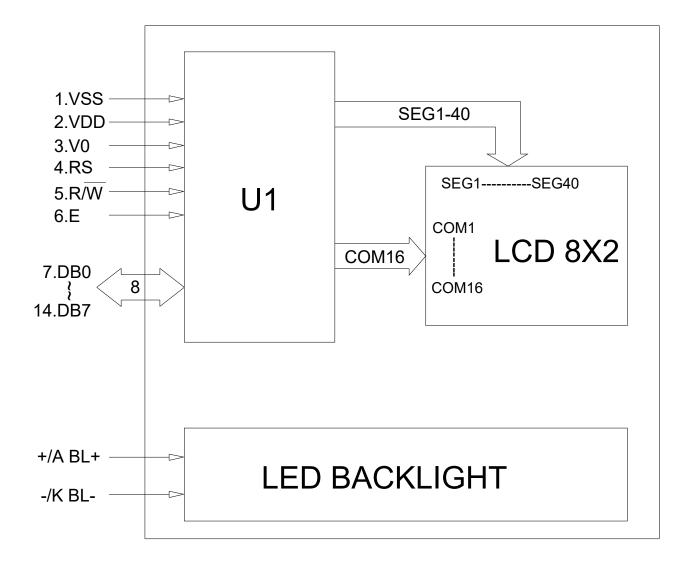


Cr= Brightness of Non-selected Segment(B2) Brightness of selected Segment(B1)

#### (3). Definition of Viewing Angle $\theta$ 2 and $\theta$ 1



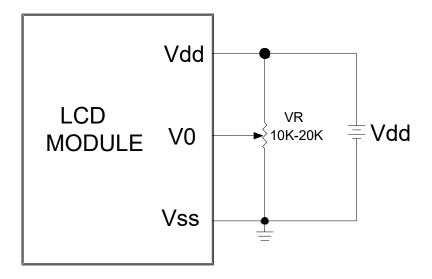
# 5.0 BLOCK DIAGRAM



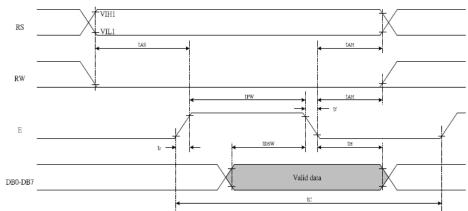
# 6.0 PIN ASSIGNMENT

Pin No.	Symbol	Function
1	Vss	Ground
2	Vdd	+5.0V
3	V0	LCD contrast adjust
4	RS	Register select
5	R/W	Read / Write Signal
6	E	Enable Signal
7	DB0	Data bit 0
8	DB1	Data bit 1
9	DB2	Data bit 2
10	DB3	Data bit 3
11	DB4	Data bit 4
12	DB5	Data bit 5
13	DB6	Data bit 6
14	DB7	Data bit 7
15	BL-	Power Supply for BL-(0V)
16	BL+	Power Supply for BL+(5.0V)

# 7.0 POWER SUPPLY

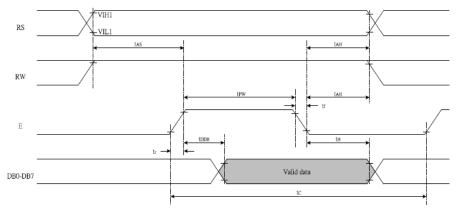


# **8.0 TIMING CHARACTERISTICS**



#### Write mode (Writing Data from MPU to ST7066U)

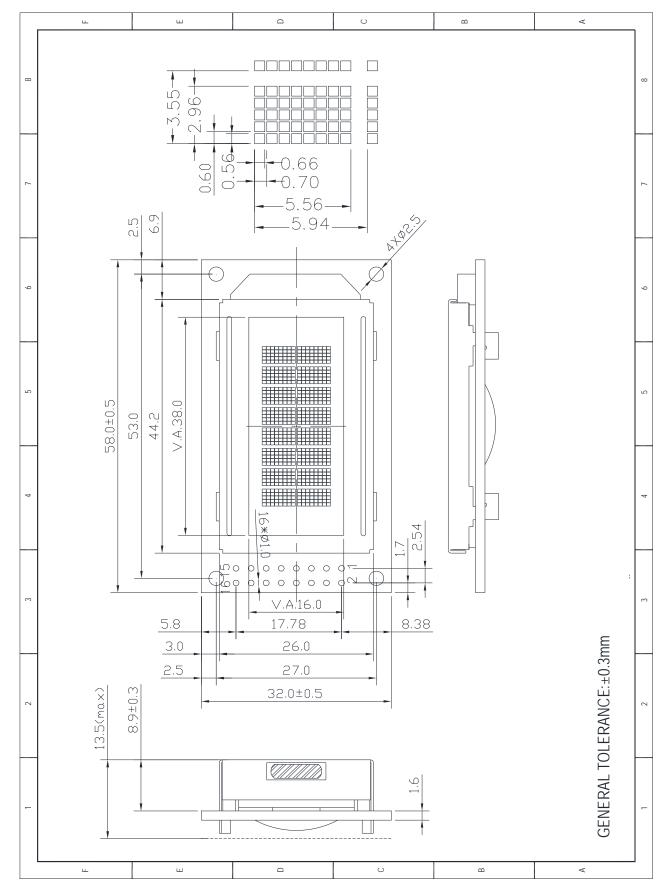
#### Read mode (Reading Data from ST7066U to MPU)



	Write Mode (Writing data from MPU to ST7066U)							
Tc	Enable Cycle Time	Pin E	1200	-	-	ns		
T <sub>PW</sub>	Enable Pulse Width	Pin E	140	-	-	ns		
T <sub>R</sub> ,T <sub>F</sub>	Enable Rise/Fall Time	Pin E	-	-	25	ns		
T <sub>AS</sub>	Address Setup Time	Pins: RS,RW,E	0	-	-	ns		
T <sub>AH</sub>	Address Hold Time	Pins: RS,RW,E	10	-	-	ns		
T <sub>DSW</sub>	Data Setup Time	Pins: DB0 - DB7	40	-	-	ns		
Τ <sub>Η</sub>	Data Hold Time	Pins: DB0 - DB7	10	-	-	ns		
	Read Mode	(Reading Data from ST70	66U to N	(IPU)				
Tc	Enable Cycle Time	Pin E	1200	-	-	ns		
T <sub>PW</sub>	Enable Pulse Width	Pin E	140	-	-	ns		
T <sub>R</sub> ,T <sub>F</sub>	Enable Rise/Fall Time	Pin E	-	-	25	ns		
T <sub>AS</sub>	Address Setup Time	Pins: RS,RW,E	0	-	-	ns		
T <sub>AH</sub>	Address Hold Time	Pins: RS,RW,E	10	-	-	ns		
T <sub>DDR</sub>	Data Setup Time	Pins: DB0 - DB7	-	-	100	ns		
Τ <sub>Η</sub>	Data Hold Time	Pins: DB0 - DB7	10	-	-	ns		

For more details, please refer to IC specification.

# 9.0 MECHANICAL DIAGRAM



## **10.0 RELIABILITY TEST**

NO	Те	est Item	Description	<b>Test Condition</b>	Remark
1		High temperature storage	Applying the high storage temperature Under normal humidity for a long time Check normal performance	80 °C 96hrs	
2		Low temperature storage	Applying the low storage temperature Under normal humidity for a long time Check normal performance	-30°C 96hrs	
3		High temperature Operation	Apply the electric stress(Voltage and current) Under high temperature for a long time	70 °C 96hrs	Note1
4	Environmenta 1	Low temperature Operation	Apply the electric stress Under low temperature for a long time	-20ºC 96hrs	Note1 Note2
5	Test	High temperature/High Humidity Storage	Apply high temperature and high humidity storage for a long time	90% RH 40⁰C 96hrs	Note2
6		Temperature Cycle	Apply the low and high temperature cycle -30°C <> 25°C <> 80°C <> 25°C 30min 10min 30min 10min 	-30ºC/80ºC 10 cycle	
7	Mechanical Test	Vibration test(Package state)	Applying vibration to product check normal performance	Freq:10~55~10Hz Amplitude:0.75mm 1cycle time:1min X.Y.Z every direction for 15 cycles	
8		Shock test(package state)	Applying shock to product check normal performance	Drop them through 70cm height to strike horizontal plane	
9	Other			*	

#### Remark

Note1:Normal operations condition (25°C±5°C).

Note2:Pay attention to keep dewdrops from the module during this test.

# **11.0 DISPLAY INSTRUCTION TABLE**

la deve di an				Ins	tructi	on Co	ode				Description		ecution ti emp = 25°	
Instruction	RS	RW	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	Description	Fosc= 190KHz	Fosc= 270KHz	Fosc= 350KHz
Clear Display	0	0	0	0	0	0	0	0	0	1	Write "20H" to DDRAM and set DDRAM address to "00H" from AC	2.16ms	1.52ms	1.18ms
Return Home	0	0	0	0	0	0	0	0	1	-	Set DDRAM address to "00H" from AC and return cursor to its original position if shifted. The contents of DDRAM are not changed.	2.16ms	1.52ms	1.18ms
Entry Mode Set	0	0	0	0	0	0	0	1	I/D	s	Assign cursor moving direction and enable the shift of entire display	53µs	38µs	29µs
Display ON/ OFF Control	0	0	0	0	0	0	1	D	с	В	Set display (D), cursor(C), and blinking of cursor(B) on/off control bit.	53µs	38µs	29µs
Cursor or Display Shift	0	0	0	0	0	1	S/C	R/L	-	-	Set cursor moving and display shift control bit, and the direction, without changing of DDRAM data.	53µs	38µs	29µs
Function Set	0	0	0	0	1	DL	N	F	-	-	Set interface data length (DL: 8-bit/4-bit), numbers of display line (N: 2-line/1-line) and, display font type (F:5x10 dots/5x8 dots)	53µs	38µs	29µs
Set CGRAM Address	0	0	0	1	AC5	AC4	AC3	AC2	AC1	AC0	Set CGRAM address in address counter.	53µs	38µs	29µs
Set DDRAM Address	0	0	1	AC6	AC5	AC4	AC3	AC2	AC1	AC0	Set DDRAM address in address counter	53µs	38µs	29µs
Read Busy Flag and Address Counter	0	1	BF	AC6	AC5	AC4	AC3	AC2	AC1	AC0	Whether during internal operation or not can be known by reading BF. The contents of address counter can also be read.			
Write Data to RAM	1	0	D7	D6	D5	D4	D3	D2	D1	DO	Write data into internal RAM (DDRAM/CGRAM).	53µs	38µs	29µs
Read Data from RAM	1	1	D7	D6	D5	D4	D3	D2	D1	D0	Read data from internal RAM (DDRAM/CGRAM).	53µs	38µs	29µs

# **12.0 STANDARD CHARACTER PATTERNS**

Upper 4 Lower Bits	0000	0001	0010	0011	0100	0101	0110	0111	1000	1001	1010	1011	1100	1101	1110	1111
4 Bits XXXX0000	CG RAM (1)			0	a	P	•	P					9		Q	p
xxxx0001	(2)		1	1		Q		4			II	Ţ	Ŧ	Ĺ,		q
xxxx0010	(3)		11	2	B	R	b	₽ <b>~</b> •			ľ	1	Ņ	X	F	8
xxxx0011	(4)				C	<u> </u>	C.	•				ņ			:	67
xxxx0100	(5)			4	D			t.			••				┠┛	Ω
xxxx0101	(6)			5			Ē	<b>L</b> .I				·*	• <b>†</b> • .'		S	ü
xxxx0110	(7)		8	6		Ņ	ť	V			7	ŢŢ			5	Σ
xxxx0111	(8)				6	<b>III</b>	9	Ŵ					X	·!!		π
xxxx1000	(1)		Ć	8		Х		Х			·1	7	 	Ņ	J.	X
xxxx1001	(2)		)	9		Y		۱				ŗ	ļ			
xxxx1010	(3)		*	# #		<u>, , , , , , , , , , , , , , , , , , , </u>	Ĵ				•••••			ŀ		
xxxx1011	(4)			<b>u</b> 7	K			{				ij			×	<b>J</b> =
xxxx1100	(5)		7									<u>;</u> ,		<b>.</b> ,	¢.	<b> </b> 24]
xxxx1101	(6)				h		M	}				Ζ	ሳ		÷	• •••••
xxxx1110	(7)			>		•	ľ				3	Ţ		•••	ħ	
xxxx1111	(8)		/	?	Ū		0	÷			ij	y			Ö	

Note: The character generator RAM is the RAM with which the user can rewrite character patterns by program.

# 13.0 APPEARANCE CRITERIA

ltem	Description	Picture	Specification	Specification				MI	Inspection Method
Dot defects (black/wh ite dot)	Scratches		≪0.1		Ignored				Visual/
	black dot white dot on	$\phi = \frac{a+b}{2}$	0.1< <b>∮</b> ≤0.20		2	J>5			contrast by
	the polarizer dirty spot		0.20< ∳ ≤0.25		1	J>10			Inspection standard
	and bubble between the polarizer and	J:the distance	0.25< ∳ ≤0.30		0				film
	glass in the display area.	between dot and dot.	0φ>0.3		0				
black/white	Fibres in	×.W	W≤0.01		Ignored				Visual/
line defect	active area, scratches	A A A A A A A A A A A A A A A A A A A	W≤0.02 L≤5		2	J>5			contrast by
(straight line or	and black line on the		W≤0.03 L≤4		1	J>10	•		Inspection standard
curve etc. Line type	glass or	L	W≤0.04 L≤3		0	J>10			film
defects)	polarizer.	J:the distance between dot and dot.	W≪0.05 L≪2		0				
Chip on corner	sidestep on the lower	Y:width of chip X:length of chip L:width of sidestep J:distance between electrode and the farthermost edge.	Y≪1/2L, X≪1		lgnored		-		Visual/ contrast by Inspection standard film
	glass		Y≪1/2L, X≪2		2			•	
			Y≪1/2L, X≪3		1				
			Y≤1/2L, X≤1/3	J	0	J≪3			
			Y≤1/2L, X≤2/3	J	0	J≪3			
Crack			Y≤1/5L X≤5 Z ≤1/2T		Ignored	·			Visual/ contrast
		- Maria	Y≤1/4L X≤5 ≤1/2T	Z	2				by Inspection standard
		Y:width of crack X:length of crack	Y≤1/3L X≤5 Z≤ 1/2T		1		-	•	film
		L:width of sidestep T:deepth of crack Z:thickness of single glass	Y≤1/3L X≤10 ≤1/2T	Z	0				
			Y≤1/3L X≤15 Z ≤1/2T		0				
Crack			Cracks in any area	reje	cted		•		Visual
Polarizer			≤0.8	Acc	cepted			•	Visual/

#### ACM0802DA-NLW-BBW-BL (ZETTLER DISPLAYS) CHARACTER MODULE VER1.1

	be applicable for	0.8 <l≤1.0< th=""><th>Rejected</th><th>contrast</th></l≤1.0<>	Rejected	contrast
	up/bottom polarizer	1.0 <l≤1.5< td=""><td>Rejected</td><td>by Inspection</td></l≤1.5<>	Rejected	by Inspection
		1.5 <l≤2.0< td=""><td>Rejected</td><td>standard film</td></l≤2.0<>	Rejected	standard film
		Any seeable pola excursion in activ		
		The polarizer ed be line. Any inde silkscreen line w		
		incorrect sticking	missing or extra, g for polarizer and dirty on polarizer will be	
		seeable black sil arond can be ac	kscreen line from the cepted.	
		Refer to the drav	ving size requirement.	
End seal		UV glue of seal on the glass surface	Rejected	Visual/ contrast by
	L:The distance from the block to edge of glass.	The UV glue of seal overflow into the active area.	Rejected	Inspection standard film
		Direction of end seal is different from the drawing.	Rejected	
		Glue capacity of end seal < (1/3)*L	Rejected	
		the height and length of end seal is out of the drawing requirements.	Rejected	
Silkscreen line		silkscreen line overflow into the active area.	Rejected	Visual/ contrast by Inspection
		silkscreen line deviated in active area.	Rejected	standard film
		bubble of silkscreen line $\geq$ 1/3 witdth of silkscreen line	Rejected	

#### ACM0802DA-NLW-BBW-BL (ZETTLER DISPLAYS) CHARACTER MODULE VER1.1

PIN	Glue on PIN: the without pin clip wi solidification: PIN completely. The s touching will be re PIN deflection: if 5°, rejected; co to the drawing red continuous glue of PIN glue stains on polarizer and glas PIN glue exceeds rejected. Missing or extra, I PIN loosen: no p or drop. PIN:pin center ex rejected. glue, rejected. range: UV glue m 1~1.5 pin distance rejected. P must be same wit requirements.	•		Visual/ contrast by Inspection standard film	
Protective film	LCD protective fill polarizer and the raised $\leq 1/3$ lengt from same directi length should be can be accepted.		•	Visual	
Rainbow	rainbow is not in active area.	Accepted			Visual/co ntrast by
	Rainbow in active area.	Rejected		•	golden sample
	with obvious discoloration and uneven color.	Rejected			
backgroud color	There are obvious different background color from the same product lot.	Rejected		•	Visual/co ntrast by golden sample

### NOTE:

Inspection condition:

Viewing distance for cosmetic inspection is 30cm with bare eyes, and under an environment of 800 lux(20W\*2---40W) light intensity, all directions for inspecting the sample should be within 45° against perpendicular line.

#### 14.0 PRECAUTION FOR USING LCM

1. When design the product with this LCD Module, make sure the viewing angle matches to its purpose of usage.

2. As LCD panel is made of glass substrate, Dropping the LCD module or banging it against hard objects may cause cracking or fragmentation. Especially at corners and edges.

- 3. Although the polarizer of this LCD Module has the anti-glare coating, always be careful not to scratch its surface. Use of a plastic cover is recommended to protect the surface of polarizer.
- 4. If the LCD module is stored at below specified temperature, the LC material may freeze and be deteriorated. If it is stored at above specified temperature, the molecular orientation of the LC material may change to Liquid state and it may not revert to its original state. Excessive temperature and humidity could cause polarizer peel off or bubble. Therefore, the LCD module should always be stored within specified temperature range.
- 5. Saliva or water droplets must be wiped off immediately as those may leave stains or cause color changes if remained for a long time. Water vapor will cause corrosion of ITO electrodes.
- 6. If the surface of LCD panel needs to be cleaned, wipe it swiftly with cotton or other soft cloth. If it is not still clean enough, blow a breath on the surface and wipe again.
- 7. The module should be driven according to the specified ratings to avoid malfunction and permanent damage. Applying DC voltage cause a rapid deterioration of LC material. Make sure to apply alternating waveform by continuous application of the M signal. Especially the power ON/OFF sequence should be kept to avoid latchup of driver LSIs and DC charge up to LCD panel.
- 8. Mechanical Considerations
  - a) LCM are assembled and adjusted with a high degree of precision. Avoid excessive shocks and do not make any alterations or modifications. The following should be noted.
  - b) Do not tamper in any way with the tabs on the metal frame.
  - c) Do not modify the PCB by drilling extra holes, changing its outline, moving its components or modifying its pattern.
  - d) Do not touch the elastomer connector; especially insert a backlight panel (for example, EL).
  - e) When mounting a LCM makes sure that the PCB is not under any stress such as bending or twisting. Elastomer contacts are very delicate and missing pixels could result from slight dislocation of any of the elements.
  - f) Avoid pressing on the metal bezel, otherwise the elastomer connector could be deformed and lose contact, resulting in missing pixels.
- 9. Static Electricity
  - a) Operator

Ware the electrostatics shielded clothes because human body may be statically charged if not ware shielded clothes. Never touch any of the conductive parts such as the LSI pads; the copper leads on the PCB and the interface terminals with any parts of the human body.

b) Equipment

There is a possibility that the static electricity is charged to the equipment, which has a function of peeling or friction action (ex: conveyer, soldering iron, working table). Earth the equipment through proper resistance (electrostatic earth:  $1x10^8$  ohm).

Only properly grounded soldering irons should be used.

If an electric screwdriver is used, it should be well grounded and shielded from commutator sparks. The normal static prevention measures should be observed for work clothes and working benches; for the latter conductive (rubber) mat is recommended.

c) Floor

#### Floor is the important part to drain static electricity, which is generated by operators or equipment.

There is a possibility that charged static electricity is not properly drained in case of insulating floor. Set the electrostatic earth (electrostatic earth: 1x10<sup>8</sup> ohm).

d) Humidity

Proper humidity helps in reducing the chance of generating electrostatic charges. Humidity should be kept over 50%RH.

e) Transportation/storage

The storage materials also need to be anti-static treated because there is a possibility that the human body or storage materials such as containers may be statically charged by friction or peeling.

The modules should be kept in antistatic bags or other containers resistant to static for storage.

f) Soldering

Solder only to the I/O terminals. Use only soldering irons with proper grounding and no leakage. Soldering temperature : 355° C  $\pm$  10° C

Soldering time: 3 to 4 sec.

Use eutectic solder with resin flux fill.

If flux is used, the LCD surface should be covered to avoid flux spatters. Flux residue should be removed afterwards.

#### g) Others

The laminator (protective film) is attached on the surface of LCD panel to prevent it from scratches or stains. It should be peeled off slowly using static eliminator.

Static eliminator should also be installed to the workbench to prevent LCD module from static charge. 10. Operation

- a) Driving voltage should be kept within specified range; excess voltage shortens display life.
- b) Response time increases with decrease in temperature.
- c) Display may turn black or dark blue at temperatures above its operational range; this is (however not pressing on the viewing area) may cause the segments to appear "fractured".
- d) Mechanical disturbance during operation (such as pressing on the viewing area) may cause the segments to appear "fractured".
- 11. If any fluid leaks out of a damaged glass cell, wash off any human part that comes into contact with soap and water. The toxicity is extremely low but caution should be exercised at all the time.
- 12. Disassembling the LCD module can cause permanent damage and it should be strictly avoided.
- 13. LCD retains the display pattern when it is applied for long time (Image retention). To prevent image retention, do not apply the fixed pattern for a long time. Image retention is not a deterioration of LCD. It will be removed after display pattern is changed.
- 14. Do not use any materials, which emit gas from epoxy resin (hardener for amine) and silicone adhesive agent (dealcohol or deoxym) to prevent discoloration of polarizer due to gas.
- 15. Avoid the exposure of the module to the direct sunlight or strong ultraviolet light for a long time.