

# PRODUCT SPECIFICATION

**5.0" TN TFT LCD MODULE**

**MODEL: T050800480-A3TMN-068 Ver:1.0**



< ◇ > Preliminary Specification

< ◆ > Finally Specification

CUSTOMER'S APPROVAL	
CUSTOMER :	
SIGNATURE:	DATE:

APPROVED BY	PM REVIEWED	PD REVIEWED	PREPARED BY

## Revision History

Revision	Date	Originator	Detail	Remarks
1.0	2017.04.25	ZFY	Initial Release	

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## 1. General Description

The specification is a transmissive type color active matrix liquid crystal display (LCD) which uses amorphous thin film transistor (TFT) as switching devices. This product is composed of a TFT-LCD panel, driver ICs and a backlight unit.

## 2. Module Parameter

Features	Details	Unit
Display Size(Diagonal)	5.0"	
LCD type	TN TFT	
Display Mode	Normally White/Transmissive	
Resolution	800 RGB x 480	Pixels
View Direction	12 O'clock	Best Image
Gray Scale Inversion Direction	6 O'clock	
Module Outline	120.7 (H) x 75.8 (V) x 3.1 (T) (Note1)	mm
Active Area	108 (H) x 64.8(V)	mm
Pixel Size	135 (H) x 135(V)	um
Pixel Arrangement	R.G.B Vertical Stripe	
Polarizer Surface Treatment	Anti-Glare	
Display Colors	16.7M	
Interface	24-bit RGB interface	
With or Without Touch Panel	Without	
Operating Temperature	-20~70	°C
Storage Temperature	-30~80	°C
Weight	60	g

Note 1: Exclusive hooks, posts, FFC/FPC tail etc.

## 3. Absolute Maximum Ratings

V<sub>SS</sub>=0V, Ta=25°C

Item	Symbol	Min.	Max.	Unit
Supply Voltage	VDD	-0.5	5.0	V
Storage temperature	T <sub>STG</sub>	-30	+80	°C
Operating temperature	T <sub>OP</sub>	-20	+70	°C

Note 1: If Ta below 50°C, the maximal humidity is 90%RH, if Ta over 50°C, absolute humidity should be less than 60%RH.

Note 2: The response time will be extremely slow when the operating temperature is around -10°C, and the background will become darker at high temperature operating.

## 4. DC Characteristics

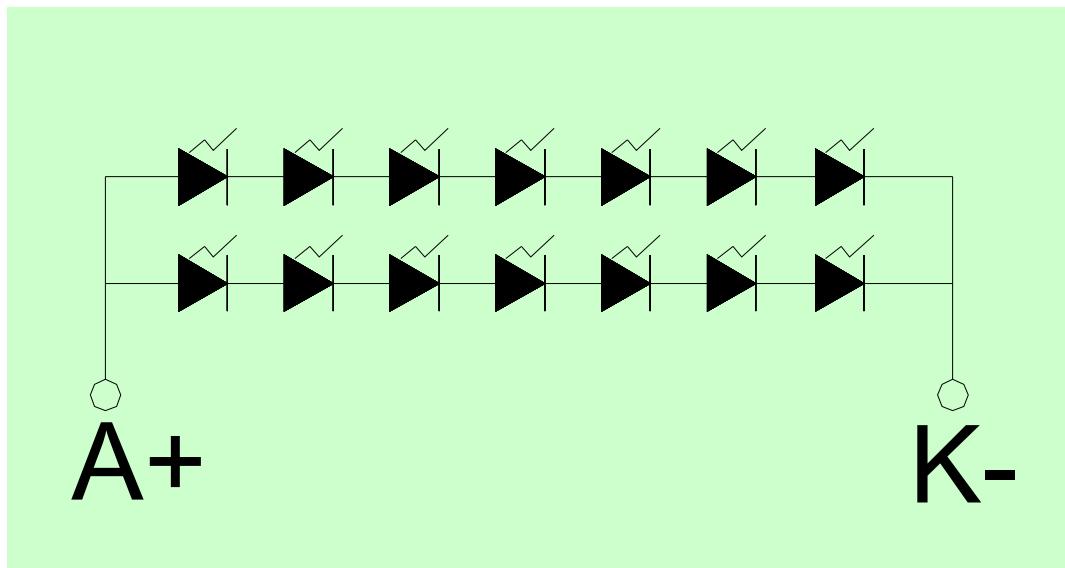
Item	Symbol	Min.	Typ.	Max.	Unit
Supply Voltage	VDD	3.0	3.3	3.6	V
Logic Low input voltage	V <sub>IL</sub>	0	-	0.3*VDD	V
Logic High input voltage	V <sub>IH</sub>	0.7*VDD	-	VDD	V
Logic Low output voltage	V <sub>OL</sub>	-	-	VSS+0.4	V
Logic High output voltage	V <sub>OH</sub>	VDD-0.4	-	-	V
Current Consumption All Black	I <sub>CC+ I<sub>IN</sub></sub>	-	110	200	mA

## 5. Backlight Characteristic

### 5.1. Backlight Characteristics

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Backlight Voltage	V <sub>LED</sub>	T <sub>a</sub> =25 °C, I <sub>F</sub> =20mA/LED	19.6	22.4	23.8	V
Backlight Current	I <sub>LED</sub>	T <sub>a</sub> =25 °C, V <sub>F</sub> =3.2V/LED	-	40	-	mA
Power dissipation	P <sub>D</sub>		-	896	-	mW
Uniformity	Avg		75	80	-	%
Drive method	<b>Constant current</b>					
LED Configuration	14 White LEDs(7 LEDs in one string and 2 groups in parallel)					

### 5.2. Backlighting circuit



## 6. Touch Screen Panel Specifications

### 6.1. Electrical Characteristics

Item	Min.	Typ.	Max.	Unit	Note
Linearity	-1.5	-	1.5	%	Analog X and Y directions
Terminal resistance	350	-	1000	$\Omega$	X (Film side)
	100	-	450	$\Omega$	Y (Glass side)
Insulation resistance	20	-	-	$M\Omega$	DC $\leq$ 10V
Voltage	-	-	10	V	DC
Chattering	-	-	10	ms	

Caution (1) : Do not operate it with a thing except a polyacetal pen (tip R0.8mm or less) or a finger, especially those with hard or sharp tips such as a ball point pen or a mechanical pencil.

### 6.2. Mechanical & Reliability Characteristics

Item	Min.	Typ.	Max.	Unit	Note
Activation force	20	-	100	g	(1)
Durability-surface scratching	Write 20,000	-	-	characters	(2)
Durability-surface pitting	1,000,000	-	-	touches	(3)
Surface hardness	3	-	-	H	

Note (1) Stylus pen Input R0.8mm polyacetal pen or Finger

Note (2) Measurement for Surface area

- Force: 150-250gf
- Speed: 60mm/sec
- Stylus: R0.8 polyacetal pen or Finger

Note (3) Pit 1,000,000 times on the Film with a R3.75 silicon rubber.

- Force: Force: 2.45N
- Speed: 3times/sec

## 7. Optical Characteristics

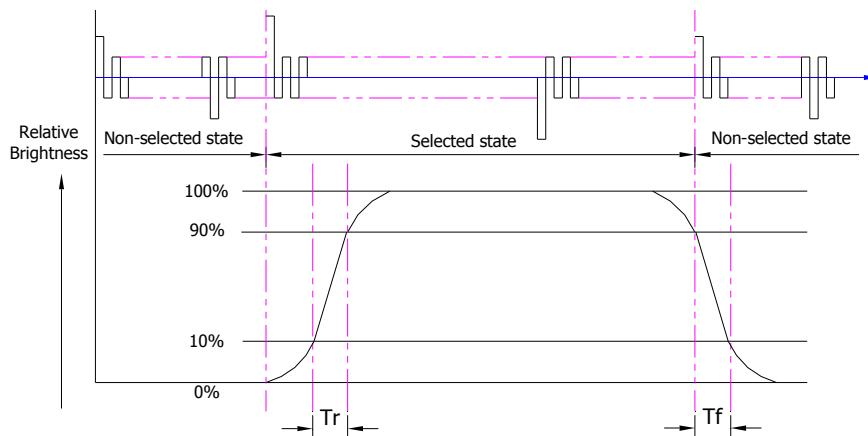
### 7.1. Optical Characteristics

Ta=25°C,  $V_{DD}$ =3.3V, TN LC+ Polarizer

	Item	Symbol	Condition	Specification			Unit	
				Min.	Typ.	Max.		
Backlight On (Transmissive Mode)	Luminance on TFT( $I_f$ =20mA/LED)	Lv	Normally viewing angle $\theta_x = \varphi_y = 0^\circ$	240	300	-	cd/m <sup>2</sup>	
	Contrast ratio(See 7.3)	CR		-	350	-		
	Response time (See 7.2)	Tr+Tf		-	20	-	ms	
Chromaticity Transmissive (See 7.5)	Red	X <sub>R</sub>	Center CR≥10	0.559	0.609	0.659		
		Y <sub>R</sub>		0.307	0.357	0.407		
	Green	X <sub>G</sub>		0.277	0.327	0.377		
		Y <sub>G</sub>		0.560	0.610	0.660		
	Blue	X <sub>B</sub>		0.102	0.152	0.202		
		Y <sub>B</sub>		0.068	0.118	0.168		
	White	X <sub>w</sub>		0.237	0.287	0.337		
		Y <sub>w</sub>		0.280	0.330	0.380		
	Viewing Angle (See 7.4)	Horizontal	Center CR≥10	60	70	-	Deg.	
				60	70	-		
		Vertical		45	50	-		
				60	70	-		
	NTSC Ratio(Gamut)			-	50	-	%	

### 7.2. Definition of Response Time

#### 7.2.1. Normally Black Type (Negative)



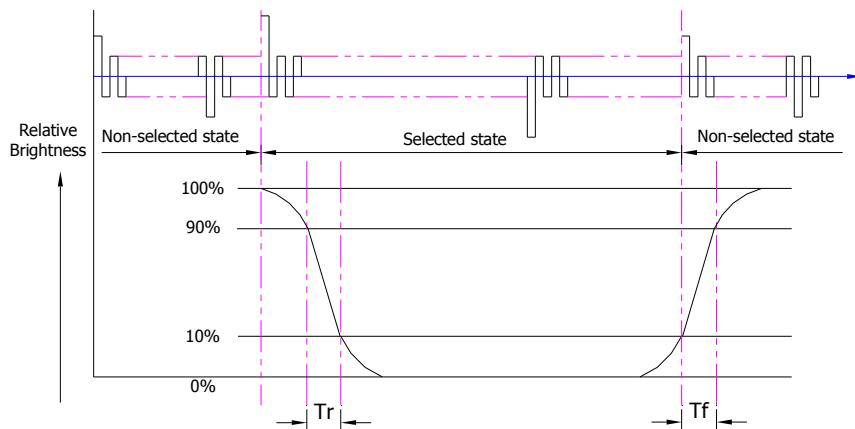
Tr is the time it takes to change from non-selected stage with relative luminance 10% to selected state with relative luminance 90%;

Tf is the time it takes to change from selected state with relative luminance 90% to

non-selected state with relative luminance 10%

Note : Measuring machine: LCD-5100

### 7.2.2. Normally White Type (Positive)



$T_r$  is the time it takes to change from non-selected stage with relative luminance 90% to selected state with relative luminance 10%;

$T_f$  is the time it takes to change from selected state with relative luminance 10% to non-selected state with relative luminance 90%;

Note : Measuring machine: LCD-5100 or EQUI

### 7.3. Definition of Contrast Ratio

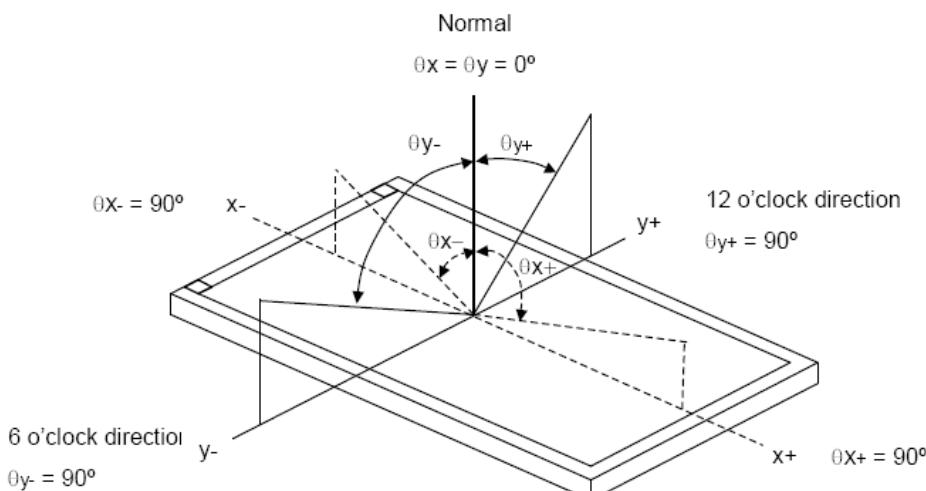
Contrast is measured perpendicular to display surface in reflective and transmissive mode.

The measurement condition is:

Measuring Equipment	Eldim or Equivalent
Measuring Point Diameter	3mm//1mm
Measuring Point Location	Active Area centre point
Test pattern	A: All Pixels white
	B: All Pixel black
Contrast setting	Maximum

Definitions: CR (Contrast) = Luminance of White Pixel / Luminance of Black Pixel

### 7.4. Definition of Viewing Angles



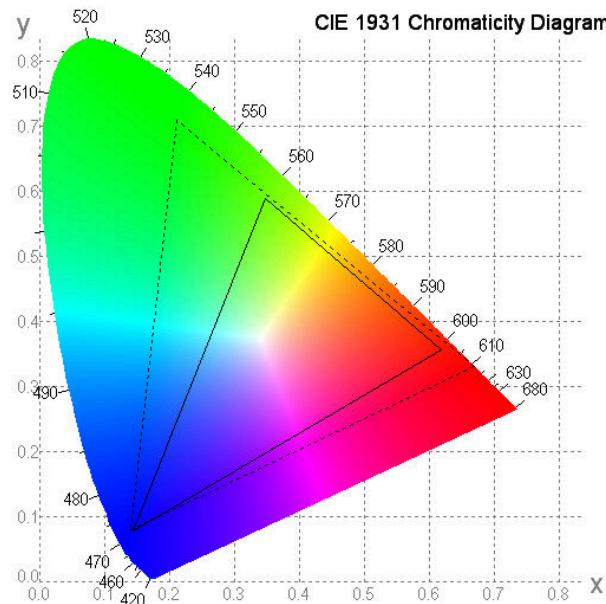
Measuring machine: LCD-5100 or EQUI

## 7.5. Definition of Color Appearance

R,G,B and W are defined by (x, y) on the IE chromaticity diagram

NTSC=area of RGB triangle/area of NTSC triangleX100%

Measuring picture: Red, Green, Blue and White (Measuring machine: BM-7)



## 7.6. Definition of Surface Luminance, Uniformity and Transmittance

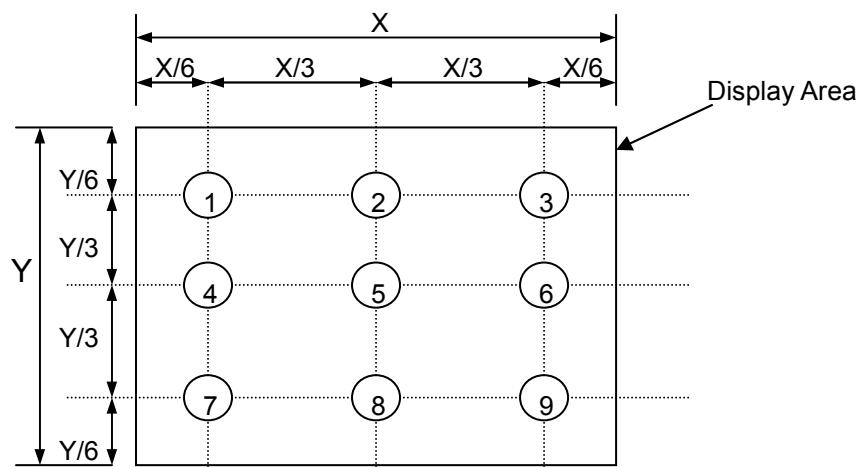
Using the transmissive mode measurement approach, measure the white screen luminance of the display panel and backlight.

7.6.1. Surface Luminance:  $L_V = \text{average } (L_{P1}:L_{P9})$

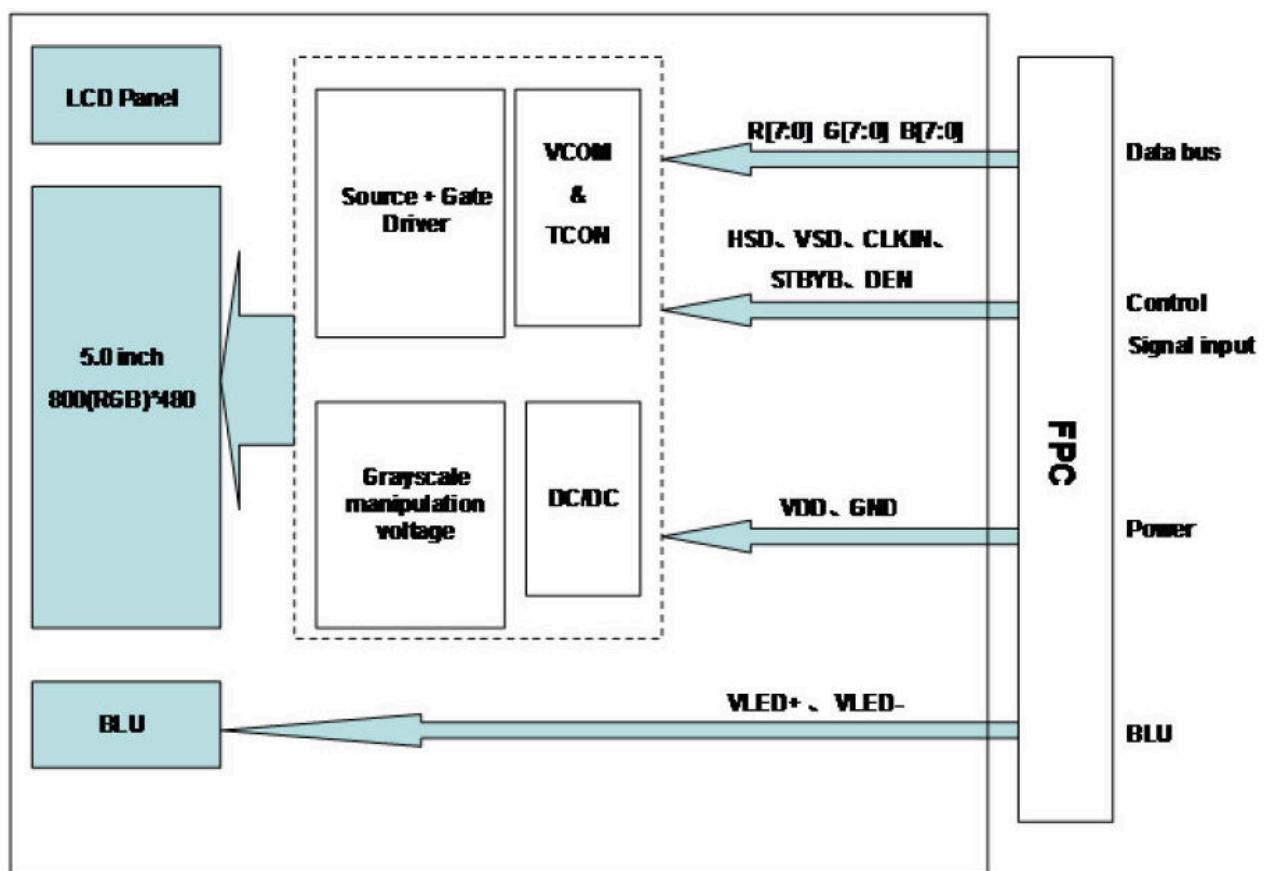
7.6.2. Uniformity = Minimal  $(L_{P1}:L_{P9}) / \text{Maximal } (L_{P1}:L_{P9}) * 100\%$

7.6.3. Transmittance =  $L_V \text{ on LCD} / L_V \text{ on Backlight} * 100\%$

Note: Measuring machine: BM-7



## 8. Block Diagram and Power Supply



## 9. Interface Pins Definition

No.	Symbol	Function	Remark
1	VLED-	Ground (Cathode).	
2	VLED+	LED Input Terminal (Anode).	
3	GND	Ground.	
4	VDD	Power voltage.	
5	R0	Red data (LSB).	
6	R1	Red data.	
7	R2	Red data.	
8	R3	Red data.	
9	R4	Red data.	
10	R5	Red data.	
11	R6	Red data.	
12	R7	Red data (MSB).	
13	G0	Green data (LSB).	
14	G1	Green data.	
15	G2	Green data.	
16	G3	Green data.	
17	G4	Green data.	
18	G5	Green data.	
19	G6	Green data.	
20	G7	Green data (MSB).	
21	B0	Blue data (LSB).	
22	B1	Blue data.	
23	B2	Blue data.	
24	B3	Blue data.	
25	B4	Blue data.	
26	B5	Blue data.	
27	B6	Blue data.	
28	B7	Blue data (MSB).	
29	GND	Ground.	
30	CLKIN	Clock for input data. Data latched at falling edge of this signal.	
31	STBYB	Display on/off.	
32	HSD	Horizontal sync signal.	
33	VSD	Vertical sync signal.	
34	DEN	Data enable.	
35	NC	No connection.	
36	GND	Ground.	
37	NC	No connection.	
38	NC	No connection.	
39	NC	No connection.	
40	NC	No connection.	

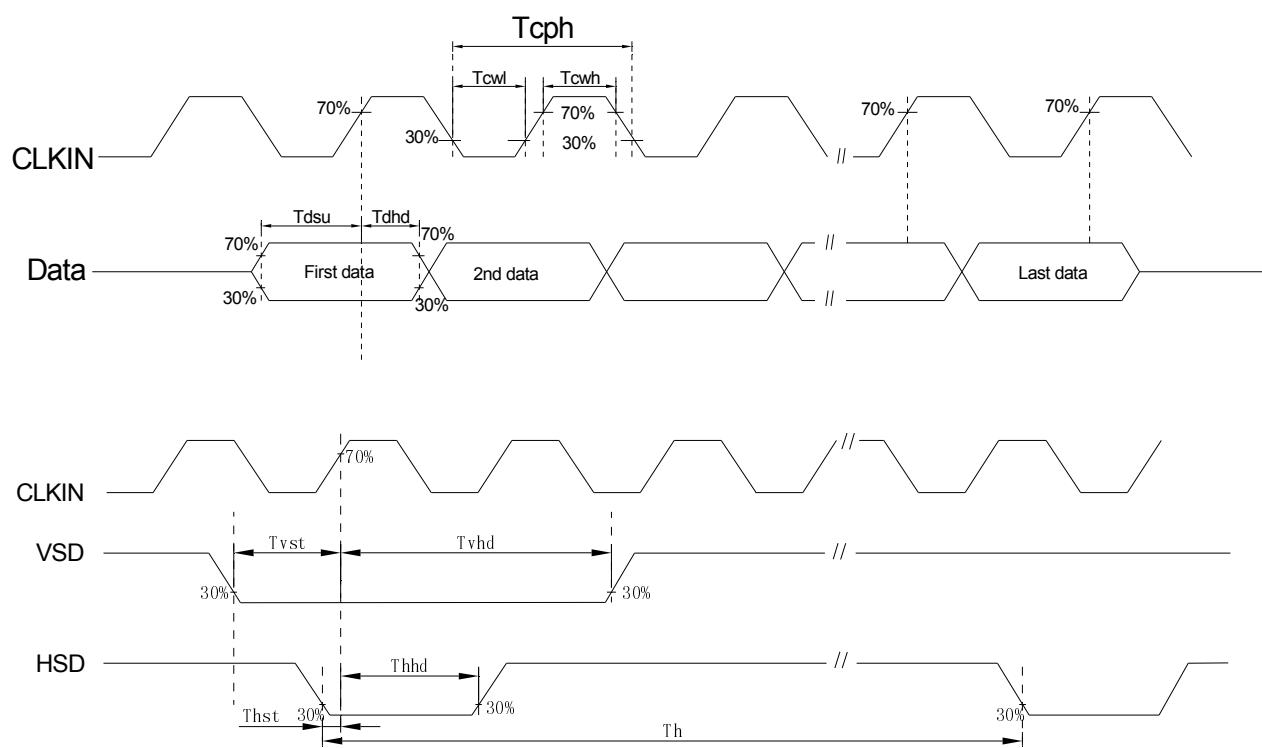
## 10. Timing Chart

### 1) Parallel RGB AC Characteristics

(VDDD=2.3 to 3.6V, AVDD=6.5 to 13.5V, GND=AGND=0V, TA=-20 to +85°C)

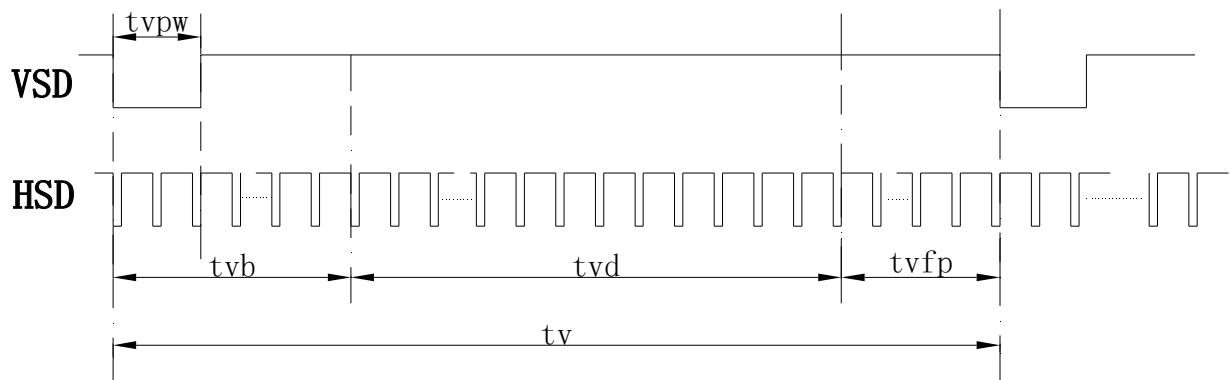
Parameter	Symbol	MIN.	Typ.	MAX.	UNIT	Conditions
CLKIN Frequency	Fclk	-	33	50	MHz	VDDD = 2.3V ~ 3.6V
CLKIN Cycle Time	Tclk	20	30	-	ns	
CLKIN Pulse Duty	Tcwh	40	50	60	%	Tclk
Time from HSD to Source Output	Thso	-	Tld	-	ns	CLKIN
Time from HSD to LD	Thld	-	Tld	-	ns	CLKIN
Time from HSD to STV	Thstv	-	2	-	ns	CLKIN
Time from HSD to CKV	Thckv	-	20	-	ns	CLKIN
Time from HSD to OEV	Thoev	-	4	-	ns	CLKIN
LD Pulse	Twld	-	10	-	ns	CLKIN
CKV Pulse Width	Twckv	-	66	-	ns	CLKIN
OEV Pulse Width	Twoev	-	Tld+10	-	ns	CLKIN

### 2) Input Clock and Data Timing Diagram

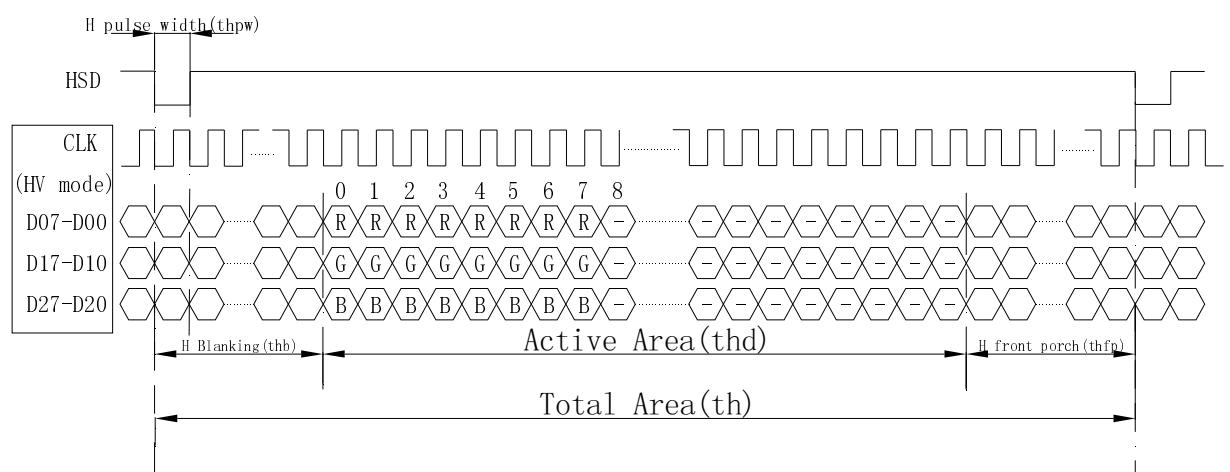


### 3) Data Input Format

#### Vertical input Format



#### Horizontal input timing



**Timing Characteristics****Horizontal input timing**

Parameter	Symbol	Value			Unit	
		Min.	Typ.	Max.		
Horizontal display area	thd	800			DCLK	
DCLK frequency	fclk	-	30	50	MHZ	
1 Horizontal Line	th	928			DCLK	
HSD pulse width	Min.	thpw	1			
	Typ.		48			
	Max.		-			
HSD Back Porch(Blanking)	thb	-	88	-		
HSD Front Porch	thfp	-	40	-		

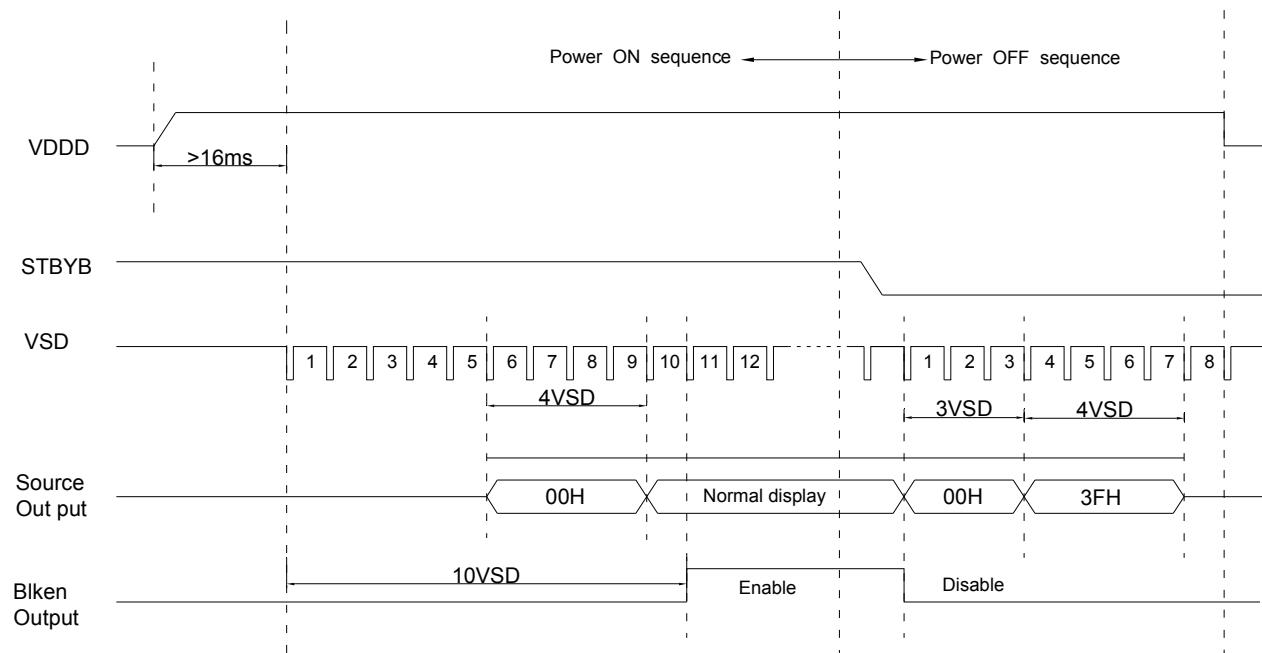
**Vertical input timing**

Parameter	Symbol	Value			Unit
		Min.	Typ.	Max.	
Vertical display area	tvd	480			H
VSD period time	tv	-	525	-	H
VSD pulse width	tvpw	-	3	-	H
VSD Back Porch(Blanking)	tvb	-	32	-	H
VSD Front Porch	tvfp	-	13	-	H

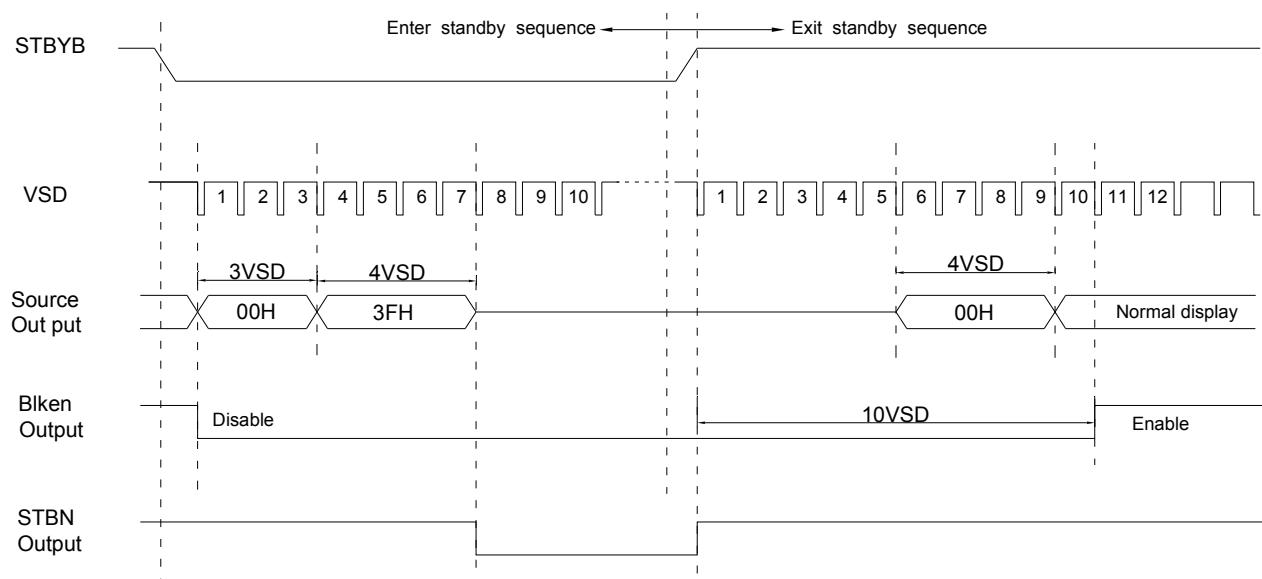
#### 4) Power ON/Off Sequence

In order to prevent IC from power on reset fail, the rising time ( $T_{POR}$ ) of the digital power supply VDD should be maintained within the specifications. Refer to “AC Characteristics” for more detail on timing.

#### Power-On/Off Timing Sequence:



#### Enter and Exit Standby Mode Sequence:



## 11. Quality Assurance

### 11.1. Purpose

This standard for Quality Assurance assures the quality of LCD module products supplied to customer.

### 11.2. Standard for Quality Test

#### 11.2.1 Sampling Plan:

GB2828.1-2012

Single sampling, general inspection level II

#### 11.2.2 Sampling Criteria:

Visual inspection: AQL 1.5%

Electrical functional: AQL 0.65%.

#### 11.2.3 Reliability Test:

Detailed requirement refer to Reliability Test Specification.

### 11.3. Nonconforming Analysis & Disposition

#### 11.3.1 Nonconforming analysis:

11.3.1.1 Customer should provide overall information of non-conforming sample for their complaints.

11.3.1.2 After receipt of detailed information from customer, the analysis of nonconforming parts usually should be finished in one week.

11.3.1.3 If can not finish the analysis on time, customer will be notified with the progress status.

#### 11.3.2 Disposition of nonconforming:

11.3.2.1 Non-conforming product over PPM level will be replaced.

11.3.2.2 The cause of non-conformance will be analyzed. Corrective action will be discussed and implemented.

### 11.4. Agreement Items

Shall negotiate with customer if the following situation occurs:

11.4.1 There is any discrepancy in standard of quality assurance.

11.4.2 Additional requirement to be added in product specification.

11.4.3 Any other special problem.

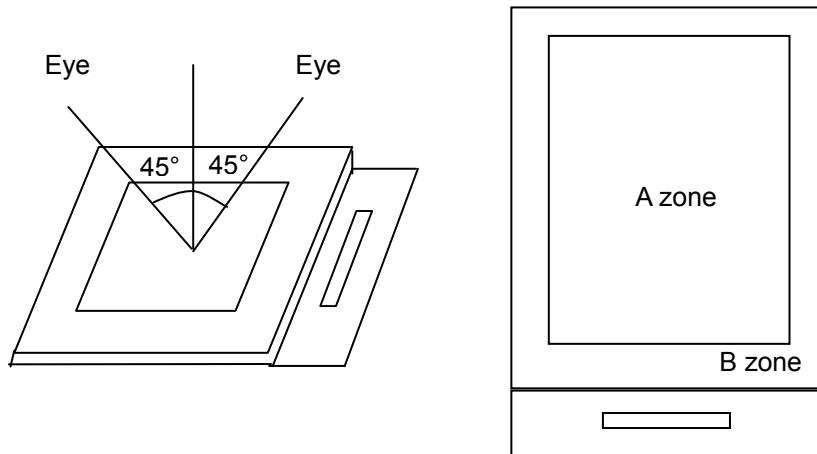
### 11.5. Standard of the Product Visual Inspection

#### 11.5.1 Appearance inspection:

11.5.1.1 The inspection must be under illumination about 1000 – 1500 lx, and the distance of view must be at 30cm ± 2cm.

11.5.1.2 The viewing angle should be 45° from the vertical line without reflection light or follows customer's viewing angle specifications.

11.5.1.3 Definition of area: A Zone: Active Area, B Zone: Viewing Area,

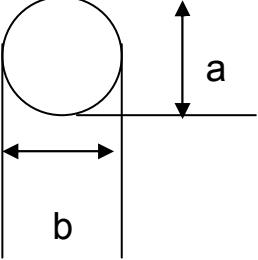


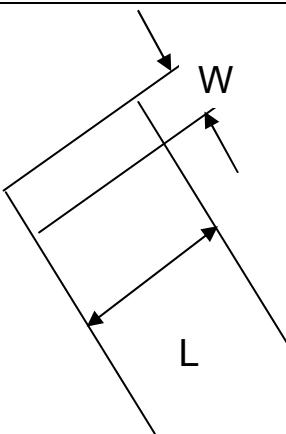
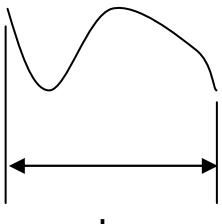
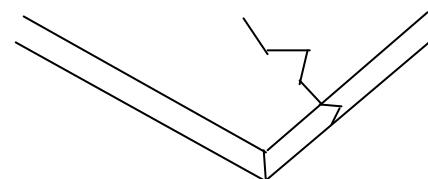
### 11.5.2 Basic principle:

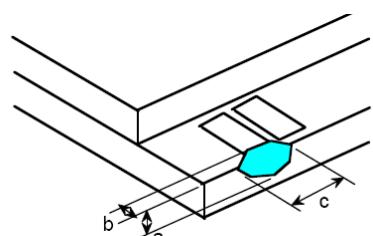
11.5.2.1 A set of sample to indicate the limit of acceptable quality level must be discussed by both us and customer when there is any dispute happened.

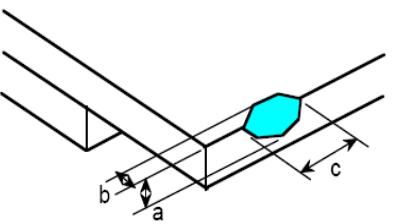
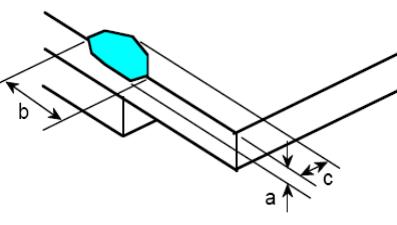
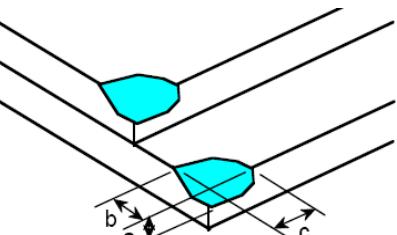
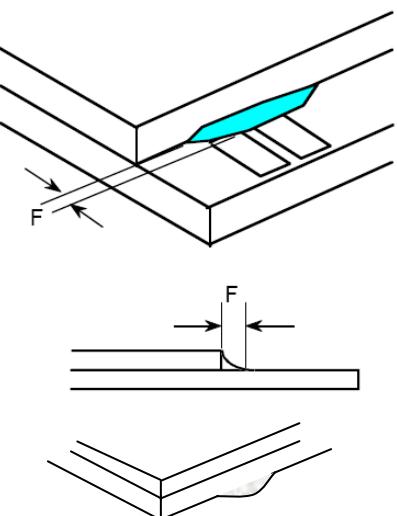
11.5.2.2 New item must be added on time when it is necessary.

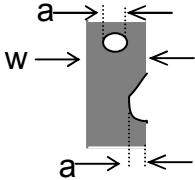
## 11.6. Inspection Specification

No.	Item	Criteria (Unit: mm)														
01	Black / White spot Foreign material (Round type) Pinholes Stain Particles inside cell. (Minor defect)	 $\phi = (a + b) / 2$	<table border="1"> <thead> <tr> <th>Area Size</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td><math>\phi \leq 0.20</math></td> <td>Ignore</td> </tr> <tr> <td><math>0.20 &lt; \phi \leq 0.50</math></td> <td><math>N \leq 3</math></td> </tr> <tr> <td><math>0.50 &lt; \phi</math></td> <td>0</td> </tr> </tbody> </table>	Area Size	Acc. Qty	$\phi \leq 0.20$	Ignore	$0.20 < \phi \leq 0.50$	$N \leq 3$	$0.50 < \phi$	0					
Area Size	Acc. Qty															
$\phi \leq 0.20$	Ignore															
$0.20 < \phi \leq 0.50$	$N \leq 3$															
$0.50 < \phi$	0															
		Distance between 2 defects should more than 5mm apart.														
02	Electrical Defect (Minor defect)	<table border="1"> <thead> <tr> <th>Bright dot</th> <th>Display Area</th> <th>Total</th> <th rowspan="4">Note1</th> </tr> </thead> <tbody> <tr> <td><math>N \leq 2</math></td> <td><math>N \leq 2</math></td> <td><math>N \leq 2</math></td> </tr> <tr> <td><b>Dark dot</b></td> <td><math>N \leq 4</math></td> <td><math>N \leq 4</math></td> </tr> <tr> <td><b>Total dot</b></td> <td><math>N \leq 4</math></td> <td><math>N \leq 4</math></td> </tr> </tbody> </table> <p><b>Mura</b></p> <p>Not visible through 5% ND filters.</p>	Bright dot	Display Area	Total	Note1	$N \leq 2$	$N \leq 2$	$N \leq 2$	<b>Dark dot</b>	$N \leq 4$	$N \leq 4$	<b>Total dot</b>	$N \leq 4$	$N \leq 4$	Note2
Bright dot	Display Area	Total	Note1													
$N \leq 2$	$N \leq 2$	$N \leq 2$														
<b>Dark dot</b>	$N \leq 4$	$N \leq 4$														
<b>Total dot</b>	$N \leq 4$	$N \leq 4$														
		Remark: 1. Bright dot caused by scratch and foreign object accords to item 1.														

03	Black and White line Scratch Foreign material (Line type) (Minor defect)	  <table border="1" data-bbox="595 662 1214 932"> <thead> <tr> <th>Length</th><th>Width</th><th>Acc. Qty</th></tr> </thead> <tbody> <tr> <td>/</td><td><math>W \leq 0.1</math></td><td>Ignore</td></tr> <tr> <td><math>L \leq 2.5</math></td><td><math>0.1 &lt; W \leq 0.2</math></td><td>3</td></tr> <tr> <td><math>L &gt; 2.5</math></td><td><math>0.2 &lt; W</math></td><td>0</td></tr> <tr> <td colspan="2">Total</td><td>3</td></tr> </tbody> </table> <p>Distance between 2 defects should more than 3mm apart. Scratches not viewable through the back of the display are acceptable.</p>	Length	Width	Acc. Qty	/	$W \leq 0.1$	Ignore	$L \leq 2.5$	$0.1 < W \leq 0.2$	3	$L > 2.5$	$0.2 < W$	0	Total		3
Length	Width	Acc. Qty															
/	$W \leq 0.1$	Ignore															
$L \leq 2.5$	$0.1 < W \leq 0.2$	3															
$L > 2.5$	$0.2 < W$	0															
Total		3															
04	Glass Crack (Minor defect)	 <p>Crack is potential to enlarge, any type is not allowed.</p>															

05	Glass Chipping Pad Area: (Minor defect)	 <table border="1" data-bbox="856 1662 1325 1830"> <thead> <tr> <th>Length and Width</th><th>Acc. Qty</th></tr> </thead> <tbody> <tr> <td><math>c &gt; 3.0, b &lt; 1.0</math></td><td>1</td></tr> <tr> <td><math>c &lt; 3.0, b &lt; 1.0</math></td><td>3</td></tr> <tr> <td colspan="2"><math>a &lt; \text{Glass Thickness}</math></td></tr> </tbody> </table>	Length and Width	Acc. Qty	$c > 3.0, b < 1.0$	1	$c < 3.0, b < 1.0$	3	$a < \text{Glass Thickness}$	
Length and Width	Acc. Qty									
$c > 3.0, b < 1.0$	1									
$c < 3.0, b < 1.0$	3									
$a < \text{Glass Thickness}$										

06	<p>Glass Chipping Rear of Pad Area: (Minor defect)</p> 	<table border="1" data-bbox="864 287 1329 505"> <thead> <tr> <th>Length and Width</th><th>Acc. Qty</th></tr> </thead> <tbody> <tr> <td><math>c &gt; 3.0, b &lt; 1.0</math></td><td>1</td></tr> <tr> <td><math>c &lt; 3.0, b &lt; 1.0</math></td><td>2</td></tr> <tr> <td><math>c &lt; 3.0, b &lt; 0.5</math></td><td>4</td></tr> <tr> <td colspan="2"><math>a &lt; \text{Glass Thickness}</math></td></tr> </tbody> </table>	Length and Width	Acc. Qty	$c > 3.0, b < 1.0$	1	$c < 3.0, b < 1.0$	2	$c < 3.0, b < 0.5$	4	$a < \text{Glass Thickness}$	
Length and Width	Acc. Qty											
$c > 3.0, b < 1.0$	1											
$c < 3.0, b < 1.0$	2											
$c < 3.0, b < 0.5$	4											
$a < \text{Glass Thickness}$												
07	<p>Glass Chipping Except Pad Area: (Minor defect)</p> 	<table border="1" data-bbox="864 662 1329 880"> <thead> <tr> <th>Length and Width</th><th>Acc. Qty</th></tr> </thead> <tbody> <tr> <td><math>c &gt; 3.0, b &lt; 1.0</math></td><td>1</td></tr> <tr> <td><math>c &lt; 3.0, b &lt; 1.0</math></td><td>2</td></tr> <tr> <td><math>c &lt; 3.0, b &lt; 0.5</math></td><td>4</td></tr> <tr> <td colspan="2"><math>a &lt; \text{Glass Thickness}</math></td></tr> </tbody> </table>	Length and Width	Acc. Qty	$c > 3.0, b < 1.0$	1	$c < 3.0, b < 1.0$	2	$c < 3.0, b < 0.5$	4	$a < \text{Glass Thickness}$	
Length and Width	Acc. Qty											
$c > 3.0, b < 1.0$	1											
$c < 3.0, b < 1.0$	2											
$c < 3.0, b < 0.5$	4											
$a < \text{Glass Thickness}$												
08	<p>Glass Corner Chipping: (Minor defect)</p> 	<table border="1" data-bbox="864 1044 1329 1179"> <thead> <tr> <th>Length and Width</th><th>Acc. Qty</th></tr> </thead> <tbody> <tr> <td><math>c &lt; 3.0, b &lt; 3.0</math></td><td>Ignore</td></tr> <tr> <td colspan="2"><math>a &lt; \text{Glass Thickness}</math></td></tr> </tbody> </table>	Length and Width	Acc. Qty	$c < 3.0, b < 3.0$	Ignore	$a < \text{Glass Thickness}$					
Length and Width	Acc. Qty											
$c < 3.0, b < 3.0$	Ignore											
$a < \text{Glass Thickness}$												
09	<p>Glass Burr: (Minor defect)</p> 	<table border="1" data-bbox="864 1628 1329 1718"> <thead> <tr> <th>Length</th><th>Acc. Qty</th></tr> </thead> <tbody> <tr> <td><math>F &lt; 1.0</math></td><td>Ignore</td></tr> </tbody> </table> <p>Glass burr don't affect assemble and module dimension.</p>	Length	Acc. Qty	$F < 1.0$	Ignore						
Length	Acc. Qty											
$F < 1.0$	Ignore											

10	FPC Defect: (Minor defect)	 <p>10.1 Dent, pinhole width <math>a &lt; w/3</math>. (w: circuitry width.)</p> <p>10.2 Open circuit is unacceptable.</p> <p>10.3 No oxidation, contamination and distortion.</p>								
11	Bubble on Polarizer (Minor defect)	<table border="1"> <thead> <tr> <th>Diameter</th><th>Acc. Qty</th></tr> </thead> <tbody> <tr> <td><math>\varphi \leq 0.30</math></td><td>Ignore</td></tr> <tr> <td><math>0.30 &lt; \varphi \leq 0.50</math></td><td><math>N \leq 2</math></td></tr> <tr> <td><math>0.50 &lt; \varphi</math></td><td><math>N=0</math></td></tr> </tbody> </table>	Diameter	Acc. Qty	$\varphi \leq 0.30$	Ignore	$0.30 < \varphi \leq 0.50$	$N \leq 2$	$0.50 < \varphi$	$N=0$
Diameter	Acc. Qty									
$\varphi \leq 0.30$	Ignore									
$0.30 < \varphi \leq 0.50$	$N \leq 2$									
$0.50 < \varphi$	$N=0$									
12	Dent on Polarizer (Minor defect)	<table border="1"> <thead> <tr> <th>Diameter</th><th>Acc. Qty</th></tr> </thead> <tbody> <tr> <td><math>\varphi \leq 0.25</math></td><td>Ignore</td></tr> <tr> <td><math>0.25 &lt; \varphi \leq 0.50</math></td><td><math>N \leq 4</math></td></tr> <tr> <td><math>0.50 &lt; \varphi</math></td><td>None</td></tr> </tbody> </table>	Diameter	Acc. Qty	$\varphi \leq 0.25$	Ignore	$0.25 < \varphi \leq 0.50$	$N \leq 4$	$0.50 < \varphi$	None
Diameter	Acc. Qty									
$\varphi \leq 0.25$	Ignore									
$0.25 < \varphi \leq 0.50$	$N \leq 4$									
$0.50 < \varphi$	None									
13	Bezel	<p>13.1 No rust, distortion on the Bezel.</p> <p>13.2 No visible fingerprints, stains or other contamination.</p>								
14	Touch Panel	<p>D: Diameter W: width L: length</p> <p>14.1 Spot: <math>D &lt; 0.25</math> is acceptable  <math>0.25 \leq D \leq 0.4</math>  2dots are acceptable and the distance between defects should more than 10 mm.</p> <p><math>D &gt; 0.4</math> is unacceptable</p> <p>14.2 Dent: <math>D &gt; 0.40</math> is unacceptable</p> <p>14.3 Scratch: <math>W \leq 0.03</math>, <math>L \leq 10</math> is acceptable,  <math>0.03 &lt; W \leq 0.10</math>, <math>L \leq 10</math> is acceptable  Distance between 2 defects should more than 10 mm.  <math>W &gt; 0.10</math> is unacceptable.</p>								
15	LCD Ripple	<p>Touch the touch panel, cannot see the LCD ripple.</p> <p>Pen: R 0.8mm silicon rubber.</p> <p>Operation Force:100g</p>								
16	PCB	<p>16.1 No distortion or contamination on PCB terminals.</p> <p>16.2 All components on PCB must same as documented on the BOM/component layout.</p> <p>16.3 Follow IPC-A-600F.</p>								

17	Soldering	Follow IPC-A-610C standard
18	Electrical Defect (Major defect)	<p>The below defects must be rejected.</p> <p>18.1 Missing vertical / horizontal segment, 18.2 Abnormal Display. 18.3 No function or no display. 18.4 Current exceeds product specifications. 18.5 LCD viewing angle defect. 18.6 No Backlight. 18.7 Dark Backlight. 18.8 Touch Panel no function.</p>

Remark: LCD Panel Broken shall be rejected. Defect out of LCD viewing area is acceptable.

### 11.7. Classification of Defects

11.7.1 Visual defects (Except no / wrong label) are treated as minor defect and electrical defect is major.

11.7.2 Two minor defects are equal to one major in lot sampling inspection.

### 11.8. Identification/marking criteria

Any unit with illegible / wrong /double or no marking/ label shall be rejected.

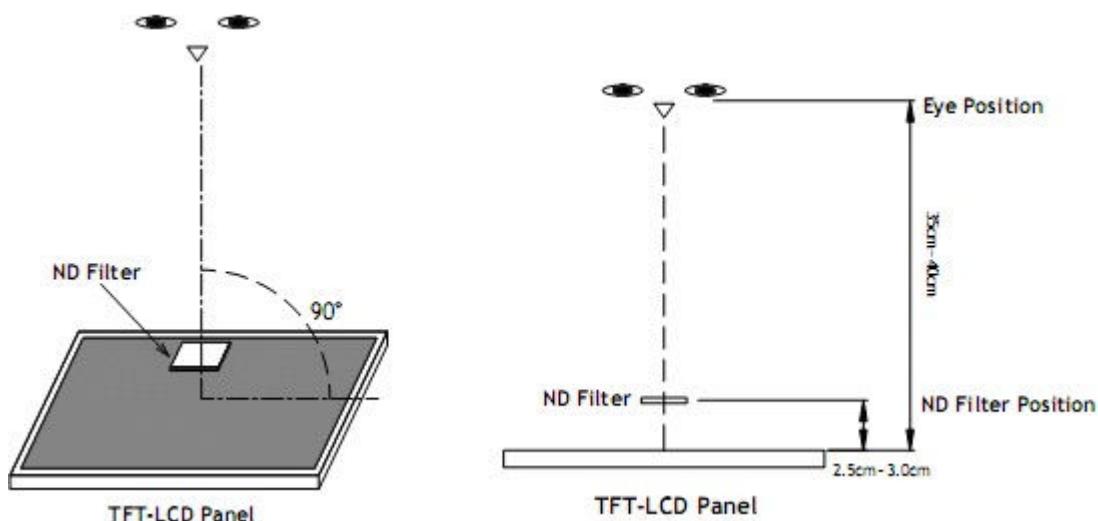
### 11.9. Packaging

11.9.1 There should be no damage of the outside carton box, each packaging box should have one identical label.

11.9.2 Modules inside package box should have compliant mark.

11.9.3 All direct package materials shall offer ESD protection

**Note1:** Bright dot is defined as the defective area of the dot is larger than 50% of one sub-pixel area.



**Bright dot:** The bright dot size defect at black display pattern. It can be recognized by 2% transparency of filter when the distance between eyes and panel is  $350\text{mm} \pm 50\text{mm}$ .

**Dark dot:** Cyan, Magenta or Yellow dot size defect at white display pattern. It can be recognized by 5% transparency of filter when the distance between eyes and panel is  $350\text{mm} \pm 50\text{mm}$ .

**Note2:** Mura on display which appears darker / brighter against background brightness on parts of display area.

## 12. Reliability Specification

No	Item	Condition	Quantity	Criteria
1	High Temperature Operating	<b>70°C, 96Hrs</b>	2	GB/T2423.2 -2008
2	Low Temperature Operating	<b>-20°C, 96Hrs</b>	2	GB/T2423.1 -2008
3	High Humidity	<b>50°C, 90%RH, 96Hrs</b>	2	GB/T2423.3 -2006
4	High Temperature Storage	<b>80°C, 96Hrs</b>	2	GB/T2423.2 -2008
5	Low Temperature Storage	<b>-30°C, 96Hrs</b>	2	GB/T2423.1 -2008
6	Thermal Cycling Test	-20°C, 60min~70°C, 60min, 20 cycles.	2	GB/T2423.22 -2012
7	Packing vibration	Frequency range:10Hz~50Hz Acceleration of gravity:5G X, Y, Z 30 min for each direction.	2	GB/T5170.14 -2009
8	Electrical Static Discharge	Air: $\pm 8KV$ 150pF/330Ω 5 times Contact: $\pm 4KV$ 150pF/330Ω 5 times	2	GB/T17626.2 -2006
9	Drop Test (Packaged)	Height:80 cm,1 corner, 3 edges, 6 surfaces.	2	GB/T2423.8 -1995

Note1. No deflection cosmetic and operational function allowable.

Note2. Total current Consumption should be below double of initial value

## 13. Precautions and Warranty

### 13.1. Safety

13.1.1 The liquid crystal in the LCD is poisonous. Do not put it in your mouth. If the liquid crystal touches your skin or clothes, wash it off immediately using soap and water.

13.1.2 Since the liquid crystal cells are made of glass, do not apply strong impact on them. Handle with care.

### 13.2. Handling

13.2.1 Reverse and use within ratings in order to keep performance and prevent damage.

13.2.2 Do not wipe the polarizer with dry cloth, as it might cause scratch. If the surface of the LCD needs to be cleaned, wipe it swiftly with cotton or other soft cloth soaked with petroleum IPA, do not use other chemicals.

### 13.3. Storage

13.3.1 Do not store the LCD module beyond the specified temperature ranges.

### 13.4. Metal Pin (Apply to Products with Metal Pins)

#### 13.4.1 Pins of LCD and Backlight

13.4.1.1 Solder tip can touch and press on the tip of Pin LEAD during the soldering

##### 13.4.1.2 Recommended Soldering Conditions

Solder Type: Sn96.3~94-Ag3.3~4.3-Cu0.4~1.1

Maximum Solder Temperature: 370 °C

Maximum Solder Time: 3s at the maximum temperature

Recommended Soldering Temp: 350±20 °C

Typical Soldering Time: ≤3s

##### 13.4.1.3 Solder Wetting



#### 13.4.2 Pins of EL

13.4.2.1 Solder tip can touch and press on the tip of EL leads during soldering.

13.4.2.2 No Solder Paste on the soldering pad on the motherboard is recommended.

##### 13.4.2.3 Recommended Soldering Conditions

Solder type: Nippon Alimit Leadfree SR-34, size 0.5mm

Recommended Solder Temperature: 270~290 °C

Typical Soldering Time: ≤2s

Minimum solder distance from EL lamp (body): 2.0mm

13.4.2.4 No horizontal press on the EL leads during soldering.

13.4.2.5 180° bend EL leads three times is not allowed.

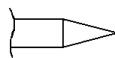
#### 13.4.2.6 Solder Wetting



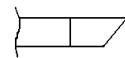
Recommended

Not Recommended

#### 13.4.2.7 The type of the solder iron:

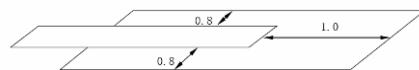


Recommended



Not Recommended

#### 13.4.2.8 Solder Pad



### 13.5. Operation

- 13.5.1 Do not drive LCD with DC voltage
- 13.5.2 Response time will increase below lower temperature
- 13.5.3 Display may change color with different temperature
- 13.5.4 Mechanical disturbance during operation, such as pressing on the display area, may cause the segments to appear “fractured”.

### 13.6. Static Electricity

- 13.6.1. CMOS LSIs are equipped in this unit, so care must be taken to avoid the electro-static charge, by ground human body, etc.
- 13.6.2. The normal static prevention measures should be observed for work clothes and benches.
- 13.6.3. The module should be kept into anti-static bags or other containers resistant to static for storage.

### 13.7. Limited Warranty

- 13.7.1 Our warranty liability is limited to repair and/or replacement. We will not be responsible for any consequential loss.
- 13.7.2 If possible, we suggest customer to use up all modules in six months. If the module storage time over twelve months, we suggest that recheck it before the module be used.
- 13.7.3 After the product shipped, any product quality issues must be feedback within three months, otherwise, we will not be responsible for the subsequent or consequential events.

## **14. Packaging**

TBD

## 15. Outline Drawing

T050800480-A3TMN-068  
Rev:1.0

