



DATA IMAGE CORPORATION

TFT Module Specification Preliminary ITEM NO.: FG0208J0DSSWDGT1

Table of Contents

1.	COVER & CONTENTS ·····	1
2.	RECORD OF REVISION ······	2
3.	GENERAL SPECIFICATIONS ·····	3
4.	ABSOLUTE MAXIMUM RATINGS ······	3
5.	ELECTRICAL CHARACTERISTICS ······	3
6.	PIN CONNECTIONS ·····	4
7.	BLOCK DIAGRAM ·····	5
8.	AC CHARACTERISTICS ·····	6
9.	TOUCH PANEL CHARACTERISTICS	11
10.	OPTICALCHARACTERIST	12
11.	APPEARANCE SPECIFICATIONS FOR VERIFONE	14
12.	QUALITY ASSURANCE ·····	17
13.	LCM PRODUCT LABEL DEFINE ·····	18
14.	PRECAUTIONS IN USE LCM·····	20
15.	OUTLINE DRAWING	21
16.	PACKAGE INFORMATION ······	22

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2. RECORD OF REVISION

Rev	Date	ltem	Page	Comment			
1	18/APR/14'			Initial Preliminary			
2	11/AUG/14'	10	12	Modify Viewing Angle.			



Paramete	er	Specifications	Unit		
Screen Siz	ze	2.8" (diagonal)	inch		
Pixel configu	ration	RGB Stripe			
Display For	mat	240(RGB) X 320	Dot		
LCD Active	Area	57.6(W) x43.2 (H)	mm		
Pixel Siz	е	0.18(W) x0.18(H)	mm		
Outline Dimension		67(W)x49(H)x4.1(T)	mm		
Viewing direction(Gr	ay inversion)	12 o'clock			
Weight		20	g		
Surface Treat	tment	Anti-glare			
Tomporaturo Pango	Operation	-20~70	°C		
Storage		-30~80	°C		
Our components and processes are compliant to RoHS and REACH standard					

3. GENERAL SPECIFICATIONS

4. ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	MIN.	Тур.	MAX.	Unit	Comment
Power supply Voltage	VDD	-0.3		+4.6	V	Notes 1,2
Logic Input Voltage	VIN	-0.3		VDD+0,5	V	

Notes:

1. If the LSI is used above these absolute maximum ratings, it may become permanently damaged. Using the LSI within the following electrical characteristics limit is strongly recommended for normal operation. If these electrical characteristic conditions are also exceeded, the LSI will malfunction and cause poor reliability. 2. VDD, GND must be maintained.

5. ELECTRICAL CHARACTERISTICS

GND=0V,Ta=25°C

ltem	Symbol	Min.	Тур.	Max.	Unit	Remark
Power Supply Voltage	VDD	2.3	-	3.3	V	
Input high voltage	VIH	0.7VDD		VDD	V	
Input low voltage	VIL	0		0.3VDD	V	
Current consumption	IDD		5		mA	



5.1. Backlight driving for power conditions							
ltem	Symbol	Min.	Typical	Max.	Unit	Remark	
LED module Forward voltage	VLED		12.4		V		
LED module current	ILED		20		mA		
LED life time		10,000			Hours	Note 1	

Note 1 The "LED dice life time" is defined as the brightness decrease to 50% original brightness that the ambient temperature is 22 and LED dice current=20mA.



6. PIN CONNECTIONS

Pin No	Symbol	Description
1	GND	Ground
2	GND	Ground
3	GND	Ground
4	TAMPER	Wire to pin 42
5	GND	Ground
6	VDD	power source
7	VDD	power source
8	/CS	Chip select pin
9	SCL	SCL : SPI clock signal
10	SDI	SPI input pin
11	SDO	SPI output pin
12	GND	Ground
13~18	B(0~5)	Blue data
19	GND	Ground
20~25	G(0~5)	Green data
26	GND	Ground
27~32	R(0~5)	Red data
33	GND	Ground
34	LCD_DOE	Data ENABLE signal
35	GND	Ground
36	LCD_CLK	Dot clock signal
37	GND	Ground
38	LCD_HSYNC	Line synchronizing signal
39	LCD_VSYNC	Frame synchronizing signal
40	/RESET	System Reset signal
41	GND	Ground
42	TAMPER_Signal	Wire to pin 4
43	A	LED anode (A)
44	K	LED cathode (K)
45	GND	Ground



7. BLOCK DIAGRAM





8. AC Characteristics 8.1 Serial Interface Characteristics



Figure 8.1.1 Serial Interface Characteristics

Parameter	Symbol	Conditions	Min.	Тур.	Maz.	Unit
Serial clock cycle (Write)	tscycw		20	-	-	
SCL "H" pulse width (Write)	tshw	SCL	8	-	-	ns
SCL "L" pulse width (Write)	tslw		8	-	-	
Data setup time (Write)	tsds	SDIO	10	-	-	200
Data hold time (Write)	tsdh	3010	10	-	-	115
Serial clock cycle (Read)	tscycr		150	-	-	
SCL "H" pulse width (Read)	tshr	SCL	60	-	-	ns
SCL "L" pulse width (Read)	tslr		60	-	-	
Access Time	tacc	SD1_for maximum CL=30pF For minimum CL=8pF	10	-	50	ns
Output disable time	tон	SDO For maximum CL=30pF For minimum CL=8pF	15	-	50	ns
SCL to Chip select	tscc	SCLINCS	20	-	-	ns
NCS "H" pulse width	tchw	NCS	40	-	-	ns
Chip select setup time	tcss	NCC	15	-	-	12.0
Chip select hold time	tсsн	NUS	15	-	-	115

Note: The input signal rise time and fall time (tr, tf) is specified at 15 ns or less.

Logic high and low levels are specified as 30% and 70% of VDD for Input signals.







Figure 8. 2.1 RGB Interface Characteristics

			Lipit		
Item	Symbol	Min	Туре.	Max	Unit
Pixel low pulse width	TCLKLT	15	-	-	ns
Pixel high pulse width	TCLKHT	15	-	-	ns
Vertical Sync. set-up time	TVSST	15	-	-	ns
Vertical Sync. hold time	TVSSH T	15	-	-	ns
Horizontal Sync. set-up time	THSST	15	-	-	ns
Horizontal Sync. hold time	TVSSH T	15	-	-	ns
Data Enable set-up time	TDEST	15	-	-	ns
Data Enable hold time	TDEHT	15	-	-	ns
Data set-up time	TDST	15	-	-	ns
Data hold time	TDHT	15	-	-	ns
Phase difference of sync signal falling edge	Thv	0	-	240	Dotclk

Note: The input signal rise time and fall time (tr, tf) is specified at 15 ns or less.

Input Signal Slope

Output Signal Slope









Characteristics	Symbol	Min	Тур	Max	Unit
Vertical Timing					
Vertical cycle period	TVP	324	326	452	HS
Vertical low pulse width	TVS	2	2	-	HS
Vertical low pulse width	TVFP	2	2	6	HS
Vertical back porch	TVBP	2	4	126	HS
Vertical blanking period	TVBL	4	6	132	HS
Vertical active area	TVDISP		320		HS
Vertical refresh rate	TVDISP	50	60	80	Hz
Horizontal Timing					
Horizontal cycle period	THP	244	252	1008	DOTCLK
Horizontal low pulse width	THS	2	2	256	DOTCLK
Horizontal front porch	THFP	2	4	256	DOTCLK
Horizontal back porch	THBP	2	8	256	DOTCLK
Horizontal blanking period	THBL	4	12	256	DOTCLK
Horizontal active area	THDISP		240		DOTCLK
Pixel clock cycle TVRR=60Hz	fCLKCYC	3.9		16.6	MHz

Note: (1) IOVCC=1.65 to 3.3V, VCI=2.3 to 3.3V, VSSA=VSSD=0V, Ta=-30 to 70 (to +85 no damage)

(2) Data lines can be set to "High" or "Low" during blanking time – Don't care.

(3) HP is multiples of DOTCLK.





Figure 8.3.1 Reset Input Timing

Symbol	Parameter	Related Pins	Min.	Тур.	Max.	Unit	Note
tRESW	Reset low pulse width(1)	NRESET	10	-	-	μs	
	Reset complete time(2)		-	-	5	ms	When reset applied during STB mode
tRESW			-	-	120	ms	When reset applied during STB mode
tPRES	Reset goes high level after Power on time	NRESET & IOVCC	1	-	-	ms	

Note: (1) Spike due to an electrostatic discharge on NRESET line does not cause irregular system reset according to the table below.

NRESET Pulse	Action		
Shorter than 5 µ	Reset Rejected		
Longer than 10 µs	Reset		
Longer than 10 us	Reset Start		

(2) During the resetting period, the display will be blanked (The display is entering blanking sequence,

which maximum time is 120 ms, when Reset Starts in STB Out –mode. The display remains the blank state in STB –mode) and then return to Default condition for H/W reset.

(3) During Reset Complete Time, ID2 and VCOMOF value in OTP will be latched to internal

register during this period. This loading is done every time when there is H/W reset complete time (tREST) within 5ms after a rising edge of NRESET.

(4) Spike Rejection also applies during a valid reset pulse as shown below



(5) It is necessary to wait 5msec after releasing !RES before sending commands. Also STB Out command cannot be sent for 120msec.



9. TOUCH PANEL CHARACTERISTICS

1.Input Method and Activation Force

Input M	ethod	Activation Force	
1.6mm (dia. Delrin stylus	100gf (Max)	

2. Typical Optical Characteristics

ITEM	Parameter
Visible Light Transmission	≥80%
Surface harness	≥3H

3. Electrical Specification

ITEM	Parameter		
Operating Voltage		10Vmax	
Circuit close resistance X		150~450Ω	
	Y	300~850Ω	
Linear Test		≤1.5%	

4. Linearity

ITEM		Parameter
Linear Test Specification Direction	Х	≤1.5%
	Y	≤1.5%

5. Specification

ITEM	Parameter
Operating Temperature	-20°C~+70°C ,240hrs
Storage Temperature	-30°C~+80°C ,240hrs

1. Durability test:

1.1 Finger touches

Touch panel is hit 1 millions times with a silicone rubber of R3.75 finger, The measurement must satisfy the following:

- Circuit close resistance: x 150~450Ω; y 300~850Ω
- Linearity test: X: ≤1.5% ;Y: ≤1.5%



6.2 Stylus writing

Touch panel is drawn by R0.8 Derlin stylus pen, at 150g forces, repeat one inch by 100k times. The measurement must satisfy the following:

- Circuit close resistance: x 150~450Ω; y 300~850Ω
- Linearity test: X: ≤1.5% ;Y: ≤1.5%



ltem		Cumula al	Conditions	Sp	ecificatio	ns	11	Nata	
	nem		Conditions	Min.	Тур.	Max.	Unit	NOLE	
Luminan	Luminance		Center	160	200		Cd/m ²	Note: 4,5	
Uniformit	ty		θx=θy=0°	80			%	Note: 4,5	
Contrast	Ratio	CR		150	250			Note: 1,4	
Response Time		Ton+ Toff			30		ms	Note: 2	
	Red	Rx		0.582	0.632	0.682			
		Ry	Center θx=θy=0°	0.278	0.328	0.378		Note: 4	
	Green	Gx		0.246	0.296	0.346			
Chroma		Gy		0.526	0.576	0.626			
ticity	Blue	Bx		0.083	0.133	0.183		NOLE. 4	
		By		0.072	0.122	0.172			
	White	Wx		0.254	0.304	0.354			
		Wy		0.284	0.334	0.384			
	Hor.	θx+		60	70				
Viewing		θx-	Center	60	70		daa	Note: 2.4	
Angle	Ver	Өу+	CR≥10	60	70		ueg.	NOLE: 3,4	
	ver.	Өу-		40	50		1		

Note (1) Definition of Contrast Ratio (CR):

The contrast ratio can be calculated by the following

expression. Contrast Ratio (CR) = L63 / L0

L63: Luminance of gray level 63

L 0: Luminance of gray level 0

CR =CR (5)

CR (X) is corresponding to the Contrast Ratio of the point X at Figure in Note (5).



Note (2) Definition of Response Time (Ton, Toff):



Note (4) Measurement Set-Up:

The LCD module should be stabilized at a given temperature for 20 minutes to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting Backlight for 20 minutes in a windless room.



Note (5) Definition of Brightness Uniformity (B-uni):





11.1 Inspection condition

11.1.1 Inspection conditions

- 11.1.1.1 Inspection Distance: 35 ± 5 cm
- 11.1.1.2 View Angle:
 - (1) Inspection under operating condition : $\pm 5^{\circ}$
 - (2) Inspection under non-operating condition : $\pm 45^{\circ}$





11.1.2 Environment conditions :

Ambient Te	mperature :	25±5
Ambient H	lumidity :	65±5%
Ambient	Cosmetic Inspection	More than 600lux
Illumination	Functional Inspection	300 ~ 800lux

11.2 Definition of applicable Zones





No.	Parameter	Criteria						
		Display function: N	lo Display mal	function (N	lajor)			
		Contrast ratio (Black, White): Does not meet specified range in the spec. (Major) (Note:3)						
		Line Defect: No ob and colored. (Majo	ovious Vertical or) (Note:1)	and Horizo	ontal line d	lefect in brigl	nt, dark	
		Point Defect (Red,	green, blue, d	ark): Activ	e area ≤4o	dots (Minor)(Note:1)	
		Item	Acceptable	Total	Class	Of AQL		
		Pright	number		Defec	ts Leve	1	
		Dark	3	4				
		Adjacent Bright	1	1	Mino	r 1.5		
		Adjacent Dark	1	1				
1	Operating	Non-uniformity: Visible through 2%	ND filter white	e, R, G, B a	ind gray 50	0%pattern. (Minor)	
•	operating			<u>e spois sn</u> ptable	Class Of	f AQL	<u>)</u>	
		Dimension	numt	prasie	Defects	Level		
		$D \leq 0.3$	*					
	0.3 < D ≤0.5		3	3		1.5		
		D> 0.5						
		D = (Long + Short) / 2 * :		egard	ł	ł		
		Foreign Material in Line or spiral		shape (W:	≤1/4L) (No	ote: 4)		
		Dimens	sion	Accep num	Acceptable number		AQL Level	
		W>0.1mm,L>5m	m	0				
		L 5mm,0.05mm	v <w 0.1mm<="" td=""><td>3</td><td colspan="2">3</td><td>1.5</td></w>	3	3		1.5	
		L 5mm.W<0.05	mm	k	r			
		L: Length W: W	Vidth * : Dis	regard			I	
2	External Inspection (non-operating)	Dimension: Outline	e (Major)					
		Bezel appearance	or)					
		Scratch on the polarize & Touch Panel: (Note:2)				1 .	, , , , , , , , , , , , , , , , , , ,	
		Dimension		Acce nu	eptable mber	Class Of Defects	AQL Level	
		W>0.1mm,L>5mm			0	_		
		L 5mm,0.05mm <w 0.<="" td=""><td></td><td>3</td><td>Minor</td><td>1.5</td></w>			3	Minor	1.5	
		L 5mm,W<0.05	mm		*			
		L: Length W: W	Vidth * : Dis	regard				



	Dent and spots shape on the polarize (Note:2): (Note: 5)				
	Dimonsion	Acceptable	Class Of	AQL	
	Dimension	number	Defects	Level	
	$D \leq 0.3$	*			
	0.3 < D ≤0.5	3	Minor	1.5	
	D> 0.5	0			
	D = (Long + Sho	rt) / 2 * : Di	sregard		-

			Definition
Class of	Major	AQL 0.65%	It is a defect that is likely to result in failure or to reduce materially the usability of the product for the intended function.
0616013	Minor	AQL 1.5%	It is a defect that will not result in functioning problem with deviation classified.

Note:1.(a)Bright point defect is defined as point defect of R,G,B with area >1/2 pixel respectively (b)Dark point defect is defined as visible in full white pattern.

(c)The point defect must under 2% ND Filter visible.

- Note:2 The external inspection should be conducted at the distance 30± 5cm between the eyes of inspector and the panel .
- Note:3 Luminance measurement for contrast ratio is at the distance 50± 5cm between the detective head and the panel with ambient luminance less than 1 lux. Contrast ratio is obtained at optimum view angle.

Note:4 W-Width in mm , L-length of Max.(L1,L2) in mm.



Note:5 Spot Foreign Material (W L/4)



11.4 Sampling Condition

Unless otherwise agree in written, the sampling inspection shall be applied to the incoming inspection of customer. Lot size: Quantity of shipment lot per model.

Sampling type: normal inspection, single sampling Sampling table: MIL-STD-105E Inspection level: Level II



Test Condition

12.1.1 Temperature and Humidity(Ambient Temperature)

Temperature	:	$20 \pm 5^{\circ}C$
Humidity	:	$65 \pm \mathbf{5\%}$

12.1.2 Operation

Unless specified otherwise, test will be conducted under function state.

12.1.3 Container

Unless specified otherwise, vibration test will be conducted to the product itself without putting it in a container.

12.1.4 Test Frequency

In case of related to deterioration such as shock test. It will be conducted only once.

No.	Reliability Test Item & Level	Test Level	Remark
1	High Temperature Storage Test	Ta=80°C,240hrs	IEC68-2-2
2	Low Temperature Storage Test	Ta =-30°C,240hrs	IEC68-2-1
3	High Temperature Operation Test	Ta =70°C,240hrs	IEC68-2-2
4	Low Temperature Operation Test	Ta =-20°C,240hrs	IEC68-2-1
5	High Temperature and High Humidity Operation Test	Ta =60°C,90% RH,240hrs	IEC68-2-3
6	Temperature Cycle Test (No operation)	$-30^{\circ}C \rightarrow +25^{\circ}C \rightarrow +80^{\circ}C,50$ Cycles 30 min 5min 50 min	IEC68-2-14
7	Vibration Test (No operation)	Frequency:10 ~ 55 Hz Amplitude:1.0 mm Sweep Time:11min Test Period:6 Cycles for each Direction of X,Y,Z	IEC68-2-6
8	Shock Test (No operation)	100G, 6ms Direction:± X,± Y,± Z Cycle:3 times	IEC68-2-27
9	Electrostatic Discharge Test (No operation)	150pF,330Ω Air:± 15KV;Contact: ± 12KV 10 times/point;4 points/panel face	IEC-61000-4-2

12.1.5 Test Method



Product Label style:



BarCode Define:





Product Name Define:





- 1. ASSEMBLY PRECAUTIONS
 - (1) You must mount a module using holes arranged in four corners or four sides.
 - (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
 - (3) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment.
 - (4) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
 - (5) Do not open the case because inside circuits do not have sufficient strength.
 - (6) Please do not take a LCD module to pieces and reconstruct it. Resolving and reconstructing modules may cause them not to work well.
 - (7) Please do not touch metal frames with bare hands and soiled gloves. A color change of the metal frames can happen during a long preservation of soiled LCD modules.
 - (8) Please pay attention to handling lead wire of backlight so that it is not tugged in connecting with inverter.
- 2. OPERATING PRECAUTIONS
 - (1) Please be sure to turn off the power supply before connecting and disconnecting signal input cable.
 - (2) Please do not change variable resistance settings in LCD module. They are adjusted to the most suitable value. If they are changed, it might happen LCD does not satisfy the characteristics specification
 - (3) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
 - (4) When fixed patterns are displayed for a long time, remnant image is likely to occur.
 - (5) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimize the interference.
 - (6) Please consider that LCD backlight takes longer time to become stable of radiation characteristics in low temperature than in room temperature.
- 3. ELECTROSTATIC DISCHARGE CONTROL
 - (1) The operator should be grounded whenever he/she comes into contact with the module. Never touch any of the conductive parts such the copper leads on the PCB and the interface terminals with any parts of the human body.

- (2) The modules should be kept in antistatic bags or other containers resistant to static for storage.
- (3) Only properly grounded soldering irons should be used.
- (4) If an electric screwdriver is used, it should be well grounded and shielded from commutator sparks.
- (5) The normal static prevention measures should be observed for work clothes and working benches; for the latter conductive (rubber) mat is recommended
- (6) Since dry air is inductive to statics, a relative humidity of 50-60% is recommended.
- 4. STORAGE PRECAUTIONS
 - (1) When you store LCDs for a long time, it is recommended to keep the temperature between 0°C-40°C without the exposure of sunlight and to keep the humidity less than 90%RH.
 - (2) Please do not leave the LCDs in the environment of high humidity and high temperature such as 60°C 90%RH
 - (3) Please do not leave the LCDs in the environment of low temperature; below -20°C.
- 5. OTHERS
 - (1) A strong incident light into LCD panel might cause display characteristics' changing inferior because of polarizer film, color filter, and other materials becoming inferior. Please do not expose LCD module direct sunlight Land strong UV rays
 - (2) Please pay attention to a panel side of LCD module not to contact with other materials in preserving it alone.
 - (3) For the packaging box, please pay attention to the followings:
 - (4) Please do not pile them up more than 5 boxes. (They are not designed so.) And please do not turn over.
 - (5) Please handle packaging box with care not to give them sudden shock and vibrations. And also please do not throw them up.
 - (6) Packing box and inner case for LCDs are made of cardboard. So please pay attention not to get them wet. (Such like keeping them in high humidity or wet place can occur getting them wet.)
- 6. LIMITED WARRANTY

Unless otherwise agreed between DATA IMAGE and customer, DATA IMAGE will replace or repair any of its LCD and LCM which is found to be defective electrically and visually when inspected in accordance with DATA IMAGE acceptance standards, for a period on one year from date of shipment. Confirmation of such date shall be based on freight documents. The warranty liability of DATA IMAGE is limited to repair and/or replacement on the terms set forth above. DATA IMAGE will not responsible for any subsequent or consequential events.



Confidential Document **15. OUTLINE DRAWING**





. Anti-statio \$ LCM (2) (1) Item Size(L*W*H) Quantity Note 1.Master Carton 482*282*279 1 2.Inner Carton 267*224*115 4 40 Quantity Per Inner Carton Quantity Per Master Carton 160 $N \cdot W$ $G \cdot W$ 11.6 (kg) 9.6 (kg)

16. PACKAGE INFORMATION