# **SPECIFICATIONS**

PRODUCT : LCD MODULE

**MODEL NO.** : S69507A-DF025MB

CUSTOMER NO.: DS11005200

	SUCCESS	CUSTOMER	
PREPARED	CHECKED	APPROVED	APPROVED
XiangGS	Zeng ZD	WangYF	FOR APPROVAL  MC'TRONIC  MC'Tronic s.r.l società unipersonale Via Novara, 35 28010 VARPICIO D'AGGGNA (NO)
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■APPROVAL FOR SPECIFICATIONS AND SAMPLE

## 深圳市宇顺电子股份有限公司

#### SUCCESS ELECTRONICS LTD

"Not to use the substances and their applications of SUCCESS Management Standard for Environment-related Substances to be Controlled "

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# **RECORDS OF REVISION**

DATE	REVISED NO.	REVISED DESCRIPTIONS	PREPARED	CHECKED	APPROVED
2021-12-9	01	New release	Xianggs	Zengzhaopeng	WangYF
2022-2-18	02	Item 8.0:Update initial code	Xianggs	Zengzhaopeng	WangYF



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#### 1. GENERAL SPECIFICATIONS

#### 1-1.DESCRIPTION:

The S69507A-DD025MB is a dot matrix mono Liquid Crystal Display Module(LCM). This specification covers the delivery requirements for the liquid crystal display delivered by SUCCESS ELECTRONIC to Customer.

#### 1-2. FEATURES

(1) Display Type: DFSTN, Negative, Transmissive, 60'clock, Z Size Compensate

(2) Driving Method: VDD=3.3V, 1/65 duty, 1/9 bias

(3) Built-in controller: ST7567(4) With White Backlight

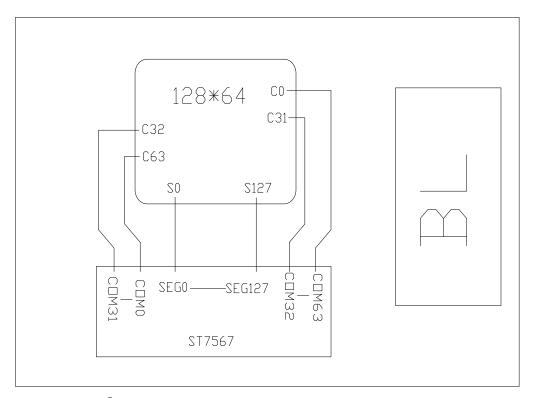
#### 1-3.GENERAL SPECIFICATION

ITEM	SPECIFICATIONS	UNIT
DISP.CONSTRUCTION	128*64 Dots	
OUTLINE DIMEMSIONS	67.15(W)*48.15(H)*5.30(T)( Not Included FPC)	mm
VIEWING AREA	60.00(W) x31.40(H)	mm
ACTIVE AREA	57.57(W) x 28.77(H)	mm
DOT SIZE	0.42(W) x 0.42(H)	mm
DOT PITCH	0.45(W) x 0.45(H)	mm
ASSY.TYPE	COG+FPC+BL	
INTERFACE	6800/SPI	
BACKLIGHT	White	



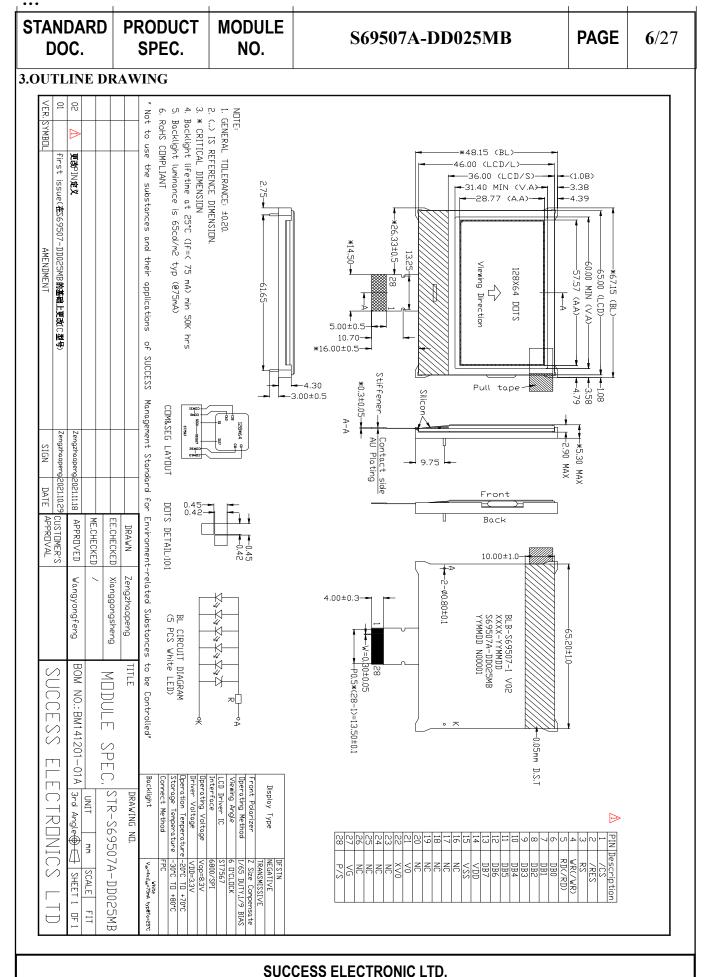
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#### 2. BLOCK DIAGRAM



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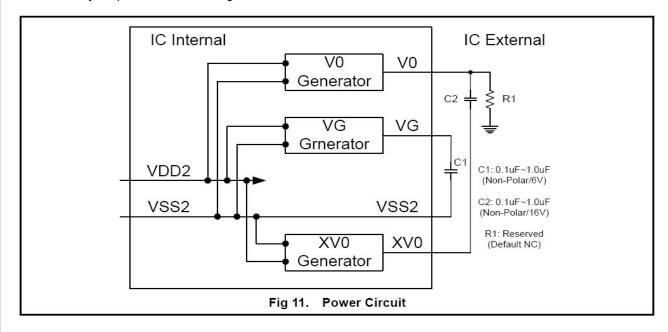
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NTERFAC	E ASSIGNMENT				
1	Chip select input	pin		/CS	
2	Reset input pin			/RE	S
3	Data/instruction s	selection pin		RS	
4	Read/Write signa When R/W = "1" When R/W = "1"	H": Read		WR	(/WR)
5	Enable signal, sta	Enable signal, start data read/write			
6~13	8 bit data Bus Lir	ie		DB(	)~DB7
14	Logic power supp	oly, +3.3V		VDI	)
15	Ground, 0V			VSS	5
16~20	NO CONNECT			NC	
21	V0 is the LCD dr	iving voltage for co	ommon circuits at negative frame	V0	
22	XV0 is the LCD	driving voltage for	common circuits at positive frame.	XV	)
23~26	NO CONNECT			NC	
27	VG is the LCD d	riving voltage for se	egment circuits	VG	
28	This pin configur	es the interface to p	parallel mode or serial mode.	P/S	



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#### **5.APPLICATION CIRCUIT**

The recommended external power components need only 2 capacitors. The detailed values of these two capacitors are determined by the panel size and loading.



#### **Regulator Circuit**

The built-in high accuracy regulation circuit has 8 regulation ratios and each one has 64 EV-levels for voltage adjustment. Without additional external component, the output voltage can be changed by instructions such as "Regulation Ratio" and "Set EV". The detailed setting method can be found in the INSTRUCTION DESCRIPTION section.

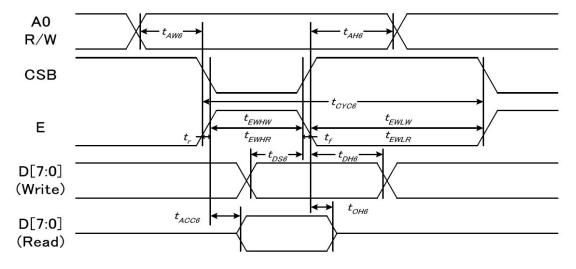


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#### 6. TIMING CHARACTERISTICS

6-1 For the 6800 series MPU(Default Mode)

#### System Bus Timing for 6800 Series MPU



(VDD1 = 3.3V , Ta =25°C)

Item	Signal	Symbol	Condition	Min.	Max.	Unit
Address setup time	AO	tAW6		0	_	
Address hold time	AU	tAH6		10	_	
System cycle time		tCYC6		240	_	
Enable L pulse width (WRITE)		tEWLW		80	4 <u></u>	
Enable H pulse width (WRITE)	E	tEWHW		80	_	
Enable L pulse width (READ)		tEWLR		80	, <del></del>	ns
Enable H pulse width (READ)		tEWHR		140		
Write data setup time		tDS6		40	_	
Write data hold time	D[7:0]	tDH6		10	_	
Read data access time	[0.7]	tACC6	CL = 16 pF	_	70	
Read data output disable time		tOH6	CL = 16 pF	5	50	

(VDD1 = 2.8V , Ta =25°C)

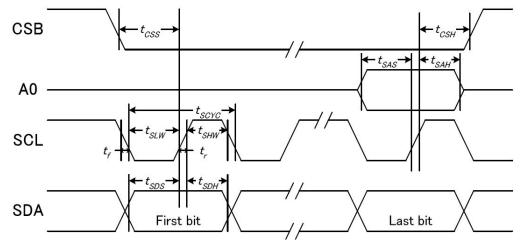
	1				71 - 2.0V ,	
ltem	Signal	Symbol	Condition	Min.	Max.	Unit
Address setup time	A0	tAW6		0	_	
Address hold time	AU	tAH6		0	_	
System cycle time		tCYC6		400	·	
Enable L pulse width (WRITE)		tEWLW		220	-	
Enable H pulse width (WRITE)	E	tEWHW		180	_	
Enable L pulse width (READ)		tEWLR		220		ns
Enable H pulse width (READ)		tEWHR		180	1—1	
Write data setup time		tDS6		40	_	
Write data hold time	D(7:0)	tDH6		20	·	
Read data access time	D[7:0]	tACC6	CL = 16 pF	-	140	
Read data output disable time		tOH6	CL = 16 pF	10	100	



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6-2 For 4-Line Serial Interface

#### System Bus Timing for 4-Line Serial Interface



(VDD1 = 3.3V , Ta =25°C)

200707	50000	9962 00 05.00	9500000	17.311-95.07	-2007	ACT 105 (000)
Item	Signal	Symbol	Condition	Min.	Max.	Unit
Serial clock period		tSCYC		50	_	
SCLK "H" pulse width	SCLK	tSHW		25	_	
SCLK "L" pulse width		tSLW		25		
Address setup time	40	tSAS		20		
Address hold time	A0	tSAH		10	_	ns
Data setup time	SDA	tSDS		20	=	1
Data hold time	SDA	tSDH		10	_	
CSB-SCLK time	CCD	tCSS		20	_	
CSB-SCLK time	CSB	tCSH		40	_	

(VDD1 = 2.8V , Ta =25°C)

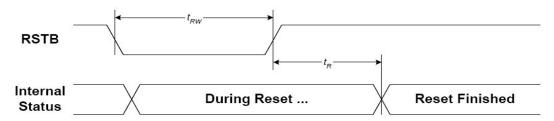
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Item	Signal	Symbol	Condition	Min.	Max.	Unit
Serial clock period		tSCYC		100	-	
SCLK "H" pulse width	SCLK	tSHW		50	<del>-</del> -:	
SCLK "L" pulse width		tSLW		50	_	
Address setup time	A0	tSAS		30	_	
Address hold time	Au	tSAH		20		ns
Data setup time	CD4	tSDS		30		
Data hold time	SDA	tSDH		20	-	
CSB-SCLK time	CCP	tCSS		30		
CSB-SCLK time	CSB	tCSH		60	_	



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#### 6-3 RESET INPUT TIMING

#### **Hardware Reset Timing**



(VDD1 = 3.3V , Ta =25°C)

Item	Symbol	Condition	Min.	Max.	Unit
Reset time	tR		<del></del> /	1.0	
Reset "L" pulse width	tRW		1.0	1 - 1 - 1 - 1 - 1 - 1 - 1 - 1 - 1 - 1 -	us

(VDD1 = 2.8V , Ta =25°C)

Item	Symbol	Condition	Min.	Max.	Unit
Reset time	tR		==0	2.0	
Reset "L" pulse width	tRW		2.0	=	us

(VDD1 = 1.8V , Ta =25°C)

Item	Symbol	Condition	Min.	Max.	Unit
Reset time	tR		3.0	3.0	
Reset "L" pulse width	tRW			9 <u></u>	us



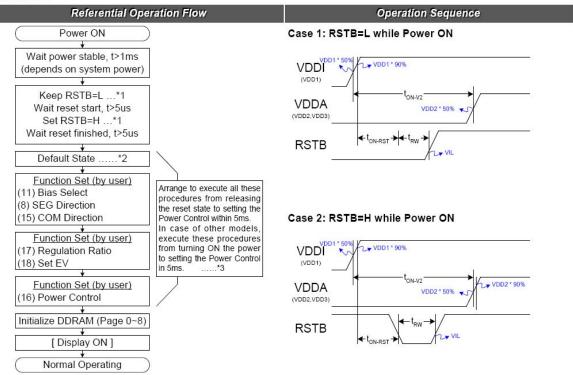
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#### 7. POWER ON/OFF SEQUENCE

#### (1)POWER ON:

This section introduces some reference operation flows.

#### Power ON



Note: The detailed description can be found in the respective sections listed below.

- 1. Please refer to the timing specification of  $t_{\text{RW}}$  and  $t_{\text{R}}$ .
- 2. Refer to Section RESET CIRCUIT.
- 3. The 5ms requirement depends on the characteristics of LCD panel and the external component of the power circuit. It is recommended to check with the real products with external component.
- 4. The detailed instruction functionality is described in Section 9. INSTRUCTION DESCRIPTION;
- 5. Power stable is defined as the time that the later power (VDDI or VDDA) reaches 90% of its rated voltage.

#### Timing Requirement:

Item	Symbol	Requirement	Note
VDDA power delay	t <sub>ON-V2</sub>	0 ≤ t <sub>ON-V2</sub>	Applying VDDI and VDDA in any order will not damage IC.
RSTB input time	ton-rst	No Limitation	If RSTB is Low, High or unstable during power ON, a successful hardware reset by RSTB is required after VDDI is stable.      RSTB=L can be input at any time after power is stable.      t <sub>RW</sub> & t <sub>R</sub> should match the timing specification of RSTB.      To prevent abnormal display, the recommended timing is: 0 ≤ t <sub>ON-RST</sub> ≤ 30 ms.

The requirement listed here is to prevent abnormal display on LCD module.

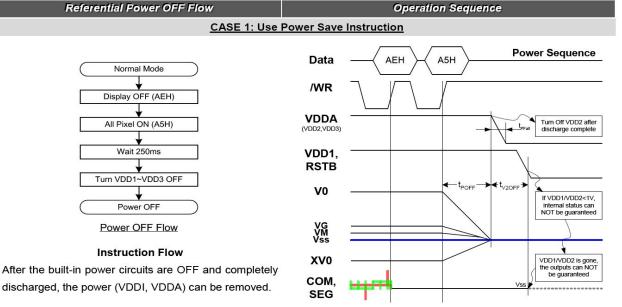


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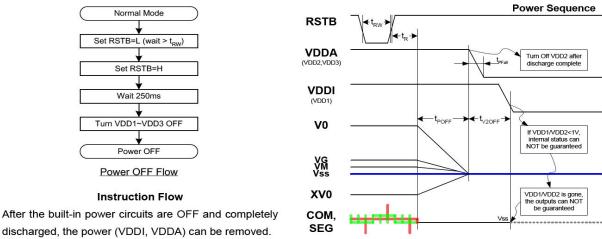
#### (2)POWER OFF:

#### **Power OFF Flow and Sequence**

In power save mode, LCD outputs are fixed to VSS and all analog outputs are discharged. The power can be turned OFF after ST7567 is in the power save mode. The power save mode can be triggered by the following two methods.



#### CASE 2: Use Hardware Reset Function



discharged, the power (VDDI, VDDA) can be removed. Note:

#### tPOFF: Internal Power discharge time. => 250ms (max). 1.

- t<sub>V2OFF</sub>: Period between VDDI and VDDA OFF time. => 0 ms (min). 2.
- It is NOT recommended to turn VDDI OFF before VDDA. Without VDDI, the internal status cannot be guaranteed and internal discharge-process maybe stopped. The un-discharged power maybe flows into COM/SEG output(s) and the liquid crystal in panel maybe polarized.
- 4. IC will NOT be damaged if either VDDI or VDDA is OFF while another is ON.
- The timing is dependent on panel loading and the external capacitor(s).
- The timing in these figures is base on the condition that: LCD Panel Size = 1.4" with C1=1uF, C2=1uF. 6.
- When turning VDDA OFF, the falling time should follow the specification:  $20\text{ms} \le t_{Pfall} \le 0.2\text{sec}$



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#### 8. RECOMMENDED INITIAL CODES

```
void Initial(void)
  CS=0;
  RES=1; DelayuS(5000);
  RES=0;
           DelayuS(5000);
  RES=1; DelayuS(5000);
                DelayuS(3); // RESET
  writec(0xe2);
  writec(0xa2);
                DelayuS(3); // 1/9 bias
  writec(0xa0); DelayuS(3); //ADC select , Normal
  writec(0xc8);
                DelayuS(3); //Common output reverse
  writec(0xf8);
                DelayuS(3); //4 booster
  writec(0x00);
                DelayuS(3);
  writec(0x24);
                DelayuS(3); // internal resistor ratio
                DelayuS(3);
  writec(0x81);
                             //electronic volume mode set
  writec(28);
                 DelayuS(3); // electronic volume
  writec(0x2c);
                DelayuS(1000); //Power Control1
  writec(0x2e);
                DelayuS(1000); //Power Control2
  writec(0x2f);
                DelayuS(1000); //Power Control3
  writec(0x40);
                               //Set Start Line
                DelayuS(3);
  writec(0xaf);
                DelayuS(3);
                                // display ON
```



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#### 9.INSTRUCTION TABLE

INCTRUCTION		R/W			С	OMMAI	ND BY1	E			DECORIDATION	
INSTRUCTION	A0	(RWR)	<b>D</b> 7	D6	D5	D4	D3	D2	D1	D0	DESCRIPTION	
(1) Display ON/OFF	0	0	1	0	1	0	1	1	1	D	D=1, display ON D=0, display OFF	
(2) Set Start Line	0	0	0	1	S5	S4	S3	S2	S1	SO	Set display start line	
(3) Set Page Address	0	0	1	0	1	1	Y3	Y2	Y1	Y0	Set page address	
(4)	0	0	0	0	0	1	X7	X6	X5	X4	Set column address (MSB)	
Set Column Address	0	0	0	0	0	0	Х3	X2	X1	X0	Set column address (LSB)	
(5) Read Status	0	1	0	MX	D	RST	0	0	0	0	Read IC Status	
(6) Write Data	1	0	D7	D6	D5	D4	D3	D2	D1	D0	Write display data to RAM	
(7) Read Data	1	1	D7	D6	D5	D4	D3	D2	D1	D0	Read display data from RAM	
(8) SEG Direction	0	0	1	0	1	0	0	0	0	MX	Set scan direction of SEG MX=1, reverse direction MX=0, normal direction	
(9) Inverse Display	0	0	1	0	1	0	0	1	1	INV	INV =1, inverse display INV =0, normal display	
(10) All Pixel ON	0	0	1	0	1	0	0	1	0	AP	AP=1, set all pixel ON AP=0, normal display	
(11) Bias Select	0	0	1	0	1	0	0	0	1	BS	Select bias setting 0=1/9; 1=1/7 (at 1/65 duty)	
(12) Read-modify-Write	0	0	1	1	1	0	0	0	0	0	Column address increment: Read:+0 , Write:+1	
(13) END	0	0	1	1	1	0	1	1	1	0	Exit Read-modify-Write mode	
(14) RESET	0	0	1	1	1	0	0	0	1	0	Software reset	
(15) COM Direction	0	0	1	1	0	0	MY		•	•	Set output direction of COM MY=1, reverse direction MY=0, normal direction	
(16) Power Control	0	0	0	0	1	0	1	VB	VR	VF	Control built-in power circuit ON/OFF	
(17) Regulation Ratio	0	0	0	0	1	0	0	RR2	RR1	RR0	Select regulation resistor ratio	
(19) Cot E\/	0	0	1	0	0	0	0	0	0	1	Double command!! Set	
(18) Set EV	0	0	0	0	EV5	EV4	EV3	EV2	EV1	EV0	electronic volume (EV) level	
	0	0	1	1	1	1	1	0	0	0	Double command	
(19) Set Booster	0	0	0	0	0	0	0	0	0	BL	Set booster level: BL=0: 4X BL=1: 5X	
(20) Power Save	0	0			Co	mpound	Comm	and			Display OFF + All Pixel ON	
(21) NOP	0	0	1	1	1	0	0	0	1	1	No operation	
(22) Test	0	0	1	1	1	1	1	1	1	TE	Test Command Moe TE=0: releasing test comman mode TE=1: entering test command mode	

Note: Symbol "-" means this bit can be "H" or "L".



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#### 10.ELECTRICAL CHARACTERISTICS

### 10-1 Absolute maximum ratings

ITEM	CVMDOI	STA	NDARD '	INIT		
ITEM	SYMBOL	MIN	MIN TYP MAX		UNIT	
SUPPLY VOLTAGE FOR LOGIC	VDD	-0.3		4	V	
LCD POWER SUPPLY VOLTAGE	V0-XV0	-0.3		16	V	
OPERATING TEMPERATURE	T <sub>OP</sub>	-20		70	$^{\circ}\!\mathbb{C}$	
STORAGE TEMPERATURE	T <sub>STG</sub>	-30		80	$^{\circ}\mathbb{C}$	

#### 10-2 Electrical characteristics

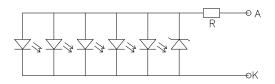
ITEM	CVMDOI	SYMBOL CONDITIONS STANDARD VALUE		ALUE	UNIT	
ITEM	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNII
SUPPLY VOLTAGE FOR LOGIC	VDD		3.2	3.3	3.4	V
SUPPLY VOLTAGE FOR LCD	V0-XV0		8.1	8.3	8.5	V
INPUT VOLTAGE "H" LEVEL	VIH		0.7VDD	-	VDD	V
INPUT VOLTAGE "L" LEVEL	VIL		VSS	-	0.3VDD	V
OUTPUT VOLTAGE "H" LEVEL	VOH	Ta= 25°C	Ta= 25°C		VDD	V
OUTPUT VOLTAGE "L" LEVEL	VOL		VSS	-	0.2VDD	V
CURRENT CONSUMPTION (CHECKER)	IDD		-	0.5	1.0	mA
POWER CONSUMPTION (CHECKER)	PC		-	1.65	3.3	mW



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#### 11. LED BACKLIGHT

11-1 LED CIRCUIT:



#### 11-2.ABSOLUTE MAXIMUN RATING

PARAMETER	SYMBOL	SPECIFICATIONS	UNIT
POWER DISSIPATION	PD	400	mW
FORWARD CURRENT	Ifm	100	mA
PEAK FORWARD CURRENT	Ifp	300	mA
REVERSE VOLTAGE	Vr	5	V
OPERATION TEMPERATURE	TOPR	-20°C∼+70°C	$^{\circ}$
STORAGE TEMPERATURE	TSTG	-30°C∼+80°C	$^{\circ}$

#### 11-3. ELECTRICAL CHARACTERISTICS (Ta=25°C)

PARAMETER	SYMBOL	LIGHT	CONDITIONS	STANDARD VALUE			UNIT
		SOURCE		MIN TYP MA		MAX	
FORWARD CURRENT	If		Vak=4.0V	1	75	100	mA
REVERSE CURRENT	IR	White	Vr= 5V/LED	-	-	10	uA
BL BRIGHTNESS	Lv		Vak=4.0V	800	1000	-	cd/m <sup>2</sup>
CIE Color	X		Vak=4.0V	0.24	0.27	0.30	-
Coordinate(without LCD)	Y		vak=4.0V	0.24	0.27	0.30	-
LUMINOUS UNIFORMITY	Δ		Vak=4.0V	75%	-	-	%.

Note: Uniformity  $\Delta$ =(minmum LV/maximum LV)\*100%.

For operation above 25°C, The Ifm、 Ifp & PD must be derated ,the Current derating is -0.36mA/°C for Dc drive and-0.9mA/°C for pulsr drive,the power dissipation is -1.08 mW/°C, The product working current must not more than the 60 % of the Ifp according to the working temperature.

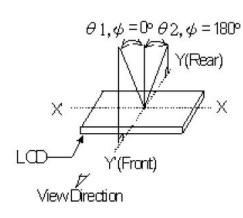
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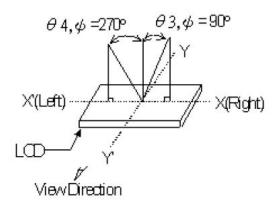
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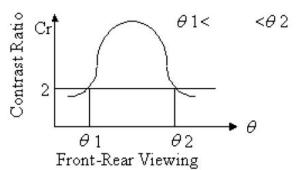
#### 12. OPTICAL CHARACTERISTICS

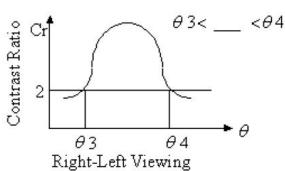
Itei	Item		Temp.	Condition	Min.	Тур.	Max.	Unit.	Note
	D.		-20°C		-	2100	2500		
Response Time fall time	Rise	tr	25℃		-	120	250		
		60℃	θ=0°	-	38	45	mS		
		-20°C	φ=0°	-	4400	5300	IIIS	-	
		tf	25℃		-	200	240		
	tillic		60°C	- 65 8	80				
				Ф=0°	-	30	-		
Viewing Angle	θ	25℃	Ф=90°	-	30	-	doa	-	
	0	Cr≥2	Ф=180°	-	30	-	deg.		
				Ф=270°	-	40	-		
Contrast	Ratio	Cr	25℃	θ=φ=0°	3	5	-	-	-

#### (1) DEFINITION OF VIEWING ANGLE





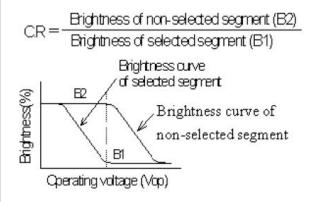




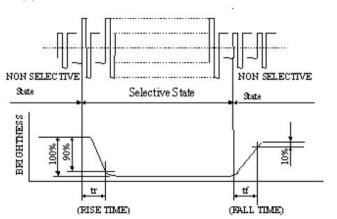


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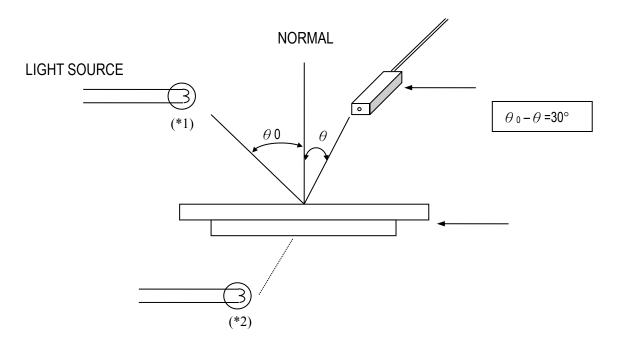
#### (2) DEFINITION OF CONTRAST



#### (3) DEFINITION OF RESPONSE



(4) MEASURING INSTRUMENTS FOR ELECTRO-OPTICAL CHARACTERISTICS



- \*1.Light source position for measuring the reflective type of LCD panel
- \*2.Light source position for measuring the transflective / transmissive types of LCD panel



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#### 13. ENVIRONMENTAL ABSOLUTE MAXIMUM RATINGS

ITEM	SYMBOL	CONDITIONS	CRITERION
OPERATING TEMPERATURE	TODD	-20℃ ~+70℃	NO DEFECT IN DISPLAYING AND
OPERATING TEMPERATURE	TOPR	-20 C ∼+70 C	OPERATIONAL FUNCTION
CTOD A CE TEMBER ATURE	ТОТС	-30℃ ~+80℃	NO DEFECT IN DISPLAYING AND
STORAGE TEMPERATURE	TSTG	-30 C ~+80 C	OPERATIONAL FUNCTION
HUMIDITY	_	See Note	WITHOUT CONDENSATION

Note: Test condition:

- 1) Temperature and humidity: if no specification, temperature set at  $25+/-2^{\circ}C$ , and humidity set at 60+/-5% RH.
  - 2)Operating state:all the tests to which the samples subject should be in operating condition.

#### 14. RELIABILITY TEST

ITEM	CONDITIONS	CRITERION		
Operating	HIGH TEMPERTURE 70℃ 120HRS	No defect in displaying and operational		
Temperature	LOW TEMPERTURE -20°C 120HRS	function		
Storage	HIGH TEMPERTURE +80℃ 120HRS	No defect in displaying and operational		
Tempereature	LOW TEMPERTURE -30°C 120HRS	function		
High Humidity &	40℃* 90%RH 120HRS	No defect in displaying and operational		
high Temp	40 C 90/6KH 120HKS	function		
	• Operating Time: 30 minutes exposure for			
Vibration	each direction (X,Y,Z)	No defect in displaying and operational		
Violation	• Sweep Frequency: 10~55Hz (1 min)	function		
	Amplitude: 1.5mm			
Thermal Shock	$-20^{\circ}\text{C}(30\text{mins}) \leftarrow \rightarrow +70^{\circ}\text{C}(30\text{mins}) 50 \text{ cycles}$	No defect in displaying and operational		
Thermal Shock	-20 C(Johnnis) X 7 + 70 C(Johnnis) 30 Cycles	function		

## SUCCESS字顺

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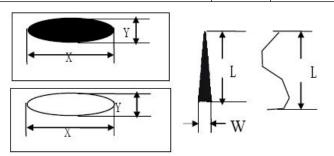
#### 15. THE STANDARD OF INSPECTION

#### 15-1 SAMPLING PLAN

Unless there is other agreement, sampling plan for incoming inspection should follow GB2828-2003.

- 15-1-1 Lot size: Quantity per shipment as one lot (different model as different lot .)
- 15-1-2 Sampling type: Normal inspection, single sampling.
  - 15-1-3 Sampling level: Level II.
  - 15-1-4 Acceptable Quality Level

Major defect: AQL=0.40 Minor defect: AQL=0.65 Total defect : AQL=0.65



#### 15-2 PANEL INSPECTION CONDITION

- 15-2-1 Environment:Room Temperature: 25 ± 5 °C. Humidity: 55 ± 5% RH.Illumination:800~1200Lux.
- 15-2-2 Inspection Distance:  $25\pm 5$  cm from the inspector to the module.
- 15-2-3 Inspection Angle: The vision of inspector should be perpendicular to the surface of the module.

#### 16-3 MODULE INSPECTION STANDARDS

#### 16-3-1 Defect definition

MAJOR:display or functional defects, serious deviation from the specifications, customers can not work properly; Severe skin defects, serious deviation from the specifications, the client does not work properly.

MINOR: slightly deviate from the specifications, does not affect the product function, but the appearance of an impact on product

Note: The following standard if no entities are specified, with mm meter.

#### 15-3-2 Product area and size code definition

A area: said display active area(characters display)

B area: says visual area (except A area)

C area: the unvisual areas.

T: it says the thickness of the single glass

L: said glass pin lengths

K: said product length

X :said glass long side direction or glass edge direction along the length of the gap

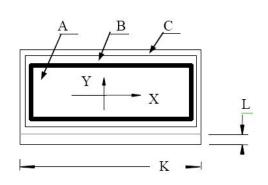
Y :said glass short side direction or gap with glass edge along the vertical length

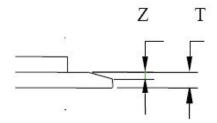
Z: the thickness of crack or gap

#### 15-3-2 Point, line definition

point:  $\Phi = (X + Y) / 2$ 

The length of the X says point length; Y says point width Line:L says the length of the line; W says line width







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			ppearance(power off)	101
	Item Dimension	Criterion Dimension out of th		AQL 1.0
	Glass crack	1. General crack  7. Y  2. corner  X  Z  3. contact pad crac	$\begin{array}{ c c c c }\hline X & Y & Z \\ \geqslant K/8 & \text{area} & \leqslant T \\ \hline \\ X & Y & Z \\ \geqslant K & \text{Not over A} \\ 8 & \text{area} & \text{No check} \\ \hline \\ 2k & & & & & & & & & & \\ 2k & & & & & & & \\ 2k & & & & & & & \\ 2k & & & & & & & \\ 2k & & & & & & & \\ 2k & & & & & & & \\ 2k & & & & & & & \\ 2k & & & & & & & \\ 2k & & & & \\ 2k & & $	2.50
1 4	Black dot \ White dot	X: long diameter Y: shot diameter D: average of diameter	D>0.5  0	2.50



TAND DO(				S69507A-	DD025MB		PAGE	<b>23</b> /2
4	Line defect	L: Length W: W Defect of polarizer		hidth $ \begin{array}{c} W \leqslant 0.02 \\ W \leqslant 0.05 \\ W \leqslant 0.05 \\ \end{array} $ $ \begin{array}{c} W \leqslant 0.05 \\ \end{array} $ es. Spot): A	Acceptabl A/B Area No check 2 2 As round to	No No	check	2.50
5	Polarizer Bubble	X	D D≤0.2 0.2≤D 0.5≤D D>1.0	≤0.5 ≤1.0	Acce table of defect A/B Area No check 3 2	C Area		2.50
6	External print of panel	Transfigure pin l	nole: same	e as segment tr	ransfinguer			2.50
7	Silicon glue	The area of painting	ng silicon g	lue must cove	r the ITO circuit.			2.50
8	Defect of PCB	The char wrong are unreceivable for gold finger of PC	or PCB.	J				2.50
9	SMT organ	deflexion of comp Trying to keep dot Damage break component.	of solderin	ng tin orbicula	r	vable for		2.50
10	Steel Frame	Break and distortion If there is one nick that following: Length≤5mm	which can	not lead to ca		nting, we	allow	2.50
15-3-4	4Inspection items	and specification for	display de	fect(power on	)			
1	Electrical Defect	Segn ort	nent missin nent s -display	Not allow  Not allow  Not allow				1.0



TAND. DOC		PRODU SPEC		MODULE NO.		S69507A-DI	0025MB	PAGI	<b>24</b> /27
2	Pin	hole	1. Pir	n hole	± ▼B A	width $W < 0.4$ $W \ge 0.4$ * D= (A+B) /2	Acceptable $D \leqslant 0.2 \& D$ $D \leqslant 0.25 \&$ $D \leqslant 0.1 \text{ acceptable}$	0≤1/2W D≤1/3W	2.50
3	Disp patte		W: De	sign dimensi	on C	Width W<0.4 W≥0.4  D: discrepant dim	Acceptable $C \cdot D \cdot G \le C \cdot C$	(1/2W) (0.2	1.0
4	Blac	ck/white	Y: sho	g diameter t diameter trage diamete	er D	D $ \begin{array}{c} D < 0.1 \\ 0.1 \le D < 0.2 \\ 0.2 \le D \le 0.25 \\ D > 0.25 \end{array} $ $ = (X+Y)/2 $	Acceptable Q'A/B Area No check 2 1	ΓΥ C Area -No check	2.50
5	Line	e defect	J. ave	L W	Length 不计 L≤3 L≤2.5	Width   W \le 0.02   W \le 0.03   0.03 \le W \le 0.05   W \le 0.05   w \le width   W \le width	Acceptable A/B Area No check 2 Sa round ty	C Area No check	2.50

#### 16. USING LCD MODULES

#### 16-1 LIQUID CRYSTAL DISPLAY MODULES

- LCD is composed of glass and polarizer. Pay attention to the following items when handling.
- (1) Please keep the temperature within specified range for use and storage. Polarization degradation, bubble generation or polarizer peel-off may occur with high temperature and high humidity.



- (2) Do not touch, push or rub the exposed polarizer with anything harder than an HB pencil lead (glass, tweezers, etc.).
  - (3) N-hexane is recommended for cleaning the adhesives used to attach front/rear polarizers and reflectors made of organic substances which will be damaged by chemicals such as acetone, toluene, ethanol and isopropylalcohol.
  - (4) If the display surface becomes contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If it is heavily contaminated, wipe gently with absorbent cotton or other soft material like chamois soaked in Isopropyl alcohol or Ethyl alcohol. Do not scrub hard to avoid damaging the display surface.
  - (5) Wipe off saliva or water drops immediately, contact with water over a long period of time may cause deformation or color fading.
  - (6) Avoid contacting oil and fats.
  - (7) Condensation on the surface and contact with terminals due to cold will damage, stain or dirty the polarizers.

    After products are tested at low temperature they must be warmed up in a container before coming is contacting with room temperature air.
  - (8) Do not put or attach anything on the display area to avoid leaving marks on.
  - (9) Do not touch the display with bare hands. This will stain the display area and degradate insulation between terminals (some cosmetics are determinated to the polarizers).
  - (10) Exercise care to minimize corrosion of the electrode. Corrosion of the electrodes is accelerated by water droplets, moisture condensation or a current flow in a high-humidity environment.
  - (11) As glass is fragile. It tends to become or chipped during handling especially on the edges. Please avoid dropping or jarring.

#### 16-2 PRECAUTION FOR HANDING LCD MODULES

Since LCM has been assembled and adjusted with a high degree of precision, avoid applying excessive shocks to the module or making any alterations or modifications to it.

- (1) Do not alter, modify or change the the shape of the tab on the metal frame.
- (2) Do not make extra holes on the printed circuit board, modify its shape or change the positions of components to be attached.
- (3) Do not damage or modify the pattern writing on the printed circuit board.
- (4) Absolutely do not modify the zebra rubber strip (conductive rubber) or heat seal connector.
- (5) Except for soldering the interface, do not make any alterations or modifications with a soldering iron.
- (6) Do not drop, bend or twist LCM. In particular, do not forcibly pull or bend the I/O cable or the backlight cable.
- (7) In order to avoid the cracking of the FPC, you should to pay attention to the area of FPC where the FPC was bent .the edge of coverlay; the area of surface of Ni-Au plating, the area of soldering land, the area of through hole.

#### 16-3 ELECTRO-STATIC DISCHARGE CONTROL

Since this module uses a CMOS LSI, the same careful attention should be paid to electrostatic discharge as for an ordinary CMOS IC.

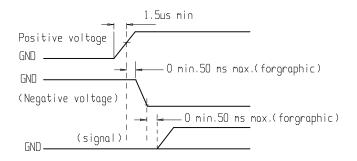


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- (1) Make certain that you are grounded when handing LCM. To minimize the performance degradation of the LCD modules resulting from destruction caused by static electricity etc., exercise care to avoid holding the following sections when handling the modules. Exposed area of the printed circuit board. Terminal electrode sections.
- (2) Before remove LCM from its packing case or incorporating it into a set, be sure the module and your body have the same electric potential.
- (3) When soldering the terminal of LCM, make certain the AC power source for the soldering iron does not leak.
- (4) When using an electric screwdriver to attach LCM, the screwdriver should be of ground potentiality to minimize as much as possible any transmission of electromagnetic waves produced sparks coming from the commutator of the motor.
- (5) As far as possible make the electric potential of your work clothes and that of the work bench the ground potential.
  - (6) To reduce the generation of static electricity be careful that the air in the work is not too dried. A relative humidity of 50%-60% is recommended.

#### 16-4 PRECAUTIONS FOR OPERATION

- (1) Viewing angle varies with the change of liquid crystal driving voltage (VO). Adjust VO to show the best contrast.
  - (2) Driving the LCD in the voltage above the limit shortens its life.
  - (3) If the LCD modules have been operating for a long time showing the same display patterns, the display patterns may remain on the screen as ghost images and a slight contrast irregularity may also appear. A normal operating status can be regained by suspending use for some time. It should be noted that this phenomenon does not adversely affect performance reliability.
  - (4) Response time is greatly delayed at temperature below the operating temperature range. However, this does not mean the LCD will be out of the order. It will recover when it returns to the specified temperature range.
  - (5) If the display area is pushed hard during operation, the display will become abnormal. However, it will return to normal if it is turned off and then back on.
  - (6) Condensation on terminals can cause an electrochemical reaction disrupting the terminal circuit. Therefore, it must be used under the relative condition of 40°C, 50% RH.
  - (7) When turning the power on, input each signal after the positive/negative voltage becomes stable.





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#### 16-5 STORAGE

When storing LCDs as spares for some years, the following precaution are necessary.

- (1) Store them in a sealed polyethylene bag. If properly sealed, there is no need for dessicant.
- (2) Store them in a dark place. Do not expose to sunlight or fluorescent light, keep the temperature between 0°C and 35°C.
  - 3) The polarizer surface should not come in contact with any other objects. (We advise you to store them in the container in which they were shipped.)
  - (4) Environmental conditions:
    - Do not leave them for more than 180hrs. at 70°C.
    - Should not be left for more than 48hrs, at -20°C.

#### **16-6 SAFETY**

- (1) It is recommended to crush damaged or unnecessary LCDs into pieces and wash them off with solvents such as acetone and ethanol, which should later be burned.
- (2) If any liquid leakes out of a damaged glass cell and comes in contact with the hands, wash off thoroughly with soap and water.

#### 14-7 LIMITED WARRANTY

Unless agreed between SUCCESS and customer, SUCCESS will replace or repair any of its LCD modules which are found to be functionally defective when inspected in accordance with SUCCESS LCD acceptance standards (copies available upon request) for a period of one year from date of shipments. Cosmetic/visual defects must be returned to SUCCESS within 90 days of shipment. Confirmation of such date shall be based on freight documents. The warranty liability of SUCCESS limited to repair and/or replacement on the terms set forth above. SUCCESS will not be responsible for any subsequent or consequential events.

#### 16-8 RETURN LCM UNDER WARRANTY

No warranty can be granted if the precautions stated above have been disregarded. The typical examples of violations are :

- Broken LCD glass.
- Circuit modified in any way, including addition of components.

Module repairs will be invoiced to the customer upon mutual agreement. Modules must be returned with sufficient description of the failures or defects. Any connectors or cable installed by the customer must be removed completely without damaging the PCB's eyelet, conductors and terminals.