

# **PRODUCT SPECIFICATION**

## MONO LCD MODULE MODEL:G1206B1SGW7G-C0 Ver:1.0 DRAWING VERSION: Ver: A

<>> Preliminary Specification

< > Finally Specification

CUSTOMER'S APPROVAL					
CUSTOMER :					
SIG	NATURE:	DATE:			

APPROVED	PM	PD	PREPARED
BY	REVIEWED	REVIEWED	BY
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## **Revision Status**

Version	Revise Date	Page	Content	Modified By
Ver 1.0	2023-05-18	-	First Issued	

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## 1. Features

The features of LCD are as follows

- \* Display mode : STN / Blue / Transmissive / Negative
- \* Controller IC : NT7538
- \* Interface Input Data : Serial interface
- \* Driving Method : 1/65 Duty, 1/9 Bias
- \* Viewing Direction : 12 o'clock
- \* Backlight : 3 LED/Side White
- \* Sample No. : G1206B1SGW7G-C0\_01/20230517

## 2. MECHANICAL SPECIFICATIONS

ltem	Specification	Unit
Module Size	56(W) x 37(H) x 6.9(D)	mm
View display area	46MIN(W) x 26MIN(H)	mm
Activity Display Area	42.21(W) x 22.37(H)	mm
Number of Dots	128 x 64	Dots
Dot Size	0.3(W) x 0.32(H)	mm
Dot Pitch	0.33(W) x 0.35(H)	mm

## 3. ELECTRICAL SPECIFICATIONS 3-1 ABSOLUTE MAXIMUM RATINGS (Ta = 25 °C)

Item	Symbol	Sta			
liem	Symbol	Min.	Тур.	Max.	Unit
Supply Voltage For Logic	Vdd – Vss	-0.3	-	4.0	V
Supply Voltage For LCD Drive	VLCD	-0.3	-	15	V
Input Voltage	Vin	-0.3	-	VDD+0.3	V
Operating Temp.	Тор	-20	-	+70	°C
Storage Temp.	Tst	-30	-	+80	°C

\*. NOTE: The response time will be extremely slow when the operating temperature is around -10 $^{\circ}$ C, and the back ground will become darker at high temperature operating.

### **3-2 ELECTRICAL CHARACTERISTICS**

Item		Symbol	Test Condition	Min.	Тур.	Max.	Unit
Logic supply Voltage LCD Drive		Vdd - Vss		2.8	3.0	3.2	V
		V <sub>LCD</sub>		8.8	9.0	9.2	V
Input Voltage	"H" Level	ViH	Ta = 25 °C	0.8Vdd	-	Vdd	V
input voltage	"L" Level	V <sub>IL</sub>		Vss	-	0.2Vdd	V
Frame Frequency		f <sub>FLM</sub>		-	78	-	Hz
Current Cons	Current Consumption			-	0.13	-	mA

## 3-3. BACKLIGHT

### 3-3-1. Absolute Maximum Ratings

ltem	Symbol	Condition	Min.	Тур.	Max.	Unit
Forward Current	IF	Ta = 25 °C	-	-	25	mA
Power Dissipation	PD	Ta = 25 C	-	-	75	mW
Reverse Current	lr	Vr = 5.0V	-	-	10	uA

\*. NOTE: This parameter is the maximum rating of a single LED.

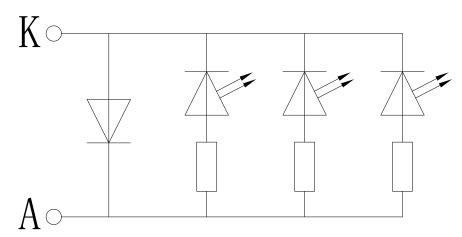
#### 3-3-2. Electrical-optical Characteristics

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Forward Voltage	Vf		3.7	4.0	4.3	V
Luminance	Lv	lf = 45 mA Ta = 25 ℃	500	-	-	cd/m <sup>2</sup>
Color coordinate	Х		0.25	0.28	0.32	
	Y		0.25	0.28	0.32	] -

\*. NOTE: The brightness is measured without LCD panel.

#### 3-3-3 Backlight circuit

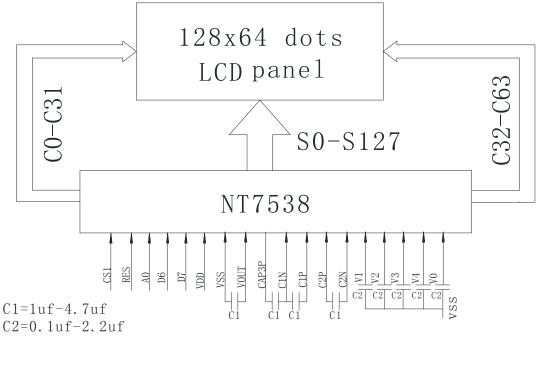
For operation above 25 °C, The Ifm & Pd must be derated , the current derating is -0.36mA/ °C for DC drive and -0.86mA/ °C for Pulse drive ,the Power dissipation is -1.5mW/ °C. The product working current must not more than the 60% of the Ifm or Ifp according to the working temperature.



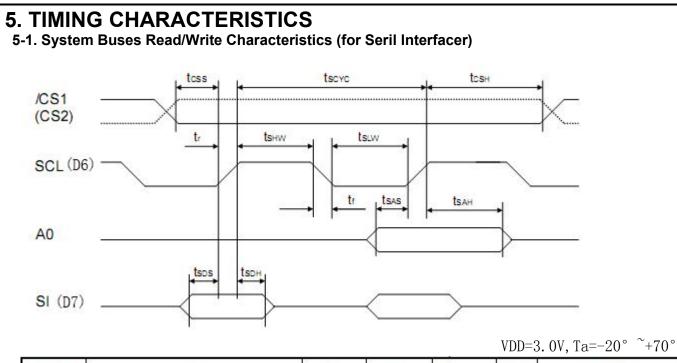
## 4. TERMINAL FUNCTIONS AND BLOCK DIAGRAM 4-1. INTERFACE PIN FUNCTION DESCRIPTION

PIN NO.	SYMBOL	FUNCIONS				
1	/CS1	Chip selection signal , When /CS1="L",then the chip select becomes active				
2	/RES	Reset signal, When /RES is set to "L", the settings are initialized.				
3	A0	Select register signal				
4	D6	the serial clock input terminal (SCL).				
5	D7	the serial data input terminal (SI)				
6	VDD	Supply voltage for logical circuit				
7	VSS	Ground (0V)				
8	VOUT	DC/DC voltage converter output				
9	C3P					
10	C1N					
11	C1P	DC/DC voltage converter.Connect CAP between them.				
12	C2P					
13	C2N					
14-17	V1-V4	A multi-level power supply for the liquid crystal drive.				
18	V0					
19	A	BLACKLIGHT +				
20	К	BLACKLIGHT -				

### 4-2. BLOCK DIAGRAM



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Symbol	Parameter	Min.	Typ.	Max.	Unit	Condition
tscyc	Serial clock cycle	120	-	-	ns	SCL
tsнw	Serial clock H pulse width	60	2	-	ns	SCL
tsLw	Serial clock L pulse width	60	-	-	ns	SCL
tsas	Address setup time	30	-	-	ns	A0
tsah	Address hold time	20	-	-	ns	A0
tsos	Data setup time	30	-	-	ns	SI
tsdh	Data hold time	20	-	-	ns	SI
tcss	Chip select setup time	20			ns	/CS1, CS2
tcsH	Chip select hold time	40	-	-	ns	/CS1, CS2

#### 5-2. Reset Timing

/RES		trw	•			
Interna Status			Dur	ing Re	set	
Symbol	Parameter	Min.	Тур.	Max.	Unit	Condition
tR	Reset Time	-	2 51 <b>-</b> 2	1.0	μs	
trw	Reset low pulse width	10	0.75	(77)	μs	/RES

## 6. COMMAND LIST

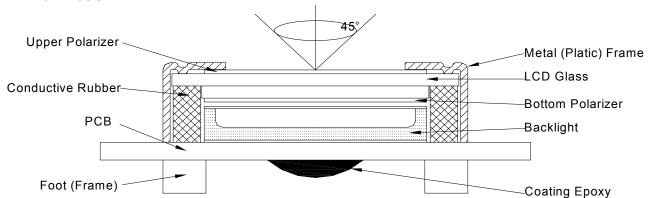
## 6-1. command table

Command		/RD		VR Code									Function
			/WR	D7	D6	D5	D4	D3	D2	D1	D0	Hex	Function
(1) Display OFF	0	1	0	1	0	1	0	1	1	1	0 1		Turn on LCD panel when high, and turn off when low
(2) Display Start Line Set	0	1	0	0	1		Disp	lay Sta	art Ad	dress		40h to 7Fh	Specifies RAM display line for COM0
(3) Page Address Set	0	1	0	1	0	1	1	F	Page A	\ddres	iS	B0h to B8h	Set the display data RAM page in Page Address register
(4) Column Address Set	0	1	0	0	0	0	1		Add	her Column		00h to	Set 4 higher bits and 4 lower bits of column address of display data
(.,	0	1	0	0	0	0	0	L		Colum Iress	in	18h	RAM in register
(5) Read Status	0	0	1		Sta	tus		0	0	0	0	XX	Reads the status information
(6) Write Display Data	1	1	0				Write	Data				XX	Write data in display data RAM
(7) Read Display Data	1	0	1				Read	Data				XX	Read data from display data RAM
(8) ADC Select	0	1	0	1	0	1	0	0	0	0	0 1		Set the display data RAM address SEG output correspondence
(9) Normal/Reverse Display	0	1	0	1	0	1	0	0	1	1	0 1	A7h	Normal indication when low, but full indication when high
(10)Entire Display ON/OFF	0	1	0	1	0	1	0	0	1	0	0 1	-	Select normal display (0) or entire display on
(11)LCD Bias Set	0	1	0	1	0	1	0	0	0	1	0 1	A2h A3h	Sets LCD driving voltage bias ratio
(12)Read-Modify-Write	0	1	0	1	1	1	0	0	0	0	0	E0h	Increments column address counter during each write
(13)End	0	1	0	1	1	1	0	1	1	1	0	EEh	Releases the Read-Modify-Write
(14)Reset	0	1	0	1	1	1	0	0	0	1	0	E2h	Resets internal functions
(15)Common Output Mode Select	0	1	0	1	1	0	0	0 1	*	*	*	C0h to CFh	Select COM output scan direction *: invalid data
(16)Power Control Set	0	1	0	0	0	1	0	1	Oper	ation S	Status	28h to 2Fh	Select the power circuit operation mode
(17)V0 Voltage Regulator Internal Resistor ratio Set	0	1	0	0	0	1	0	0	Res	Resistor Ratio 27h		20h to	Select internal resistor ratio Rb/Ra mode
(18)Electronic Volume mode Set	0	1	0	1	0	0	0	0	0	0	1	81h	
Electronic Volume Register Set	0	1	0	*	*		Electr	onic C	Control	Value	•	xx	Sets the V0 output voltage electronic volume register
(19)Set Static indicator ON/OFF	0	1	0	1	0	1	0	1	1	0	0 1	ACh ADh	Sets static indicator ON/OFF 0: OFF, 1: ON
Set Static Indicator Register	0	1	0	*	*	*	*	*	*	Mo	ode	xx	Sets the flash mode
(20)Power Save	0	1	0	<u>1</u>	-	- 20	2	-	-	2	-	- 27	Compound command of Display OFF and Entire Display ON
(21)NOP	0	1	0	1	1	1	0	0	0	1	1	E3h	Command for non-operation
(22)Oscillation Frequency Select	0	1	0	1	1	1	0	0	1	0	0 1	E4h E5h	Select the oscillation frequency
(23)Partial Display mode Set	0	1	0	1	0	0	0	0	0	1	0 1		Enter/Release the partial display mode
(24)Partial Display Duty Set	0	1	0	0	0	1	1	0	D	uty Rε	atio		Sets the LCD duty ratio for partial display mode
(25)Partial Display Bias Set	0	1	0	0	0	1	1	1	В	ias Ra	atio	38h 3Fh	display mode
(26)Partial Start Line Set	0	1	0	1	1	0	1	0	0	1	1	D3h	
Partial Start Line Set	0	1	0	1	1		Pa	artial S	Start L	ine		XX	Sets the LCD Number of partial display start line
(27)N-Line Inversion Set	0	1	0	1	0	0	0	0	1	0	1	85h	Enter N-Line inversion
Number of Line Set	0	1	0	*	*	*		Num	nber o	f Line	1	xx	Sets the number of line used for N-Line inversion
(28)N-Line Inversion Release	0	1	0	1	0	0	0	0	1	0	0	84h	Exit N-Line Inversion
(29)DC/DC Clock Set	0	1	0	1	1	1	0	0	1	1	0	E6h	Set DC/DC Clock Frequency
DC/DC Clock Division Set	0	1	0	1	1	0	0	(	Clock	Divisio	on .	xx	Set the Division of DC/DC Clock Frequency
(30)Test Command	0	1	0	1	1	1	1	*	*	*	*	F1h to FFh	IC test command. Do not use!
(31)Test Mode Reset	0	1	0	1	1	1	1	0	0	0	0		Command of test mode reset

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## 7. QUALITY SPECIFICATIONS

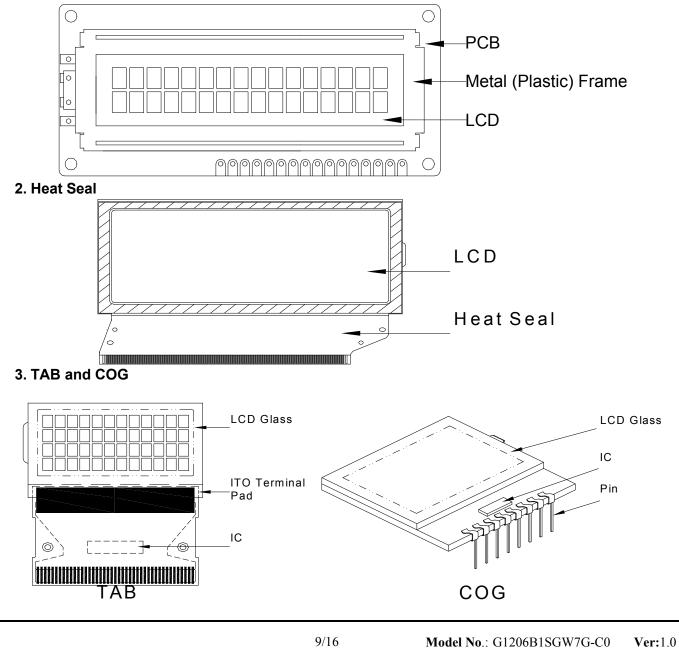
- 7-1. LCM Appearance and Electric inspection Condition
  - 1. Inspection will be done by placing LCM 30cm away from inspector's eyeballs under normal illumination.



#### 2. View Angle: with in 45° around perpendicular line.

### 7-2. Definition





## 7-3. Sampling Plan and Acceptance

1.Sampling Plan

GB2828.1.2012 ( || ) ordinary single inspection is used.

2.Acceptance	
Major defect:	AQL = 0.65
Minor defect:	AQL = 1.5

## 7-4. Criteria

### <u>1. COB</u>

Defect	Inspection Item	Inspection Standards	
Major	PCB copper flakes peeling off	Any copper flake in viewing Area should be greater than 1.0mm <sup>2</sup>	Reject
Major	Height of coating epoxy	Exceed the dimension of drawing	Reject
Major	Void or hole of coating epoxy	Expose bonding wire or IC	Reject
Major	PCB cutting defect	Exceed the dimension of drawing	Reject

### 2. SMT

Defect	Inspection Item	Inspection Standa	ards
Minor	Component marking not readable		Reject
Minor	Component height	Exceed the dimension Of drawing	Reject
Major	Component solder defect (missing , extra, wrong component or wrong orientation		Reject
Minor	Component position shift component soldering pad	X < 3/4Z Y > 1/3D	Reject Reject
Minor	Component tilt component D X Soldering pad	Y > 1/3D	Reject
Minor	Insufficient solder component PAD PCB	θ <b>≤ 20</b> °	Reject

3. Metal (Pla	astic) Frame					
Defect	Inspection Item	Inspection Standards				
Major	Crack / breakage	Any	where	Reject		
		W	L	Acceptable of Scratch		
		w<0.1mm	Any	Ignore		
		0.1≤w<0.2mm	L≤5.0mm	2		
Minor	Frame Scratch	0.2≤w<0.3mm	L≤3.0mm	1		
		w≥0.3mm	Any	0		
		Note : 1. Above criteria applicable to scratch lines with distance greater than 5mm. 2. Scratch on the back side of frame (not visible) can be ignored.				
				Acceptable of Dents / Pricks		
		Φ≤΄	2			
	Frame Dent , Prick	<b>1.0&lt;</b> Φ	1			
Minor	$\Phi = - L + W$	1.5n	0			
	2	Note : 1. Above criteria applicable to any two dents pricks with distance greater than 5mm 2. Dent / prick on the back side of frame (n visible) can be ignored				
Minor	Frame Deformation	Exceed the dimension of drawing				
Minor	Metal Frame Oxidation		Any rust			

## 4. Flexible Film Connector (FFC)

Defect	Inspection Item	Inspection Standa	rds
Minor	Tilted soldering	Within the angle +5°	Acceptable
Minor	Uneven solder joint /bump		Reject
		Expose the conductive line	Reject
Minor	Hole $\Phi = \frac{L + W}{2}$	Expose the conductive line $\Phi$ > 1.0mm Y > 1/3D	Reject
Minor	Minor	Y > 1/3D	Reject
WITTO		X > 1/2Z	Reject

#### 5. Screw

Defect	Inspection Item	Inspection Standards	
Major	Screw missing/loosen		Reject
Minor	Screw oxidation	Any rust	Reject
Minor	Screw deformation	Difficult to accept screw driver	Reject

6. Heatsea	6. Heatseal 、TCP 、FPC							
Defect	Inspection Item	Inspection Standards						
Major	Scratch expose conductive layer		Reject					
Minor	HS Hole $\Phi = \frac{L + W}{2}$	<b>Φ &gt; 0.5mm</b>	Reject					
Major	Adhesion strength	Less than the specification	Reject					
Minor	Position shift	Y > 1/3D	Reject					
WILLION		X > 1/2Z	Reject					
Major	Conductive line break		Reject					

### 7. LED Backing Protective Film and Others

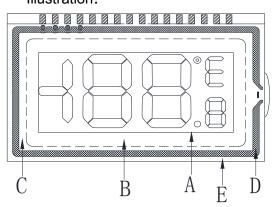
Defect	Inspection Item	Inspection Standards					
		Acceptable number of units					
		Φ <b>≤</b> 0.10mm					
		0.10<⊕≤0.15mm					
Minor	LED dirty, prick	0.15<Ф≤0.2mm	1				
		Ф>0.2mm	0				
		The distance between any two spots should be ≥ Any spot/dot/void outside of viewing area is acce					
Minor	Protective film tilt	Not fully cover LCD					
Major	COG coating	Not fully cover ITO circuit	Reject				

#### 8. Electric Inspection

Defect	Inspection Item	Inspection Standards	
Major	Short		Reject
Major	Open		Reject

#### 9.Inspection Specification of LCD

9-1:Definition: The area visible after LCD is assembled; Illustration:



A:display area (AA area )

B:View area (VA - AA area)

C:Out of sight area

(Inside edge of frame glue to VA)

 $D:Seating \ area \ (\ Box \ adhesive)$ 

E:Outside the frame glue area

Defect	Insp	oect Item			In	spect	tion St	andard	S		
						-	ZONE A				
			W	W≤	0.02	0.02<	W≪0.03	0.03 <w< th=""><th>≪0.05</th><th>W&gt;0.05</th></w<>	≪0.05	W>0.05	
		u Class Saratah	L	A	ny	L	≤2.0	L≤ł	5.0	Any	
Minor	Linear Defect	<ul><li>* Glass Scratch</li><li>* Polarizer Scratch</li></ul>	ACC. NO.	A	ny		2	1		Reject	
WIITIO		* Fiber and Linear material	Note	The d		betwe	en lines	must be	at leas	st 10mm	
				B: 1.5ti	imes of		able large				
			Zone	C: Acc	ept any	numbe	er and size		arance	defects.	
							ZONE A				
		* Foreign material	Φ	(	Φ≤0.1		<b>0.1&lt;</b> Φ	≪0.2	Ģ	Þ <b>&gt;0.2</b>	
	Spot and	between glass and	NO.		Any		3			0	
Minor Polarizer Pricked		polarizer or glass and glass * Polarizer hole or protuberance by	Note				ameter of vo defects				
		external force	Zone B: 1.5times of acceptable largest diameter size of Zone A								
			Zone	C: Acc	ept any	numbe	er and size	e of appe	arance	defects.	
			Φ	Φ≤	≦0.10		)<⊕≦ .20	0.20<Ф≤ 0.25		Ф <b>&gt;0.25</b>	
	Segment		ACC. NO.	-	EA / )mm²		2	1		0	
Minor	Defect				W is n	nore the	an 1/2 :	segment v	width	·	Reject
			Note	$\Phi = \frac{L + W}{2}$ Distance between two defect is 10mm							
			Φ	Þ Φ <b>≤0.10</b> 0		0.10<⊕≦0.20		20 0.20< <i>Φ</i> ≤0.25		Φ>0.2	
	Protuberant		w	Glu	Glue W≤1/2 Seg W≤0.2		•	•		Ignore	
Minor	Segment			3EA 100m		2		1		0	
		1 A A	1. Seg	gment						I	
			E	3	B≪0.	).4mm 0.4 <b≶< td=""><td colspan="2">≦1.0mm B</td><td>&gt;1.0mm</td></b≶<>		≦1.0mm B		>1.0mm	
	Assembly		B·	-A	B-A<	1/2B			B-	A<0.25	
Minor	Mis-alignment			Judge Acceptable			e Acceptable Acceptable				
			2. Dot	Matrix	(						
			Deformation>2° Reje					Reject			
Minor	Stain on LCD Panel Surface		simila	r one.		vise, ju	e wiped l dged acc				

## 8. RELIABILITY

No	Item	Condition	Quantity	Criteria
1	High Temperature Operating	<b>70</b> ℃, 96Hrs	2	GB/T2423.2 -2008
2	Low Temperature Operating	-20℃, 96Hrs	2	GB/T2423.1 -2008
3	High Humidity	60℃, 90%RH, 96Hrs	2	GB/T2423.3 -2016
4	High Temperature Storage	80℃, 96Hrs	2	GB/T2423.2 -2008
5	Low Temperature Storage	-30℃, 96Hrs	2	GB/T2423.1 -2008
6	Thermal Cycling Test	-20℃, 60min~70℃, 60min, 20 cycles.	2	GB/T2423.22 -2012
7	Packing vibration	Frequency range:10Hz~50Hz Acceleration of gravity:5G X,Y,Z 30 min for each direction.	2	GB/T5170.14 -2009
8	Electrical Static Discharge	Air:±8KV 150pF/330 Ω 5 times Contact:±4KV 150pF/330 Ω 5 times	2	GB/T17626.2 -2018
9	Drop Test (Packaged)	Height:80 cm,1 corner, 3 edges, 6 surfaces.	2	GB/T2423.8 -1995

Note: 1) Above conditions are suitable for our company standard products.

2) For restrict products, the test conditions listed as above must be revised.

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## 9. HANDLING PRECAUTION

(1) Mounting Method

The panel of the LCD Module consists of two thin glass plates with polarizers which easily get damaged since the Module is fixed by utilizing fitting holes in the printed circuit board. Extreme care should be taken when handling the LCD Modules.

(2) Caution of LCD handling & cleaning

When cleaning the display surface, use soft cloth with solvent (recommended below) and wipe lightly. - Isopropyl alcohol

- Ethyl alcohol

- Trichloro trifloro thane

Do not wipe the display surface with dry or hard materials that will damage the polarizer surface. Do not use the following solvent:

- Water

- Ketone

- Aromatics

(3) Caution against static charge

The LCD Module use C-MOS LSI drivers, so we recommend that you connect any unused input terminal to VDD or VSS, do not input any signals before power is turned on. And ground your body, Work/assembly table. And assembly equipment to protect against static electricity.

(4) Packaging

Modules use LCD elements, and must be treated as such. Avoid intense shock and falls from a height.
 To prevent modules from degradation. Do not operate or store them exposed directly to sunshine or

high temperature/humidity.

(5) Caution for operation

- It is indispensable to drive LCD's within the specified voltage limit since the higher voltage than the limit shorten LCD life. An electrochemical reaction due to direct current causes LCD deterioration, Avoid the use of direct current drive.

- Response time will be extremely delayed at lower temperature than the operating temperature range and on the other hand at higher temperature LCD's show dark color in them. However those phenomena do not mean malfunction or out of order with LCD's. Which will come back in the specified operating temperature range.

- If the display area is pushed hard during operation, some font will be abnormally displayed but it resumes normal condition after turning off once.

- A slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit.

Usage under the relative condition of 40 C, 50%RH or less is reequired.

#### (6) Storage

In the case of storing for a long period of time (for instance.) For years) for the purpose or replacement use, The following ways are recommended.

- Storage in a polyethylene bag with sealed so as not to enter fresh air outside in it, And with no desiccant.

- Placing in a dark place where neither exposure to direct sunlight nor light is. Keeping temperature in the specified storage temperature range.

- Storing with no touch on polarizer surface by the anything else. (It is recommended to store them as they have been contained in the inner container at the time of delivery)

(7) Safety

- It is recommendable to crash damaged or unnecessary LCD into pieces and wash off liquid crystal by using solvents such as acetone and ethanol.

Which should be burned up later.

When any liquid crystal leaked out of a damaged glass cell comes in contact with your hands, please wash it off well with soap and wate

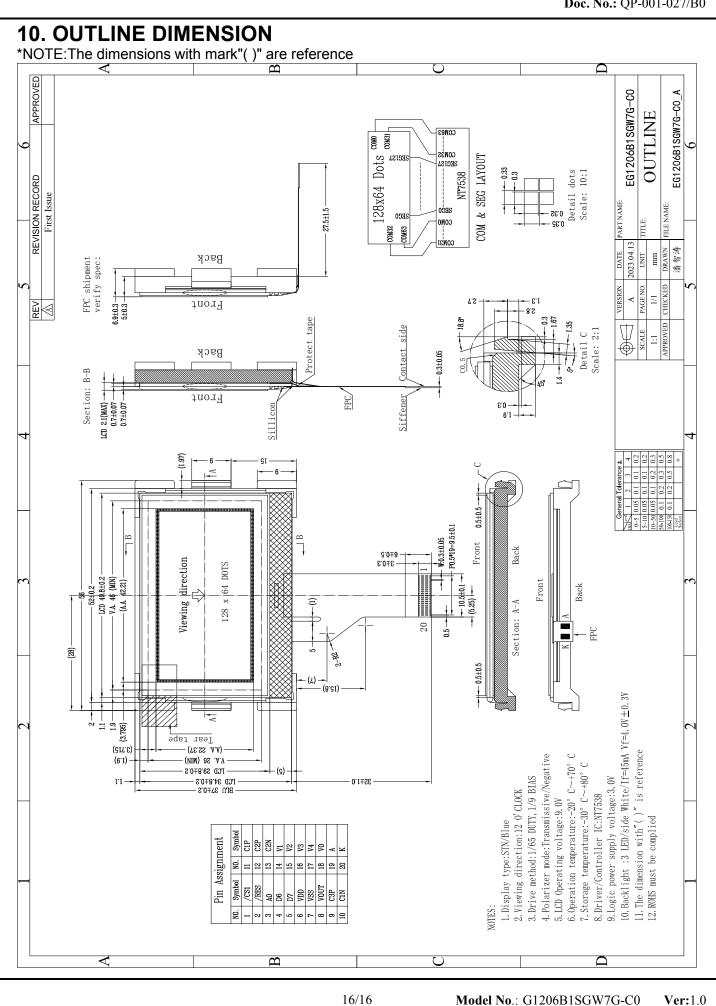
#### (8) Limited Warranty

- Our warranty liability is limited to repair and/or replacement. We will not be responsible for any consequential loss.

- If possible, we suggest customer to use up all modules in six months. If the module storage time over twelve months, we suggest that recheck it before the module be used

- After the product shipped, any product quality issues must be feedback within three months, otherwise, we will not be responsible for the subsequent or consequential events.

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