

DATA IMAGE CORPORATION

CTP Module Specification

Preliminary ITEM NO.: <u>SCF0403526GGU38</u>

Table of Contents

1.	COVER & CONTENTS ·····	1
2.	RECORD OF REVISION ·····	2
3.	GENERAL SPECIFICATIONS ······	3
4.	ELECTRICAL CHARACTERISTICS ······	3
5.	BLOCK DIAGRAM ·····	4
6.	PIN CONNECTIONS ······	5
7.	AC CHARACTERISTICS ·····	6
8.	CTP INTERFACE AND DATA FORMAT······	9
9.	COMMAND	13
10.	OPTICAL CHARACTERISTIC ·····	31
11.	QUALITY ASSURANCE ·····	33
12.	LCM PRODUCT LABEL DEFINE	37
13.	PRECAUTIONS IN USE LCM ·····	39
14.	OUTLINE DRAWING ·····	40
15.	PACKAGE INFORMATION ·····	41

Customer Companies	R&D Dept.	Q.C. Dept.	Eng. Dept.	Prod. Dept.
	ALEX	PRETTY	DAVID	KEN
Approved by	Version:	Issued Date:	Sheet Code:	Total Pages:
	3	09/JUL/15'		41



2. RECORD OF REVISION

Rev	Date	Item	Page	Comment	Source
1	07/APR/14'			Initial preliminary	ESR0304001
2	06/DEC/14'	4.2 8.1		Add LED life time. Add Report rate, Response time&(X,Y)position.	11S-EC0012
3	09/JUL/15'	8.1 11.2 14	9 34 40	Update General Specifications, Add fw Update Inspection Judgment standard Modify OUTLINE DRAWING from Rev 1 to 2.	11S-F70004
		14			



Parameter	Specifications	Unit
Display resolution	480X R.G.B x 272	dot
LCD Active area	95.04(W) x 53.856(H)	mm
Sensor A A	98.7(W) x 57.5(H)	
Screen size	4.3(Diagonal)	inch
Pixel pitch	0.198 (W) x 0.198(H)	mm
Color configuration	R.G.B. Stripe	
Overall dimension	110.5 (W) x 72.7(H) x 5.1(D)	mm
Weight	TBD	g
Surface treatment	Glare	
LCM model number	FX04032BDSSWBG01	
View Angle direction(Gray inversion)	6 o'clock	
Our components and processes are con	pliant to RoHS standard	•

4. ELECTRICAL CHARACTERISTICS

4.1 Operating Conditions

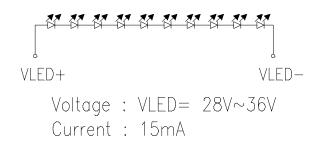
						GND=0V,Ta=25°C
Parameter	Symbol	MIN.	Тур.	MAX.	Unit	Remark
Power Supply voltage	V _{DD}	3.0	3.3	3.6	V	Note1
Power Supply Current	I _{DD}		17	20	mA	V _{DD} =3.3V
Ripple Voltage	V _{RPVDD}			100	mVp-p	
"H" level logical input voltage	V _{IH}	0.8VDD		VDD	V	
"L" level logical input voltage	V _{IL}	0		0.2VDD	V	
Operating temperature	Тора	-20		70	°C	Ambient temperature
Storage temperature	Tstg	-30		80	°C	Ambient temperature

Note1:VDD Absolute Maximum Ratings -0.3V~+6V

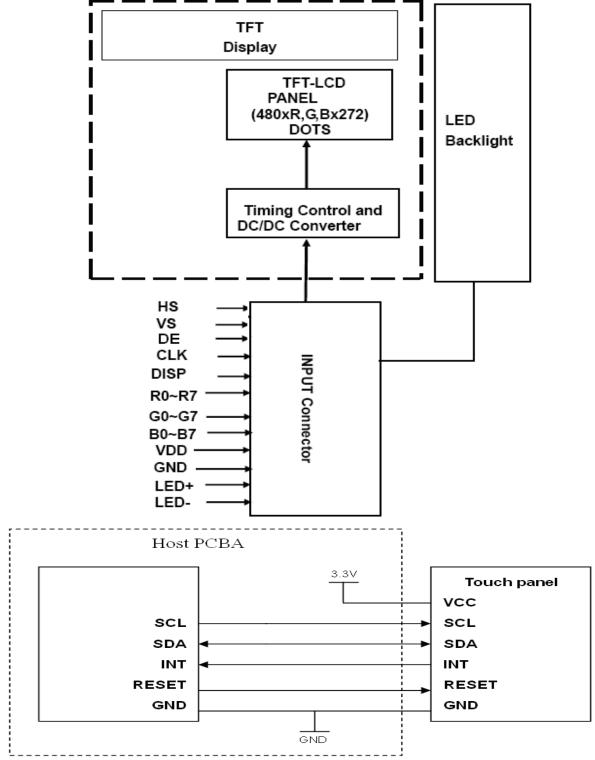
4.2 Backlight driving for power conditions

						Ta=25°C
Parameter	Symbol	Min.	Тур.	Max.	Unit	Remark
LED current	I _{LED}		15		mA	
VLED voltage	V _{LED}	28		36	V	ILED=15 mA
LED life time		15,000	30,000		Hours	Note 1

Note 1 under room temperature (25 °C, Humidity 30-60% RH)and ILED=15mA.









6.1 Input Pins Connection

Pin No	Symbol	Function	Remark
1	LED-	LED Power Source input terminal (Cathode side)	
2	LED+	LED Power Source input terminal (Anode side)	
3	NC	No Connect	
4	VDD	Power Supply : +3.3V	
5	R0		
6	R1		
7	R2		
8	R3	Digital data input D0 is LSD and D7 is MSD	
9	R4	Digital data input. R0 is LSB and R7 is MSB	
10	R5		
11	R6		
12	R7		
13	G0		
14	G1]	
15	G2		
16	G3	Digital data input C0 in LSD and C7 in MSD	
17	G4	Digital data input. G0 is LSB and G7 is MSB	
18	G5		
19	G6		
20	G7		
21	B0		
22	B1	1	
23	B2	1	
24	B3		
25	B4	Digital data input. B0 is LSB and B7 is MSB	
26	B5		
27	B6		
28	B7	1	
29	GND	Ground	
30	CLK	clock signal to sample each data	
31	DISP	Display ON/OFF Control ON=H(VDD), OFF=L(GND)	
32	HS	Horizontal synchronous signal	
33	VS	Vertical synchronous signal	
34	DE	Data enable	
35	NC	No Connect	
36	GND	Ground	
37	NC	No Connect	
38	NC	No Connect	
39	NC	No Connect	
40	NC	No Connect	



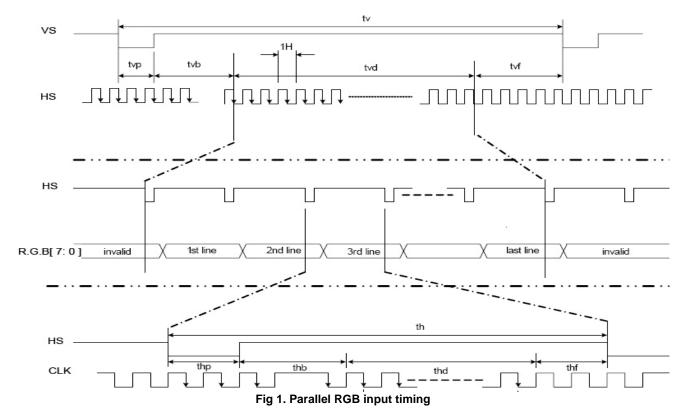
7. AC CHARACTERISTICS

7.1 Input Timing Requirement (480RGBx272, Ta =25°C, VDD=3.3V GND= 0V)

Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Clock cycle	fclk(1)	-	9	15	MHz
HS cycle	1/th	-	17.14	-	KHz
VS cycle	1/tv	-	59.94	-	Hz
Horizontal Signal					
Horizontal cycle	th	525	525	605	CLK
Horizontal display period	thd	480	480	480	CLK
Horizontal front porch	thf	2	2	82	CLK
Horizontal pulse width	thp(2)	2	41	41	CLK
Horizontal back porch	thb(2)	2	2	41	CLK
Vertical Signal				•	
Vertical cycle	tv	285	286	511	H(1)
Vertical display period	tvd	272	272	272	H(1)
Vertical front porch	tvf	1	2	227	H(1)
Vertical pulse width	tvp(2)	1	10	11	H(1)
Vertical back porch	tvb(2)	1	2	11	H (1)

Note: (1) Unit: CLK=1/ fCLK , H=th,

(2) It is necessary to keep tvp+tvb=12 and thp+thb=43 in sync mode.



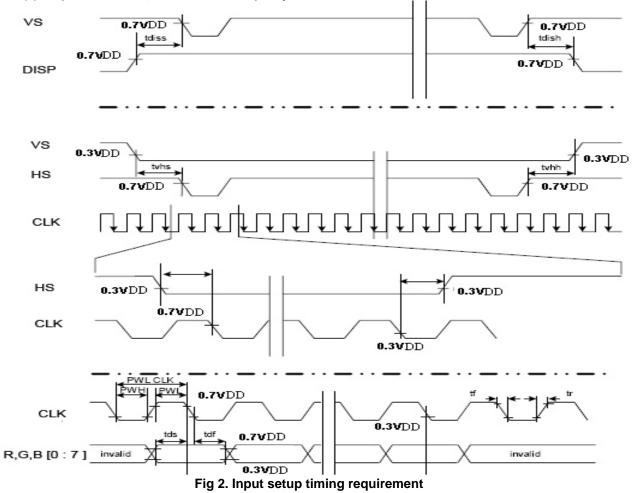
SCF0403526GGU38 REV: 3



		(Ta =25°C, VDD=3.3V ,GND= 0V, tr (1)=tf (1)=2ns)				
Parameter	Symbol	MIN.	TYP.	MAX.	Unit	
DISP setup time	tdiss	10	-	-	ns	
DISP hold time	t dish	10	-	-	ns	
Clock period	PWCLK(2)	66.7	-	-	ns	
Clock pulse high period	PWH(2)	26.7	-	-	ns	
Clock pulse low period	PWL(2)	26.7	-	-	ns	
HS setup time	ths	10	-	-	ns	
HS hold time	t hh	10	-	-	ns	
Data setup time	tds	10	-	-	ns	
Data hold time	tdh	10	-	-	ns	
VS setup time	tvhs	10	-	-	ns	
VS hold time	t vhh	10	-	-	ns	

Note: (1) tr, tf is defined 10% to 90% of signal amplitude.

(2) For parallel interface, maximum clock frequency is 15MHz.





7.3 TCON Power ON/OFF Control

The TCON IC has a power ON/OFF sequence control function. When DISP pin is pulled "H", blank data is outputted for 10-frames first, from the falling edge of the following VSYNC signal. Similarly, when DISP is pulled "L", 10-frames of blank data will be outputted from the falling edge of the following VSYNC, too.

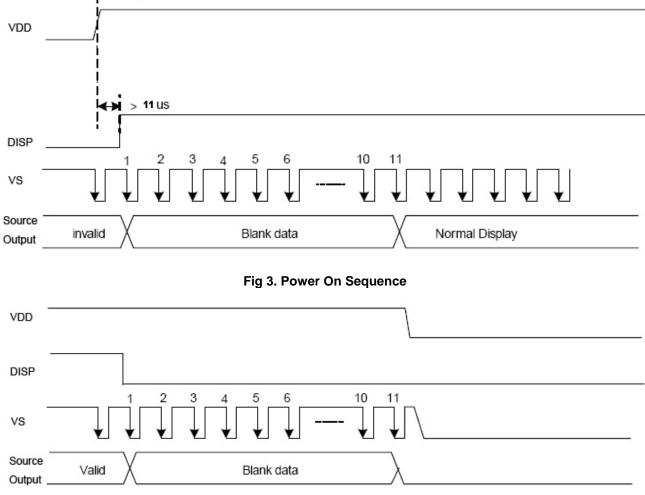
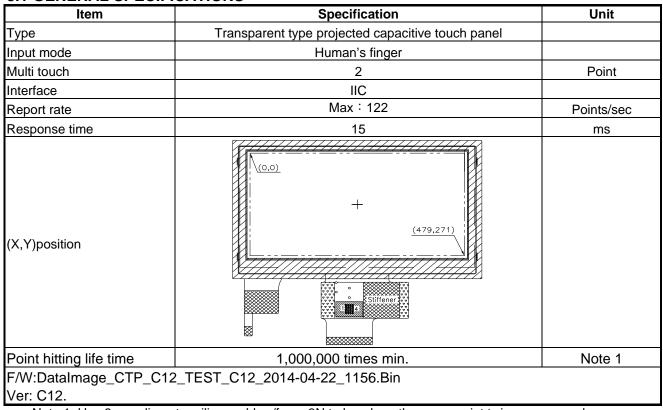


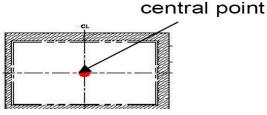
Fig 4. Power Off Sequence



8. CTP INTERFACE AND DATA FORMAT 8.1 GENERAL SPECIFICATIONS



Note 1: Use 8 mm diameter silicon rubber/force 3N to knock on the same point twice per second (no-operating), after test function check pass.



8.2 Electrical Characteristic

8.2.1 Absolute Maximum Rating

Parameter	Symbol	Spec.			Unit
Supply voltage	VCC	-0.3	-	7	V
Switch control signals output current	Output current	-	50	-	mA
Enable control voltage range	Logic Input	-0.3	-	VCC+0.3	V
Output Control Driver	Output voltage	-0.3	-	VCC	V

8.2.2 DC Characteristic

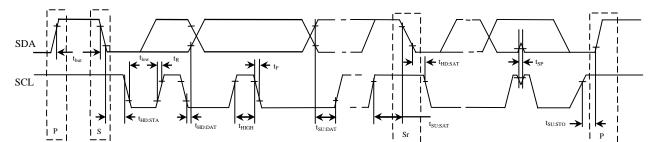
Symbol	Description	Min	Тур.	Max	Unit	Notes
VCC	Supply voltage	2.7	3.3	3.5	V	
Ivcc	Supply current		12		mA	
IST	sleep mode		30		uA	



8.3 CTP Pin Connections

No.	Name	I/O	Description
1	VCC	-	Power; VCC=3.3V
2	GND	-	Ground
3	/INT	0	Interrupt, Active low
4	SCL	I	Serial clock
5	SDA	I/O	Serial data
6	/RESET	I	Reset, Active LOW.

8.4 Interface and Data Format (Slave address is 0x94H) AC characteristics of the SDA and SCL bus lines for I²C-bus devices



		'				
Parameter	Symbol		rd-Mode BUS	Fast-N I ² C-B		Unit
		Min.	Max.	Min.	Max.	
SCL clock frequency	f _{scl}	0	100	0	400	KHz
Bus free time between STOP and START condition	t _{BUF}	4.7	-	1.3	-	μs
Hold time (repeated) START condition. After this period, the first clock pulse is generated	t _{HD:STA}	4.0	-	0.6	-	μs
LOW period of the SCL clock	t_{LOW}	4.7	-	1.3	-	μs
HIGH period of the SCL clock	t _{HIGH}	4.0	-	0.6	-	μs
Set-up time for a repeated START condition	t _{su:sta}	4.7	-	0.6	-	μs
Data hold time	$t_{HD:DAT}$	0	-	0	0.9	μs
Data set-up time	t _{su:DAT}	250	-	100	-	μs
Rise time of both SDA and SCL signals	t_{R}	-	1000	20+0.1C _b	300	μs
Fall time of both SDA and SCL signals	t⊧	-	300	20+0.1C _b	300	μs
Set-up time for STOP condition	t _{su:sto}	4.0	-	0.6	-	μs
Capacitive load for each bus line.	C,	-	400	-	400	pF

Note:

(1) All values are referred to VIH (0.7xVCC) and VIL (0.3xVCC) level.

(2) A device must internally provide a hold time of at least 300ns for the SDA signal (referred to the VIH of the SCL signal) in

(2) A device index including provide a nota time of at least events in the CDA digital (referred to the Viriel the CDA digital) for order to bridge the undefined region of the falling edge of SCL.
(3) The maximum t_{HD:DAT} has only to be met if the device does not stretch the LOW period (t_{LOW}) of the SCL signal.
(4) A fast-mode l²C-bus device can be used in a standard-mode l²C-bus system, but the requirement t_{SU:DAT} ≥ 250ns must then be met. This will automatically be the case if the device does not stretch the LOW period of the SCL signal. If such a device does stretch the LOW period of the SCL signal, it must output the next data bit to the SDA line t_{R max} $t_{SU:DAT} =$ 1000+250=1250ns (according to the standard-mode I²C-bus specification) before the SCL line is released.

(5) C_b = total capacitance of one bus line in pF.



8.5. Interface and Data Format

8.5.1 Transfer protocol (I²Cinterface)

HX8526-A support I²C interface that need 2 hardware pin – serial data (SDA) and serial clock (SCL), carry information between the devices connected to the bus. The I²C bus supports serial, 8-bit oriented, bi-directional data transferred at a rate up to 100Kbit/s in the standard-mode, or up to 400Kbit/s in the fast-mode.

The data on the SDA line must be stable during the HIGH period of the clock. The HIGH or LOW state of the data line can only change when the clock signal on the SCL line is LOW.

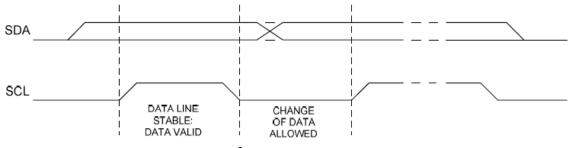
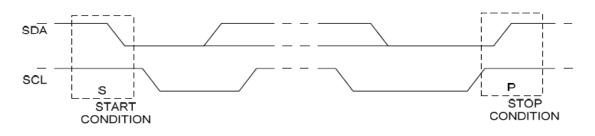


Figure 8.5.1: I²C Signal timing

Within the procedure of the I²C -bus, unique situations arise which are defined as START and STOP conditions. A HIGH to LOW transition on the SDA line while SCL is HIGH is one such unique case. This situation indicates a START condition. A LOW to HIGH transition on the SDA line while SCL is HIGH defines a STOP condition. START and STOP conditions are always generated by the master. The I²C bus is considered to be busy after the START condition. The I²C bus is considered to be free again a certain time after the STOP condition.

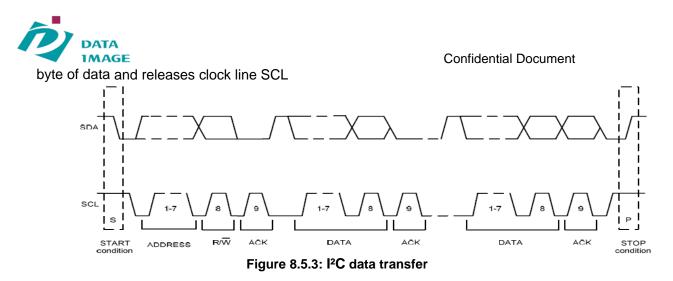




The slave address of I²C is defined at the follow description

- A. The HX8526-A QFN48 Type 1 and UFBGA Type 1 only support I²C interface and the slave address is 90h.
- B. The HX8526-A QFN 48 type 2, UFBGA Type 2, QFN64 and QFN40 support I²C interface and the slave address is selected by A0. If A0="L", the slaver address is 90h. If A0="H", the slaver address is 92h.

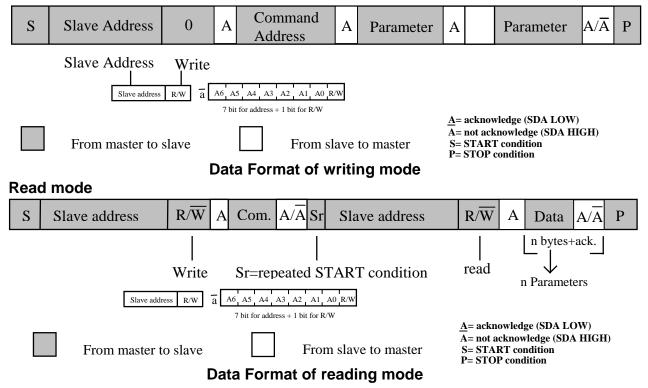
In I²C slave mode, HX8526-A waits for Master reading the data and acknowledges. Every byte put on the SDA line must be 8-bits long. The number of bytes that can be transmitted per transfer is unrestricted. Each byte has to be followed by an acknowledge bit. Data is transferred with the most significant bit (MSB) first. If HX8526-A can't receive or transmit another complete byte of data until it has performed some other function, for example servicing an internal interrupt, it can hold the clock line SCL LOW to force the master into a wait state. Data transfer then continues when the HX8526-A is ready for another



8.5.2 Format of data frame (I²C interface)

When master sends the command which be received by HX8526-A, the HX8526-A will responses the code and data .The format of communication is shown as Figure 11.4. The Command table that is written by master is defined on Table11.1 Command Table. HX8526-A will response the response code first and data later

Write mode





9. COMMAND

9.1 Command list

Hex	Operation Code	D7	D6	D5	D4	D3	D2	D1	D0	Function
0	No operation	0	0	0	0	0	0	0	0	-
80	Sleep IN	1	0	0	0	0	0	0	0	-
81	Sleep Out	1	0	0	0	0	0	0	1	-
82	Sense Off	1	0	0	0	0	0	1	0	-
83	Sense On	1	0	0	0	0	0	1	1	-
	Read Event	1	0	0	0	0	1	0	1	-
	1st parameter	B31	B30	B29	B28	B27	B26	B25	B24	-
85	2nd parameter	B23	B22	B21	B20	B19	B18	B17	B16	-
	3rd parameter	B15	B14	B13	B12	B11	B10	B9	B8	-
	4th parameter	B7	B6	B5	B4	B3	B2	B1	B0	-
	Read All Events	1	0	0	0	0	1	1	0	-
	1st parameter	B31	B30	B29	B28	B27	B26	B25	B24	-
	2nd parameter	B23	B22	B21	B20	B19	B18	B17	B16	-
	3rd parameter	B15	B14	B13	B12	B11	B10	B9	B8	-
86	4th parameter	B7	B6	B5	B4	B3	B2	B1	B0	-
	5th parameter	E3	E2	E1	E0	FI	P2	P1	P0	-
	6th parameter	B23	B22	B21	B20	B19	B18	B17	B16	-
			:	:	:	:	:	:	:	-
	(n+1)th parameter	B7	B6	B5	B4	B3	B2	B1	B0	-
	Read Latest Event	1	0	0	0	0	1	1	1	-
	1st parameter	B31	B30	B29	B28	B27	B26	B25	B24	-
87	2nd parameter	B23	B22	B21	B20	B19	B18	B17	B16	-
	3rd parameter	B15	B14	B13	B12	B11	B10	B9	B8	-
	4th parameter	B7	B6	B5	B4	B3	B2	B1	B0	-
88	Clear Stack	1	0	0	0	1	0	0	0	-
9E	TS Software Reset	1	0	0	1	1	1	1	0	-

9.2 User define command list table

Hex	Operation Code	D7	D6	D5	D4	D3	D2	D1	D0	Function
	Device ID	0	0	1	1	0	0	0	1	Response Device ID Code
31h	1st parameter				8	35				-
	2nd parameter				2	26				-
	3nd parameter				C)1				-
32h	Version ID	0	0	1	1	0	0	0	1	Read Firmware version



9.3CTP COMMAND DESCRIPTION

NOP

00 H	NOP (No Operation)											
	DNC	D7	D6	D5	D4	D3	D2	D1	D0	HEX		
Command	0	0	0	0	0	0	0	0	0	00		
Parameter	No para	meter										
Description	This con	nmand is	an empty	comman	d and it do	oes not ha	ave any ef	fect on th	e touch so	creen.		
Restriction												
Decistor		Status Availability										
Register Availability		Т	S Sleep C	Dut				Yes				
		-	rs Sleep	In				Yes				
			Status				De	fault Value	Э			
Default		Powe	er Up Seq	uence				N/A				
Dolaun	TS S/W Reset N/A											
	H/W Reset N/A											
Flow Chart												



0011	TSSLP	N (Touch	Screen S	leep In)						
80H	DNC	D7	D6	D5	D4	D3	D2	D1	D0	HEX
Command	0	1	0	0	0	0	0	0	0	80
parameter					No par					
Description		mmand ca							umption n	node.
Restriction	This cor TS Slee It will be the sup It will be	terface are mmand ha p In Mode necessar ply voltage necessar n Mode) b	es no effe e can only ry to wait es and clo ry to wait	ct when th be left by 5msec be ock circuits 5msec aff	ne touch s / the TS S fore send s to stabili er sendin	creen is a creep Out ing next c ze. g TS Slee	Iready in Comman command.	TS Sleep d (81h). . This is tc	allow tim	ne for
Register Availability			Status				A	vailability		
Availability		Т	S Sleep (Dut				Yes		
		-	TS Sleep	In				Yes		
			Status				De	fault Valu	е	
Default		Powe	er Up Sec	uence			TS S	leep In M	ode	
	Fault Power Up Sequence TS Sleep In Mode TS S/W Reset TS Sleep In Mode		ode							
			H/W Res	et			TS S	leep In Mo	ode	
Flow Chart				TSSLPIN Stop DC/DC converter Stop Internal Oscillator SSleep In Me	Jode		Legend Commar Paramet Touch Scree Action Mode Sequentia transfer			



TSSLPOUT (Touch Screen Sleep Out) 81H DNC D3 D2 D1 D0 HEX D7 D6 D5 D4 Command 81 0 1 0 0 0 0 0 0 1 parameter No parameter Description This command turns off TS Sleep In mode. This command has no effect when touch screen is already in TS Sleep Out mode. TS Sleep Out Mode can only be left by the TS Sleep In Command (80h). It will be necessary to wait 5msec before sending next command. This is to allow time for the supply voltages and clock circuits to stabilize. The touch screen loads all touch screen supplier's factory default values to the registers Restriction during this 5msec and there cannot be any abnormal effect on the touch screen functionality if factory default and register values are same when this load is done and when the touch screen is already TS Sleep Out - mode. It will be necessary to wait 5msec after sending TS Sleep In command (when in TS Sleep Out mode) before TS Sleep Out command can be sent. Status Availability Register **TS Sleep Out** Yes Availability TS Sleep In Yes Status **Default Value Power Up Sequence** TS Sleep In Mode Default TS S/W Reset TS Sleep In Mode H/W Reset TS Sleep In Mode Legend TSSLPOUT Command Start Parameter Internal Oscillator Touch Screen Flow Chart Action Start up DC/DC converter Mode Sequential transfer TS Sleep Out Mode



TS sense off (
82H			Screen S	1 (
	DNC	0 1 0 0 0 0 1 0 82												
Command	0	•	0	0	0	0	0	1	0	82				
parameter	No para				. ,									
Description			n is not se	ensing tou	uches (=	No new e	events), b	ut the tou	ich scree	n is still				
Restriction	scanning	y.												
			Status				A	vailability						
Register Availability		т	S Sleep (Dut				Yes						
Availability			TS Sleep					Yes						
			Status				De	fault Valu	е					
Default		Powe	er Up Seq	uence			TS	Sense Of	ff					
		T	S S/W Re	set			TS	Sense O	ff					
			H/W Rese	et			TS	Sense O	ff					
Flow Chart		T	TSSOF					command Parameter Touch Screen Action Mode						



TS sense on (83h)

10 361136 011			-									
83H	TSSON (Touch Screen Sense On)											
	DNC	D7	D6	D5	D4	D3	D2	D1	D0	HEX		
Command	0	1	0	0	0	0	0	1	1	83		
parameter	No para	meter					-					
Description	The tou	ch screen	is sensir	ig touches	s (= No ne	ew events).					
Restriction												
Decister			Status				A	vailability				
Register Availability		Т	S Sleep (Dut				Yes				
		-	TS Sleep	In				Yes				
			Status				De	fault Valu	е			
Default		Powe	er Up Sec	luence			TS	Sense O	ff			
_ 514414		T	S S/W Re	set			TS	Sense O	ff			
			H/W Res	et			TS	Sense O	ff			
Flow Chart			TSSON					Command Paramete Touch Screen Action Mode equential				

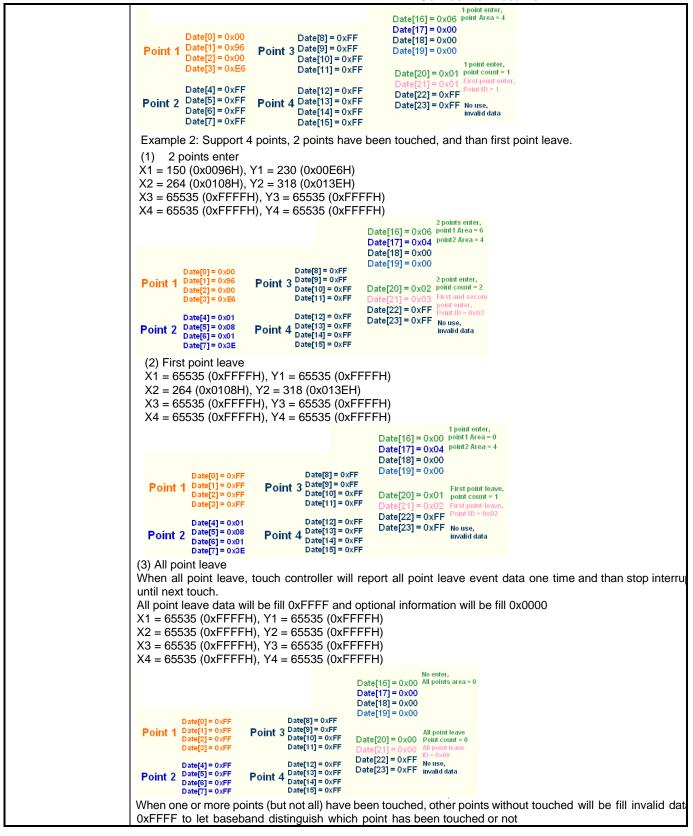


Read One Event (85h)

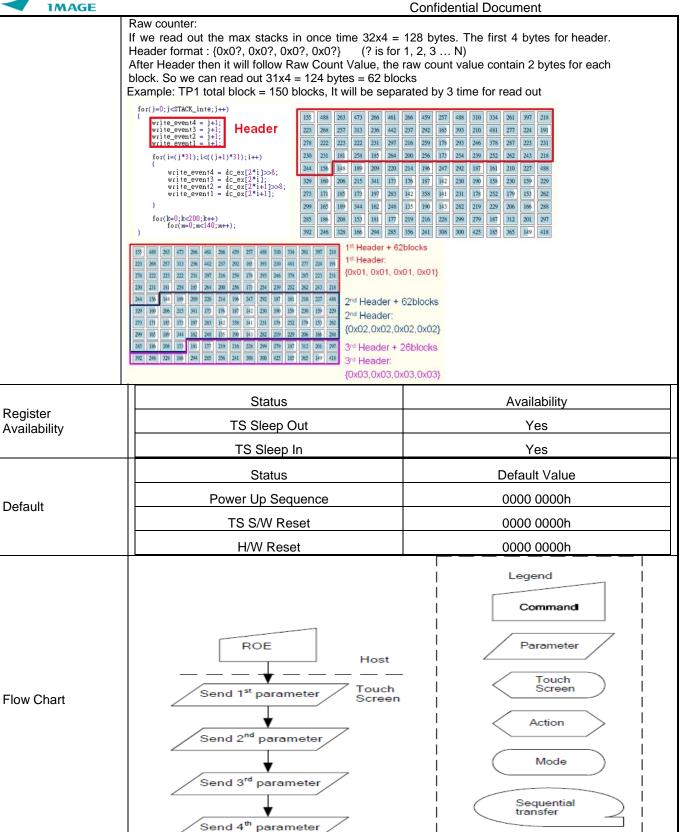
-	Read One Event (85h)										
	85H	ROE (R	ead One	Event)							
	036	DNC	D7	D6	D5	D4	D3	D2	D1	D0	HEX
Co	ommand	0	1	0	0	0	0	1	0	1	85
1	parameter	-	B31	B30	B29	B28	B27	B26	B25	B24	XX
2	parameter	-	B23	B22	B21	B20	B19	B18	B17	B16	XX
3	parameter	-	B15	B14	B13	B12	B11	B10	B9	B8	ХХ
4	parameter	-	B7	B6	B5	B4	B3	B2	B1	B0	XX
De	escription	information A returning co-ordinat Touch W Block is 2 Point ID:	on has bee ng value ca ates and rel idth: Repor I50 (96h). I Report the	n stored or an be "No E lated touch rt the touch	n the stock Event" if the i informationed block. I ee toucheo hed points	The ever e stock is e on: For examp I block, the	le: if RX=1	empty after 5, TX=10, 1	this comm		ues
		24 by		using	data by CMD 0x3	Po Po Po Po Y44 Y44 Y44 Y44 Y34 X44 Y36 X36 Y36 X37 X36 Y26 X26 X10	int 4 Aread int 3 Aread int 2 Aread int 1 Aread Low byte)d High byte)d Low byte)d High byte)d Low byte)d		ead dat CMD 0x	پ Don ب P	-
touched will be fill invalid data 0xFFFF to let baseband distinguish which point has been touched or not. Example 1: Support 4 points, one point has been touched. X1 = 150 (0x0096H), Y1 = 230 (0x00E6H) X2 = 65535 (0xFFFFH), Y2 = 65535 (0xFFFFH) X3 = 65535 (0xFFFFH), Y3 = 65535 (0xFFFFH) X4 = 65535 (0xFFFFH), Y4 = 65535 (0xFFFFH) Totally data length = 4 x 4 + 8 = 24 bytes											











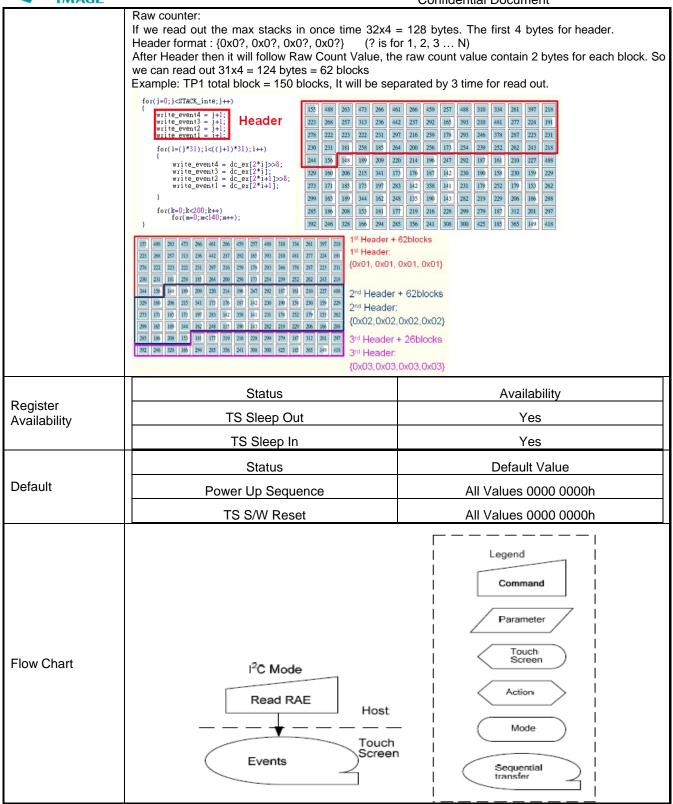


RAE (Read All Events) 86H DNC D5 D4 D3 D2 D1 D0 HEX D7 D6 Command 0 0 0 0 0 1 0 86 1 1 B31 B30 B29 B28 B27 B26 B25 B24 parameter 1 хх 2 B23 B22 B21 B20 B19 B18 B17 B16 parameter _ ΧХ 3 B15 B14 B12 B11 B10 parameter B13 B9 B8 -ΧХ B7 B6 B5 Β4 **B**3 B2 Β1 B0 4 parameter ΧХ 5 parameter E3 E2 E1 E0 F1 P2 P1 P0 ΧХ 6 B23 B22 B21 B20 B19 B18 B17 parameter B16 _ хх • • (n+1) Parameter B7 B6 B5 Β4 B3 B2 Β1 B0 хх This command returns one touch event what is the oldest co-ordinates or raw counter (dc) values information has been stored on the stock. A returning value can be "No Event" if the stock is empty. co-ordinates and related touch information: Touch Width: Report the touched block. For example: if RX=15, TX=10, the total Block is 150 (96h). If it has three touched block, the report value is 03h. Point ID: Report the ID of touched points. Points number: Report the touch number. Point 4 Area Read data by Point 3 Area Tou Ndth using CMD 0x8 Point 2 Area Don't Care Point 1 Area Don't Care Y4(Low byte) Point ID Y4(High byte) Point number X4(Low byte) X4(High byte) Y3(Low byte) 24 bytes Y3(High byte) X3(Low byte) Description X3(High byte) Y2(Low byte) Y2(High byte) X2(Low byte) X2(High byte) Y1(Low byte) Y1(High byte) X1(Low byte) Read data by using CMD 0x85, 0x86 X1(High byte) When one or more points (but not all) have been touched, other points without touched will be fill invalid data 0xFFFF to let baseband distinguish which point has been touched or not. Example 1: Support 4 points, one point has been touched. X1 = 150 (0x0096H), Y1 = 230 (0x00E6H) X2 = 65535 (0xFFFFH), Y2 = 65535 (0xFFFFH) X3 = 65535 (0xFFFH), Y3 = 65535 (0xFFFH) X4 = 65535 (0xFFFFH), Y4 = 65535 (0xFFFFH)



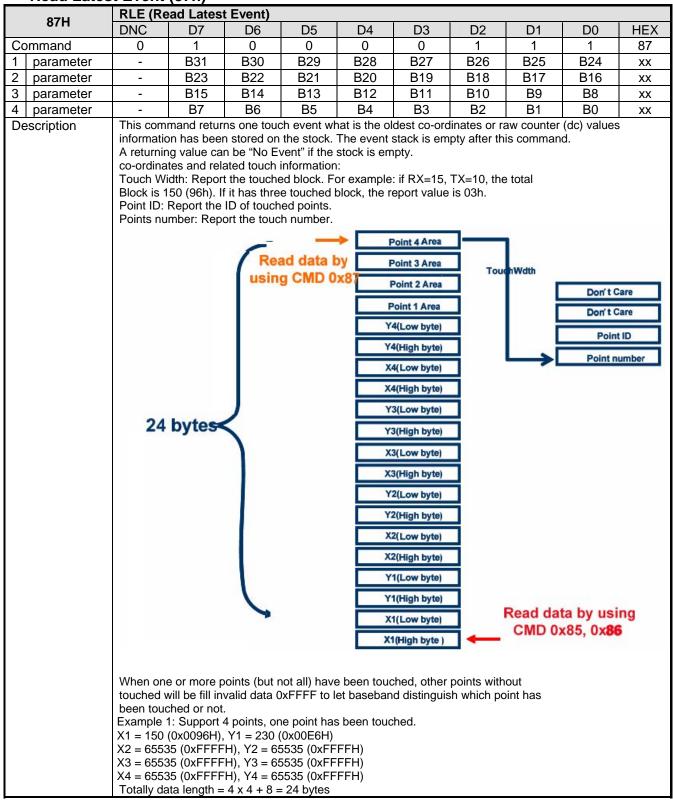
IMAGE	Confidential Document
	Totally data length = $4 \times 4 + 8 = 24$ bytes.
	Point 1 $Date[0] = 0x00$ $Date[1] = 0x96$ $Date[2] = 0x06$ Date[8] = 0xFF $Date[10] = 0xFF$ $Date[11] = 0xFF$ $Date[16] = 0x00$ $Date[17] = 0x00$ $Date[19] = 0x00$ $Date[19] = 0x00$ Point 2 $Date[4] = 0xFF$ $Date[5] = 0xFF$ $Date[5] = 0xFF$ $Date[10] = 0xFF$ $Date[11] = 0xFF$ $Date[12] = 0x01$ $Date[20] = 0x01$ $Date[21] = 0xFF$ $Date[21] = 0xFF$ $Date[22] = 0xFF$ $Date[22] = 0xFF$ $Date[23] = 0xFF$
	Example 2: Support 4 points, 2 points have been touched, and than first point leave. (2) 2 points enter X1 = 150 (0x0096H), Y1 = 230 (0x00E6H) X2 = 264 (0x0108H), Y2 = 318 (0x013EH) X3 = 65535 (0xFFFFH), Y3 = 65535 (0xFFFFH) X4 = 65535 (0xFFFFH), Y4 = 65535 (0xFFFFH)
	Point 1 Date[0] = 0x00 Date[1] = 0x06 Point 3 Date[8] = 0xFF Date[9] = 0xFF Date[1] = 0xFF 2 point enter, point enter, Date[1] = 0x00 Point 1 Date[1] = 0x96 Date[2] = 0x00 Point 3 Date[8] = 0xFF Date[9] = 0xFF 2 point enter, Date[1] = 0x00 Date[3] = 0xEF Point 3 Date[1] = 0xFF Date[1] = 0xFF Date[2] = 0x02 2 point enter, point enter, Date[2] = 0x03
	Point 2 Date[4] = 0x01 Date[5] = 0x08 Date[6] = 0x01 Date[7] = 0x3E Date[12] = 0xFF Point 10 Date[12] = 0xFF Date[13] = 0xFF Date[14] = 0xFF Date[12] = 0xFF Date[22] = 0xFF Date[23] = 0xFF No use, invalid data Date[12] = 0xFF Date[12] = 0xFF Date[13] = 0xFF Date[13] = 0xFF Date[13] = 0xFF Date[13] = 0xFF No use, invalid data
	(2) First point leave X1 = 65535 (0xFFFFH), Y1 = 65535 (0xFFFFH) X2 = 264 (0x0108H), Y2 = 318 (0x013EH) X3 = 65535 (0xFFFFH), Y3 = 65535 (0xFFFFH) X4 = 65535 (0xFFFFH), Y4 = 65535 (0xFFFFH)
	1 point enter, Date[16] = 0x00 Date[17] = 0x04 point2 Area = 4 Date[18] = 0x00 Date[19] = 0x00
	Point 1 Date[1] = 0xFF Date[2] = 0xFF Point 3 Date[3] = 0xFF Date[10] = 0xFF Date[2] = 0xFF Date[3] = 0xFF Date[11] = 0xFF Date[21] = 0xFF Date[21] = 0xFF Date[41] = 0x01 Date[12] = 0xFF Date[22] = 0xFF
	Point 2 Date[5] = 0x08 Date[6] = 0x01 Date[7] = 0x3E Point 4 Date[13] = 0xFF Date[14] = 0xFF Date[23] = 0xFF No use, invalid data (3) All point leave (3) All point leave (3) All point leave (3) All point leave
	When all point leave, touch controller will report all point leave event data one time and than stop interrupt until next touch. All point leave data will be fill 0xFFFF and optional information will be fill 0x0000. X1 = 65535 (0xFFFFH), Y1 = 65535 (0xFFFFH) X2 = 65535 (0xFFFFH), Y2 = 65535 (0xFFFFH) X3 = 65535 (0xFFFFH), Y3 = 65535 (0xFFFFH) X4 = 65535 (0xFFFFH), Y4 = 65535 (0xFFFFH)
	No enter, Date[16] = 0x00 All points area = 0 Date[17] = 0x00 Date[18] = 0x00 Date[19] = 0x00
	Point 1 Date[0] = 0xFF Date[1] = 0xFF Date[2] = 0xFF Date[8] = 0xFF Date[9] = 0xFF All point leave Date[20] = 0x0F Date[3] = 0xFF Date[10] = 0xFF Date[11] = 0xFF Date[20] = 0x00 All point leave Date[21] = 0x00
	Point 2 Date[5] = 0XFF Date[6] = 0XFF Date[7] = 0XFF Point 4 Date[13] = 0XFF Date[14] = 0XFF Date[15] = 0XFF
	When one or more points (but not all) have been touched, other points without touched will be fill invalidate 0xFFFF to let baseband distinguish which point has been touched or not.



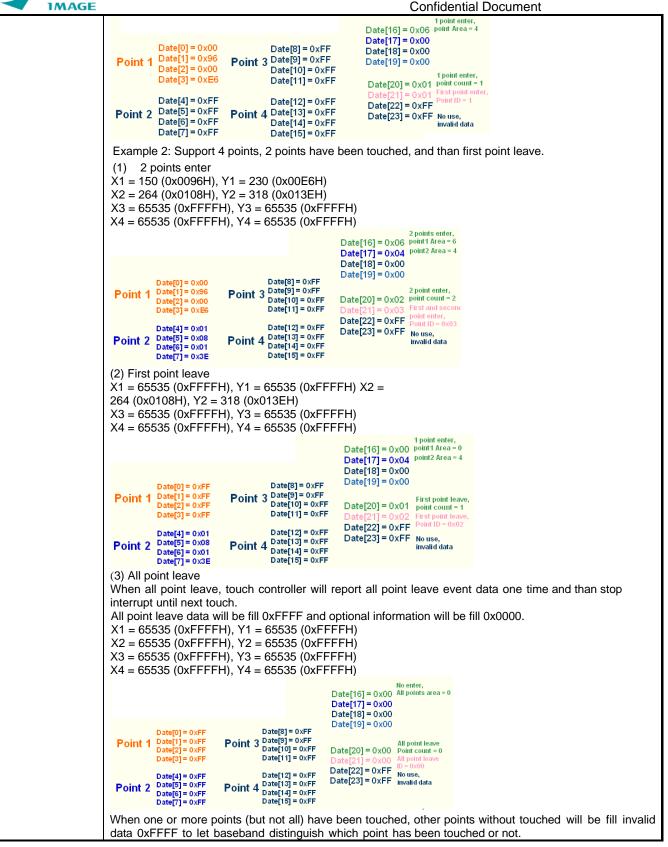




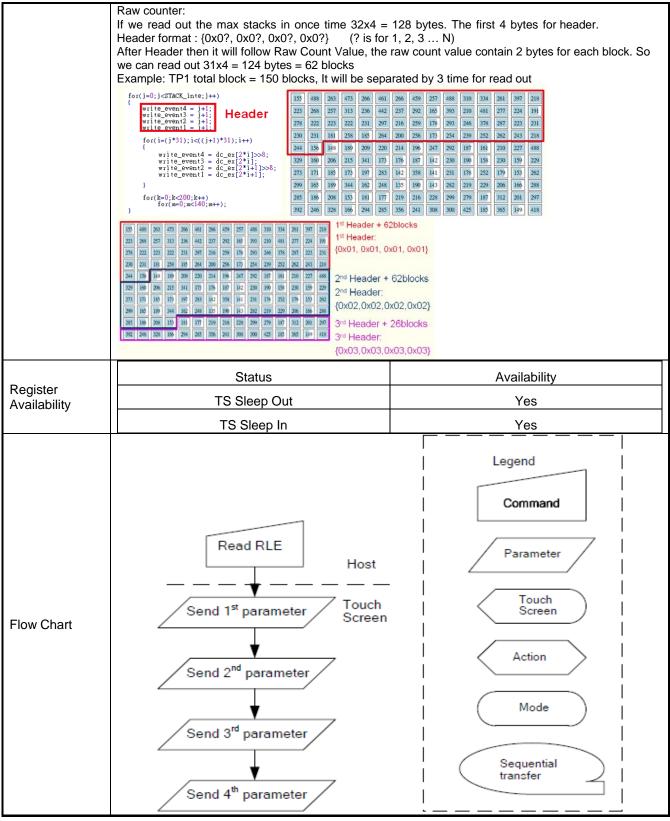
Read Latest Event (87h)













Clear Event Stack (88h)

	CLRES (Clear Event Stack)										
88H	DNC	D7	D6	D5	D4	D3	D2	D1	D0	HEX	
Command	0	1	0	0	0	1	0	0	0	88	
parameter	No para										
Description	This con	nmand cle	ears even	t stack wł	nen the or	nly return	event can	be "No E	vent".		
Restriction											
Register			Status				A	vailability			
Availability		Т	S Sleep C	Dut				Yes			
		-	rs Sleep	In				Yes			
			Status				De	fault Valu	е		
Default		Powe	er Up Seq	uence			En	npty Stacl	ĸ		
		T	S S/W Re	set			En	npty Stacl	ĸ		
			H/W Rese	et			En	npty Stacl	<u>k</u>		
Flow Chart			CLR				Par	mmand rameter Fouch Screen ction Mode			



TS Software Reset (9Eh) **TSSWRESET** (Touch Screen Software Reset) 9E H DNC D0 HEX D7 D6 D5 D4 D3 D2 D1 Command 1 9E 0 1 0 0 1 1 1 0 parameter No parameter Description When the Touch Screen Software Reset command is written, it causes a software reset. It resets the commands and parameters to their TS S/W Reset default values. (See default tables in each command description.) Note: The Memory contents are unaffected by this command It will be necessary to wait 5msec before sending new command following software reset. The touch screen loads all touch screen supplier's factory default values to the registers during this 5msec. Restriction If Software Reset is applied during TS Sleep Out mode, it will be necessary to wait 5msec before sending TS Sleep Out command. Touch Screen Software Reset Command cannot be sent during TS Sleep Out sequence. Status Availability Register **TS Sleep Out** Yes Availability TS Sleep In Yes **Default Value** Status Power Up Sequence N/A Default TS S/W Reset N/A N/A H/W Reset Legend Command Parameter Touch Screen Flow Chart TSSWRESET Action Set Commands to TS S/W Mode Default value Sequential transfer TS Sleep In Mode



	Defice in Coll		. ,									
	31 H	Device	ID			-	-		-	-		
	5111	DNC	D7	D6	D5	D4	D3	D2	D1	D0	HEX	
Сс	mmand	0	0	0	1	1	0	0	0	1	31	
1	parameter	1				8	5				00FF	
2	parameter	1				2	6				00FF	
3	parameter	1				0	1				00FF	
De	scription		nen the Device ID command is written, IC will echo the device ID to master. The index of vice ID command is 31h									
Re	gister Availability			Status					Availabil	lity		
	giotor / tranability		1	S Sleep	Out				Yes			
				TS Sleep	o In				Yes			
				Status	i			[Default Va	alue		
De	fault		Pow	er Up Se	quence				N/A			
			TS S/W Reset N/A									
		H/W Reset N/A										
Flo	ow Chart											

Version ID Command (32h)

22 11	Device ID											
32 H	DNC	D7	D6	D5	D4	D3	D2	D1	D0	HEX		
Command	0	0	0	1	1	0	0	1	0	31		
1 parameter	1		SF_Ver	sion[3:0]			F_Vers	ion[3:0]		00FF		
Description	F_Versi	on [3:0]: ⁻	The firmw	the ID coo vare versi ware ver	on of flas	h code.						
Register Availability	TS Sle	SF_Version [3:0]: The firmware version of self test code. Status Availability TS Sleep Out Yes TS Sleep In Yes										
	Status		00000			Defaul N/A	t Value					
Default	Power Up Sequence N/A TS S/W Reset N/A H/W Reset N/A											
Flow Chart												



Iten	n	Symbol	Condition	Min.	Тур.	Max.	Unit	Remark
Response	Rise	Tr	<i>θ=</i> 0°		5	10	ms	Note 4
time	Fall	Tf			15	20	ms	NOLE 4
Contras	t ratio	CR	At optimized viewing angle	200	300			Note 5
	Тор			40	50			
Viewing	Bottom		CR≥10	60	70		Der	Nete C
angle	Left			60	70		Deg.	Note 6
	Right			60	70	-		
Luminance			0.00	238	297		cd/m ²	Note 7
Uniformity		B-uni	<i>θ=</i> 0°	70	80		%	Note 8
White chromaticity		Х	<i>θ=</i> 0°	0.27	0.32	0.37		Note 7
		у		0.28	0.33	0.38		

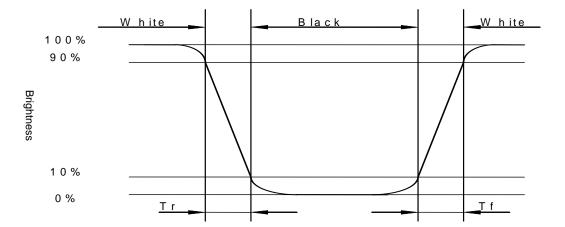
Note 1: Ambient temperature =25°C. LED current I_L = 15 mA.

Note 2: To be measured in the dark room.

Note 3: To be measured on the center area of panel with a viewing cone of 1° by Topcon luminance meter BM-7A, after 2 minutes operation.

Note 4: Definition of response time:

The output signals of photo-detector are measured when the input signals are changed from "white" to "black"(rising time) and from "black" to "white"(falling time), respectively. The response time is defined as the time interval between the 10% and 90% of amplitudes. Refer to figure as shown below.



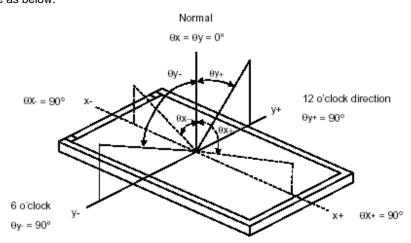
SCF0403526GGU38 REV: 3



Contrast ratio is calculated with the following formula.

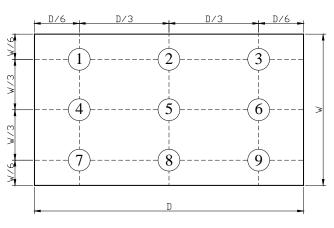
Photo-detector output when LCD is at "White" state Contrast ratio (CR)= Photo-detector output when LCD is at "Black" state

Note 6: Definition of viewing angle: Refer to figure as below.



Note 7: Measured at the center area of the panel when all the input terminals of LCD panel are electrically opened.

Note 8: Definition of Brightness Uniformity (B-uni):



Luminance Measuring Points

Minimum luminance of 9 points Maximum luminance of 9points B-uni =



11. QUALITY ASSURANCE 11.1 RA Test Condition

11.1.1 Temperature and Humidity(Ambient Temperature)

Temperature : $25 \pm 5^{\circ}$ C Humidity : $65 \pm 5^{\circ}$

11.1.2 Operation

Unless specified otherwise, test will be conducted under function state.

11.1.3 Container

Unless specified otherwise, vibration test will be conducted to the product itself without putting it in a container.

11.1.4 Test Frequency

In case of related to deterioration such as shock test. It will be conducted only once.

11.1.5 Test Method

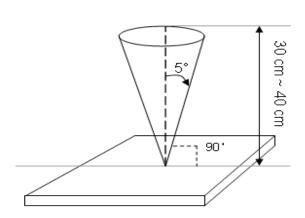
No.	Reliability Test Item & Level	Test Level	Remark
1	High temperature storage test	T=80°C ,240H	IEC68-2-2
2	Low temperature storage test	T= -30°C ,240H	IEC68-2-1
3	High temperature operation test	T=70°C,240H	IEC68-2-2
4	Low temperature operation test	T=-20℃,240H	IEC68-2-1
5	High temperature and high humidity operation test	T=60°C,90%RH,240H	IEC68-2-3
6	Thermal cycling storage test	-30℃25℃80℃,200Cycle 30min 5min 30min	IEC68-2-14
7	Vibration Test	Frequency:10 ~ 55 Hz Amplitude:1.5 mm Sweep Time:11min Test Period:6 Cycles for each Direction of X,Y,Z	IEC68-2-6
8	Shock test	100G,6ms,Direction:±X±Y±Z Cycle:3times	IEC68-2-27
9	Drop test	Height :60cm 1 conner,3edges,6surfaces	IEC68-2-32
10	Electrostatic Discharge Test	Location: LCM/TP surface Condition:150pf 330Ω Contact +/- 4kV Air +/-8kV Criteria: Class C	IEC-61000-4-2

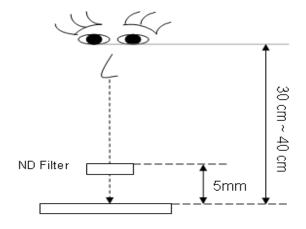


11.2 Inspection Judgment standard

11.2.1 Inspection conditions

- 11.2.1.1 Inspection Distance : 35 ± 5 cm
- 11.2.1.2 View Angle :
 - (1) Inspection that light pervious to the product: $\pm 5^{\circ}$
 - (2) Inspection that light reflects on the product: ±45°





11.2.2 Environment conditions :

Ambient Temperature :	25±5 ℃
Ambient Humidity :	30~75%RH
Ambient Illumination	600~800 lux

11.2.3 Inspection Parameters

Appearance inspection standard (D: diameter, L: length; W: width, Z: height, T: glass thickness, n: number)

Inspection item	Inspection standard	Description
No image	Prohibited	
Image abnormal	Prohibited	
Bright line	Prohibited	
Mura	It is acceptable that the defect can not be seen with 2% ND filter.	



Dot			Confidential	Document	
Dot	ltere	Acceptable	Total		
	ltem	Visible area	Total		
	Bright dot	2	5		
	Dark dot	4	5	One Dot	
	Bright adjacent dots	1	1	Two adjacent dot	
	Dark adjacent dots	2	2		
	Adjacent dots with a bright dot and a dark dot	1	1		
			<u> </u>]		
Foreign material	SPEC (unit: mm)	Acceptable		
in dot shape	D≦0.3		Ignored		
	0.3 <d≦0.5, distanc<="" td=""><td>ce>5</td><td>n≦5</td><td></td></d≦0.5,>	ce>5	n≦5		
	D>0.5		0	L = (L + W) / 2	
Inspection item	Inspection standard			Description	
Foreign material	SPEC (unit: mm)	Acceptable	1	
in line shape	W ${\leq}$ 0.05 and L ${\leq}$	7	Ignored		
	0.05 <w<math>\leq0.1, L\leq7, dis</w<math>	tance >5	n≦5		
	W>0.1 or L>7		0		
				E. Long W. Widdi	
Contamination	It is acceptable if the	e dirt can be wip	oed.		
Inspection item	Inspection standard		Description		
Scratch	SPEC (unit: mm)	Acceptable		
	W \leq 0.05 and L \leq		Ignored	. w	
	0.05 <w≦0.08, dis<="" l≦7,="" td=""><td>stance >5</td><td>n≦5</td><td>\sim</td></w≦0.08,>	stance >5	n≦5	\sim	
	0.08 <w≦0.1, dis<="" l≦7,="" td=""><td>tance >5</td><td>n≦3</td><td></td></w≦0.1,>	tance >5	n≦3		
	W>0.1 or L>7		0		
Bubble			Acceptable		
1 1	D≦0.2		Ignored	0	
	Non visible area		Ignored	0	
	Non visible area 0.2 <d≦0.3, distanc<br="">D>0.3</d≦0.3,>		lgnored n≦5 0	0 L D= (L + W) / 2	



Insufficient glue				
	SPEC (unit: mm)	Acceptable		
	Non visible area	Ignored		
	Visible area	0		
Cover & Sensor Crack	Prohibited		Y	
Sensor angle	SPEC (unit: mm)	Acceptable		
missing & edge break	Damage circuit or effect function	0		
Cover/Sensor	SPEC (unit: mm)	Acceptable	Y T	
angle missing	X≦3.0, Y≦3.0, Z≦T	Ignored		
	X>3.0, Y>3.0, Z>T	0		
Cover/Sensor	SPEC (unit: mm)	Acceptable		
edge break	X≦3.0, Y≦3.0, Z≦T	Ignored	T Y Z	
	X>3.0, Y>3.0, Z>T	0		
Inspection item	SPEC		Description	
Ink	SPEC (unit: mm)	Acceptable		
	word unclear, inverted, mistake, break line	0		
Bubble under protection film	SPEC (unit: mm)	Acceptable		
	NA			
Function	Prohibited			

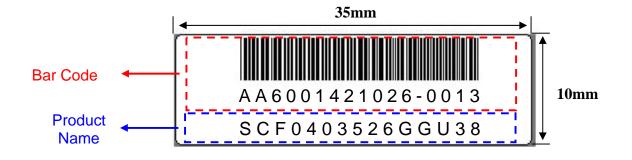
11.3 Sampling Condition Unless otherwise agree in written, the sampling inspection shall be applied to the incoming inspection of customer.

Lot size: Quantity of shipment lot per model. Sampling type: normal inspection, single sampling Sampling table: MIL-STD-105E Inspection level: Level II

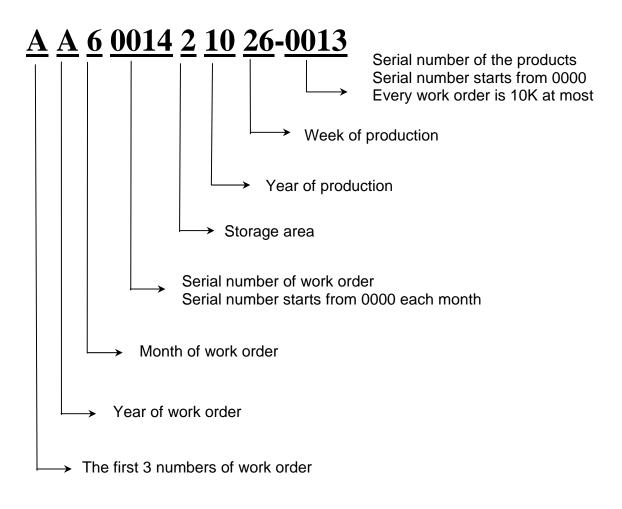
	Definition				
Class of defects	Major		It is a defect that is likely to result in failure or to reduce materially the usability of the product for the intended function.		
	Minor	AQL 1.5	It is a defect that will not result in functioning problem with deviation classified.		



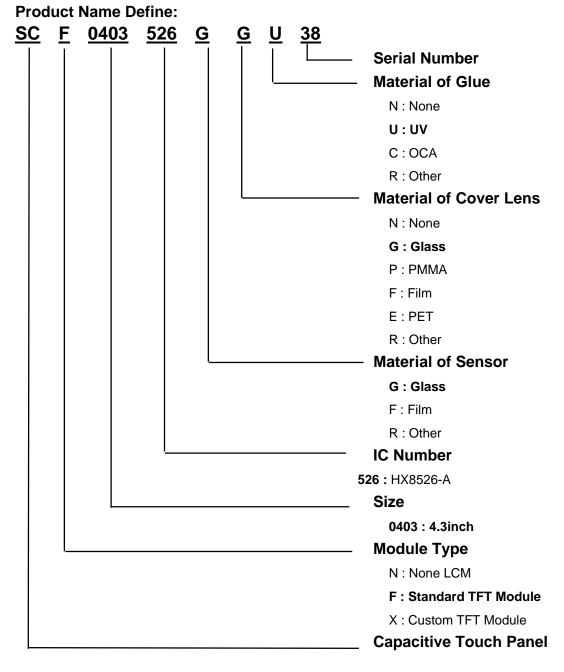
Product Label style:



BarCode Define:









13. PRECAUTION FOR USING LCM

- 1. ASSEMBLY PRECAUTIONS
 - (1) You must mount a module using holes arranged in four corners or four sides.
 - (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
 - (3) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment.
 - (4) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
 - (5) Do not open the case because inside circuits do not have sufficient strength.
 - (6) Please do not take a LCD module to pieces and reconstruct it. Resolving and reconstructing modules may cause them not to work well.
 - (7) Please do not touch metal frames with bare hands and soiled gloves. A color change of the metal frames can happen during a long preservation of soiled LCD modules.
 - (8) Please pay attention to handling lead wire of backlight so that it is not tugged in connecting with inverter.
- 2. OPERATING PRECAUTIONS
 - (1) Please be sure to turn off the power supply before connecting and disconnecting signal input cable.
 - (2) Please do not change variable resistance settings in LCD module. They are adjusted to the most suitable value. If they are changed, it might happen LCD does not satisfy the characteristics specification
 - (3) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
 - (4) When fixed patterns are displayed for a long time, remnant image is likely to occur.
 - (5) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimize the interference.
 - (6) Please consider that LCD backlight takes longer time to become stable of radiation characteristics in low temperature than in room temperature.
- 3. ELECTROSTATIC DISCHARGE CONTROL
 - (1) The operator should be grounded whenever he/she comes into contact with the module. Never touch any of the conductive parts such the copper leads on the PCB and the interface terminals with any

Confidential Document

parts of the human body.

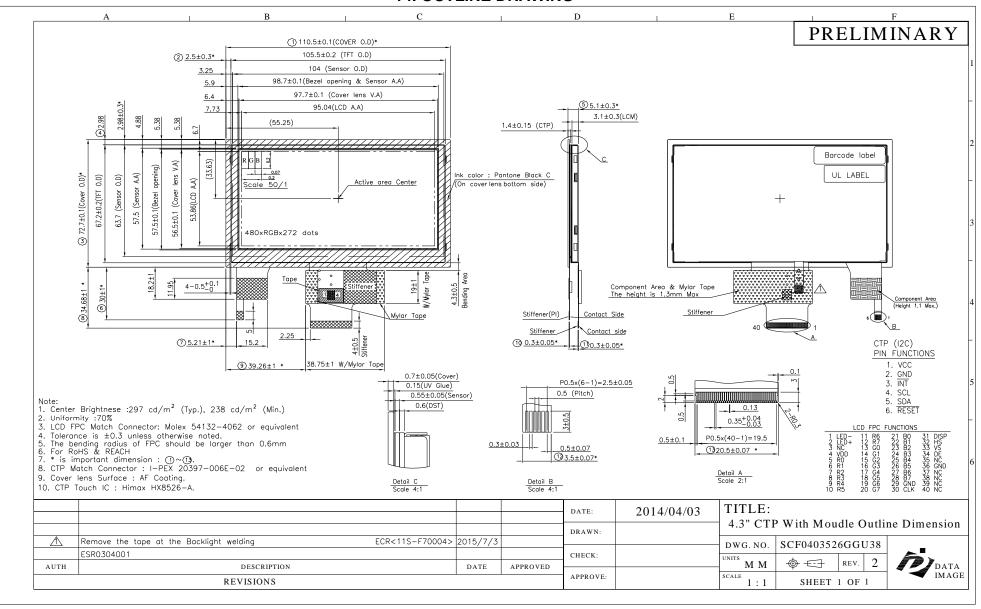
- (1) The modules should be kept in antistatic bags or other containers resistant to static for storage.
- (2) Only properly grounded soldering irons should be used.
- (3) If an electric screwdriver is used, it should be well grounded and shielded from commutator sparks.
- (4) The normal static prevention measures should be observed for work clothes and working benches; for the latter conductive (rubber) mat is recommended
- (5) Since dry air is inductive to statics, a relative humidity of 50-60% is recommended.
- 4. STORAGE PRECAUTIONS
 - (1) When you store LCDs for a long time, it is recommended to keep the temperature between 0° C-40°C without the exposure of sunlight and to keep the humidity less than 90%RH.
 - (2) Please do not leave the LCDs in the environment of high humidity and high temperature such as 60°C 90%RH
 - (3) Please do not leave the LCDs in the environment of low temperature; below -20°C.
- 5. OTHERS
 - A strong incident light into LCD panel might cause display characteristics' changing inferior because of polarizer film, color filter, and other materials becoming inferior. Please do not expose LCD module direct sunlight Land strong UV rays
 - (2) Please pay attention to a panel side of LCD module not to contact with other materials in preserving it alone.
 - (3) For the packaging box, please pay attention to the followings:
 - (4) Please do not pile them up more than 5 boxes. (They are not designed so.) And please do not turn over.
 - (5) Please handle packaging box with care not to give them sudden shock and vibrations. And also please do not throw them up.
 - (6) Packing box and inner case for LCDs are made of cardboard. So please pay attention not to get them wet. (Such like keeping them in high humidity or wet place can occur getting them wet.)

6. LIMITED WARRANTY

Unless otherwise agreed between DATA IMAGE and customer, DATA IMAGE will replace or repair any of its LCD and LCM which is found to be defective electrically and visually when inspected in accordance with DATA IMAGE acceptance standards, for a period on one year from date of shipment. Confirmation of such date shall be based on freight documents. The warranty liability of DATA IMAGE is limited to repair and/or replacement on the terms set forth above. DATA IMAGE will not responsible for any subsequent or consequential events.



Confidential Document 14. OUTLINE DRAWING





Confidential Document **15. PACKAGE INFORMATION**

TBD